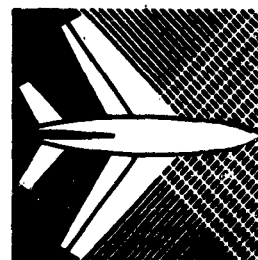
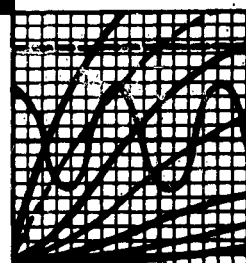
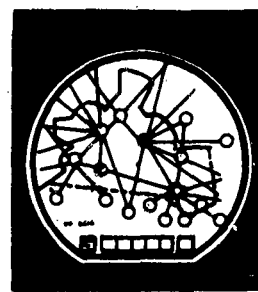


63-3-3



FINAL REPORT
Project No. 104-602T
Sub-Task 3

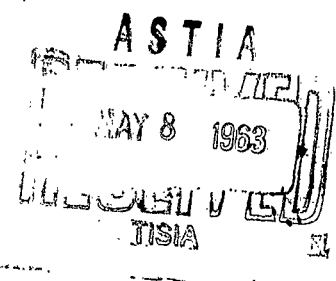
CATALOGED BY ASTIA
AS AD NO. 403484

COMPUTER PROGRAMMING OF PHASE I MASTER PROGRAM

403 484

(VOLUME III)

JUNE 1962



FEDERAL AVIATION AGENCY
Systems Research & Development Service
EXPERIMENTATION DIVISION
Atlantic City, New Jersey

FINAL REPORT
COMPUTER PROGRAMMING
OF PHASE I MASTER PROGRAM
(VOLUME III)

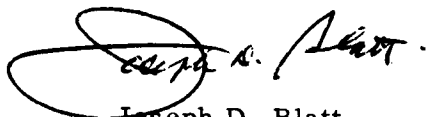
PROJECT NO. 104-602T
Sub-Task 3
Work Detail C

Prepared by:

Don W. Vogel
Kenneth W. House
William Brown
Raymond J. Hilton
Robert B. Stein

June 1962

This report has been approved for general distribution.



Joseph D. Blatt
Director, Systems Research
and Development Service
Federal Aviation Agency

Experimentation Division
National Aviation Facilities Experimental Center
Atlantic City, New Jersey

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FINAL REPORT
COMPUTER PROGRAMMING
OF PHASE I MASTER PROGRAM
(VOLUME III)

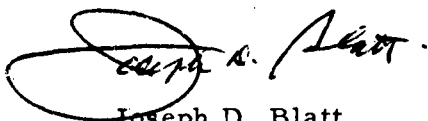
PROJECT NO. 104-602T
Sub-Task 3
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FINAL REPORT
COMPUTER PROGRAMMING
OF PHASE I MASTER PROGRAM

VOLUME III

APPENDIX X
THROUGH
APPENDIX XXI

APPENDIX X

TYPEWRITER PRINTOUT ROUTINE

3										4										5										6										7																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																	
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BUT THEY ARE ALWAYS OVERWRITTEN WITH DATA AND MAY
CONTAIN ANYTHING DESIRED.

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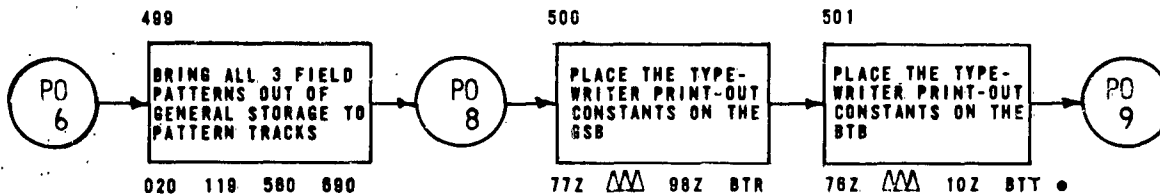
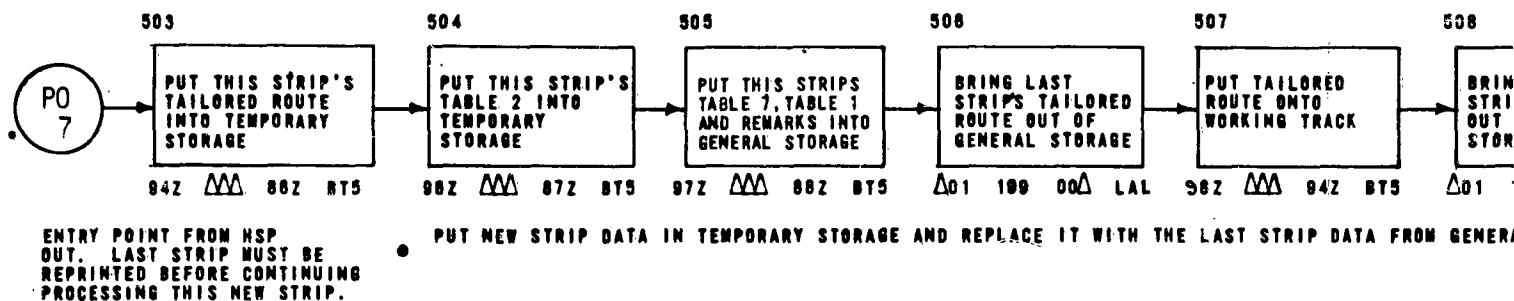
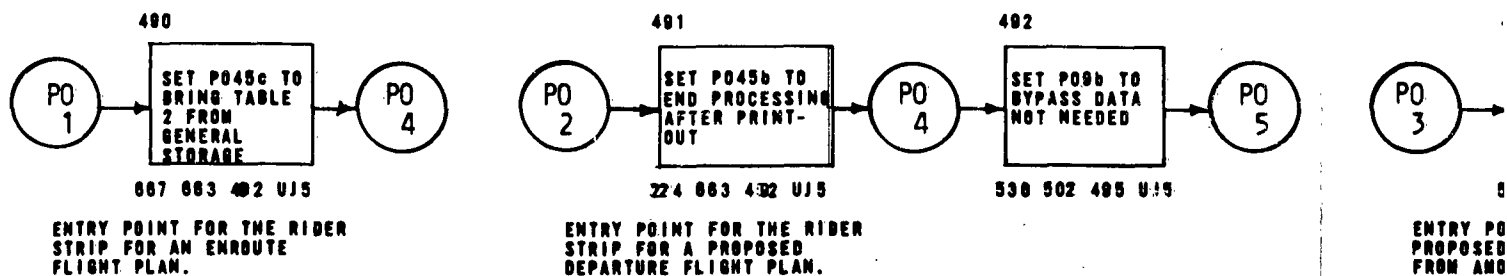
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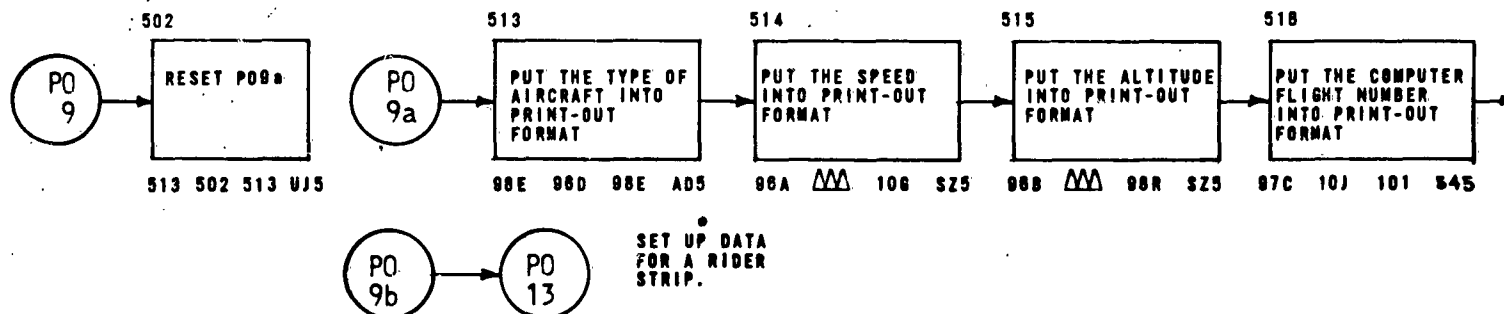
TO THE FLOW CHARTS TO DETERMINE
DATA SHOWN IS ACTUALLY ADDED
ICULAR STRIP.

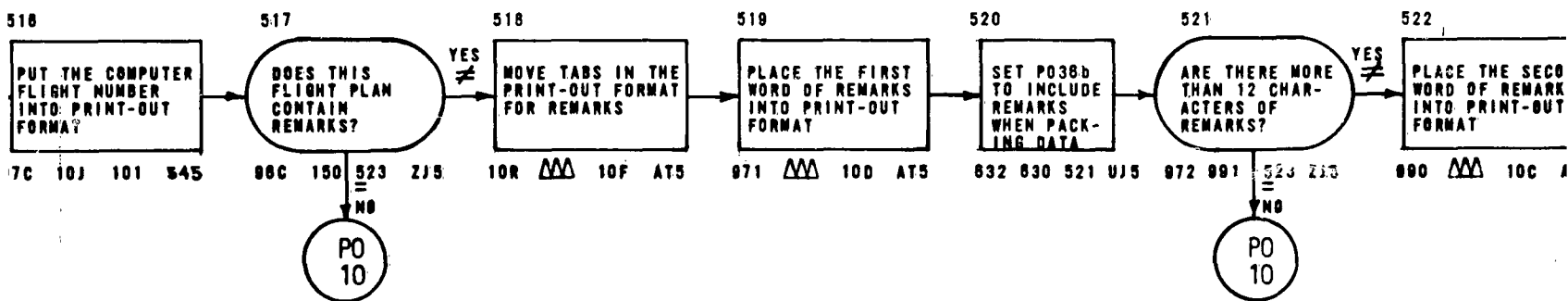
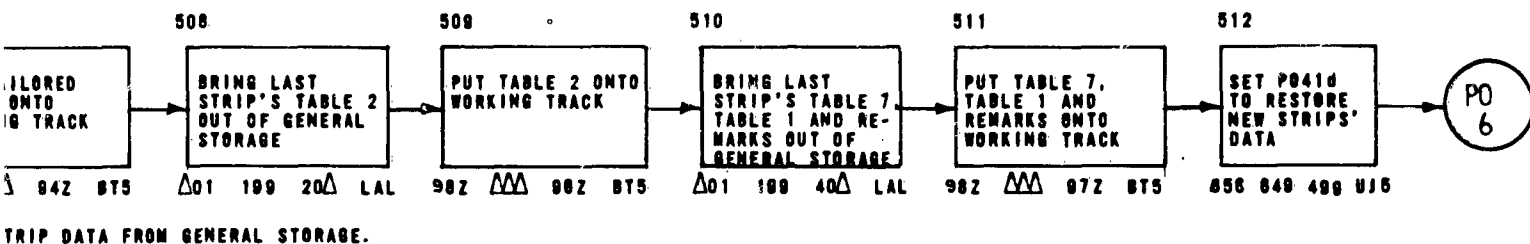
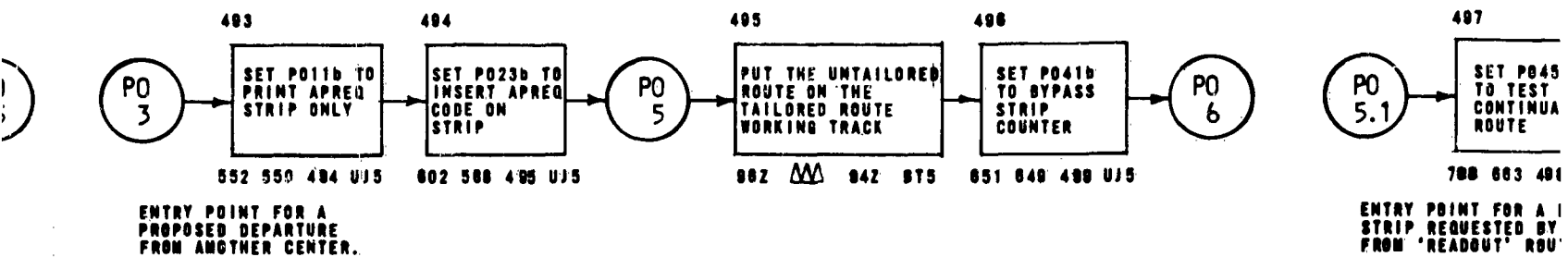
AT CONSTANTS PRESENTLY CONTAIN i's,
WRITTEN WITH DATA AND MAY



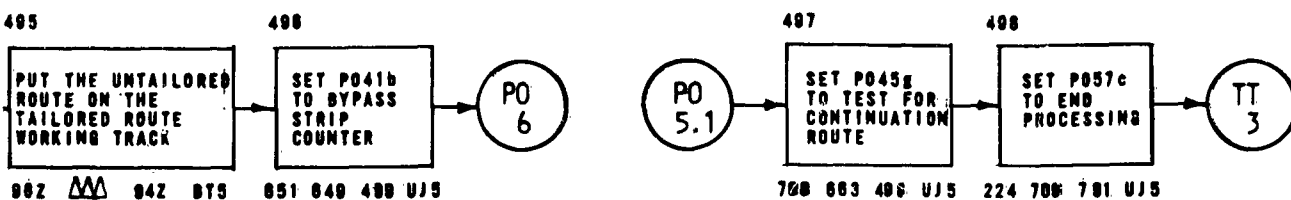


R= PICK UP PROGRAM *SELECT 2 FOR SELECTOR T-11 ON PLUGBOARD. USED BY PLUGBOARD ROUTINE TO SPACE THE AIRCRAFT IDENT.

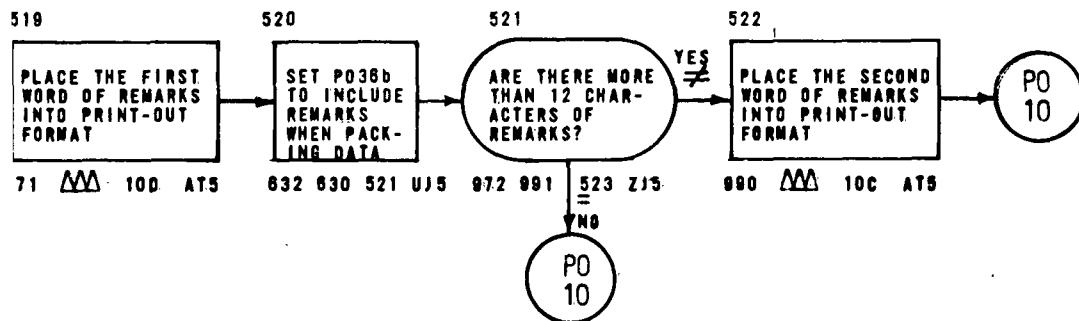
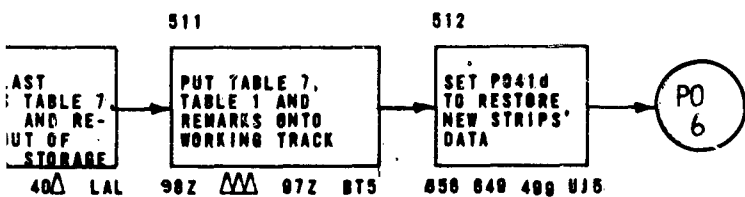




2



ENTRY POINT FOR A DUPLICATE STRIP REQUESTED BY A CONTROLLER FROM 'READOUT' ROUTINE



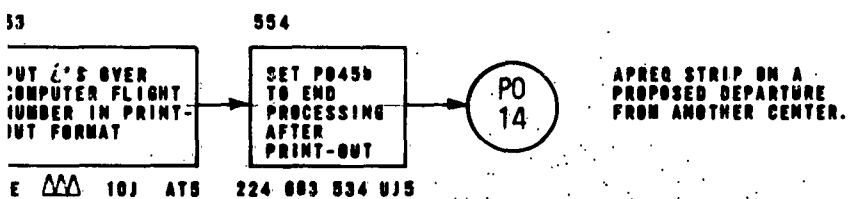
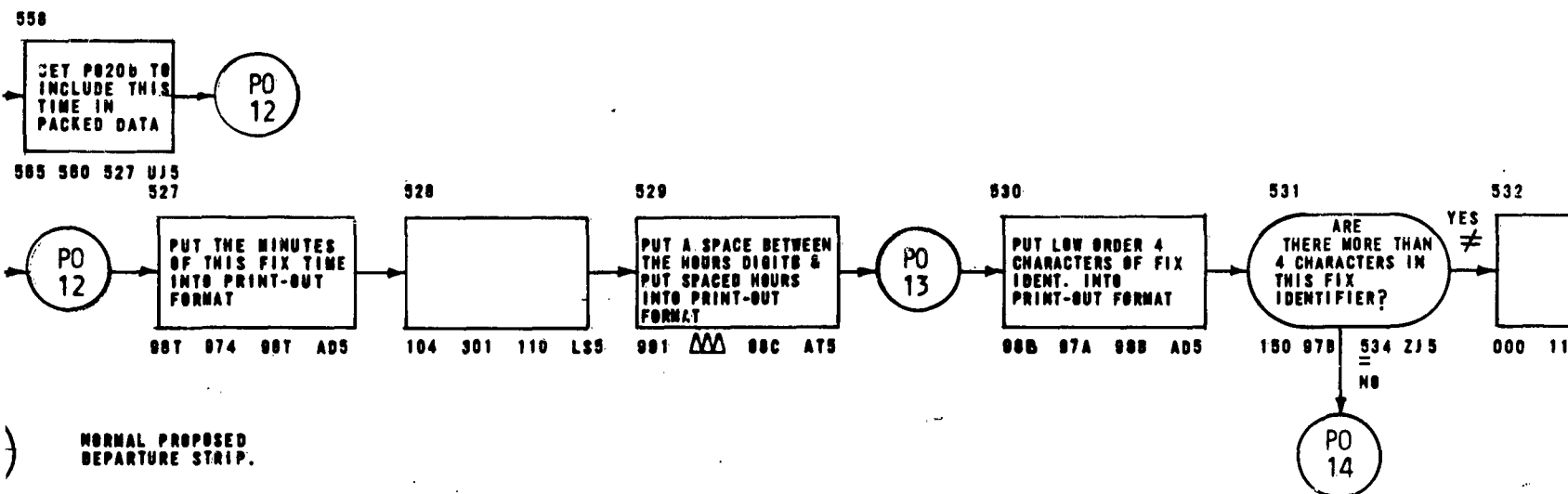
3



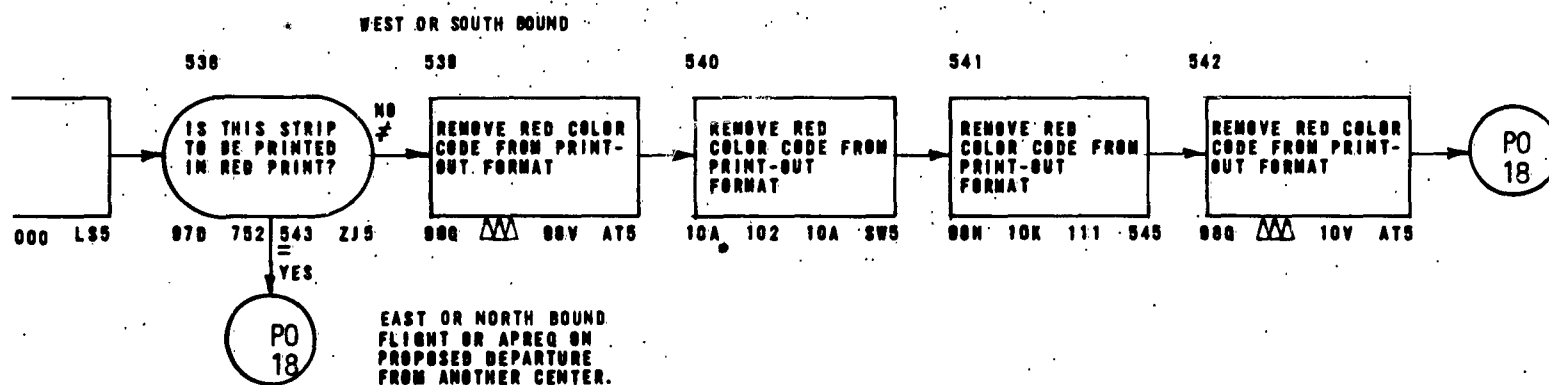
**EAST
FLIO
PROP
FROM**

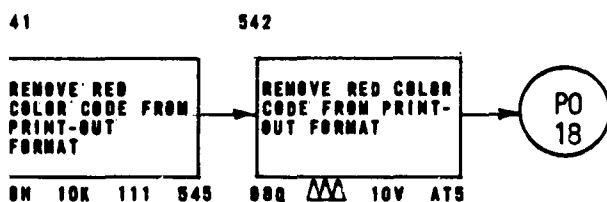
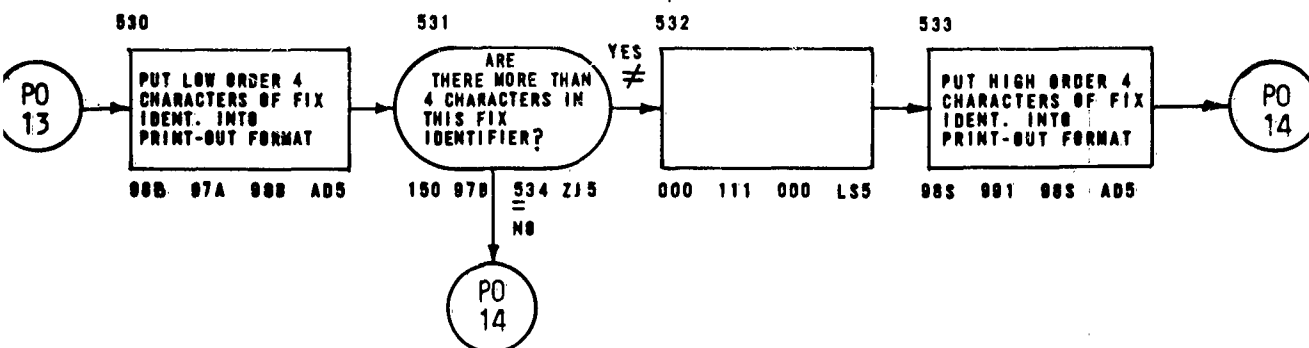
**Q= DROP OUT ALL PROGRAM
SELECTS ON THE PLUGBOARD.**

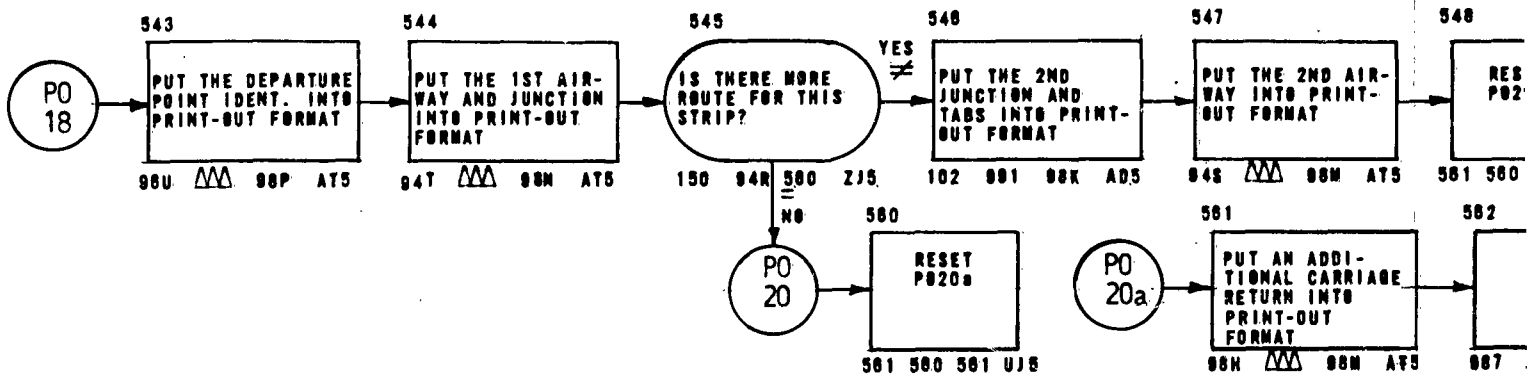
1



ON A SED

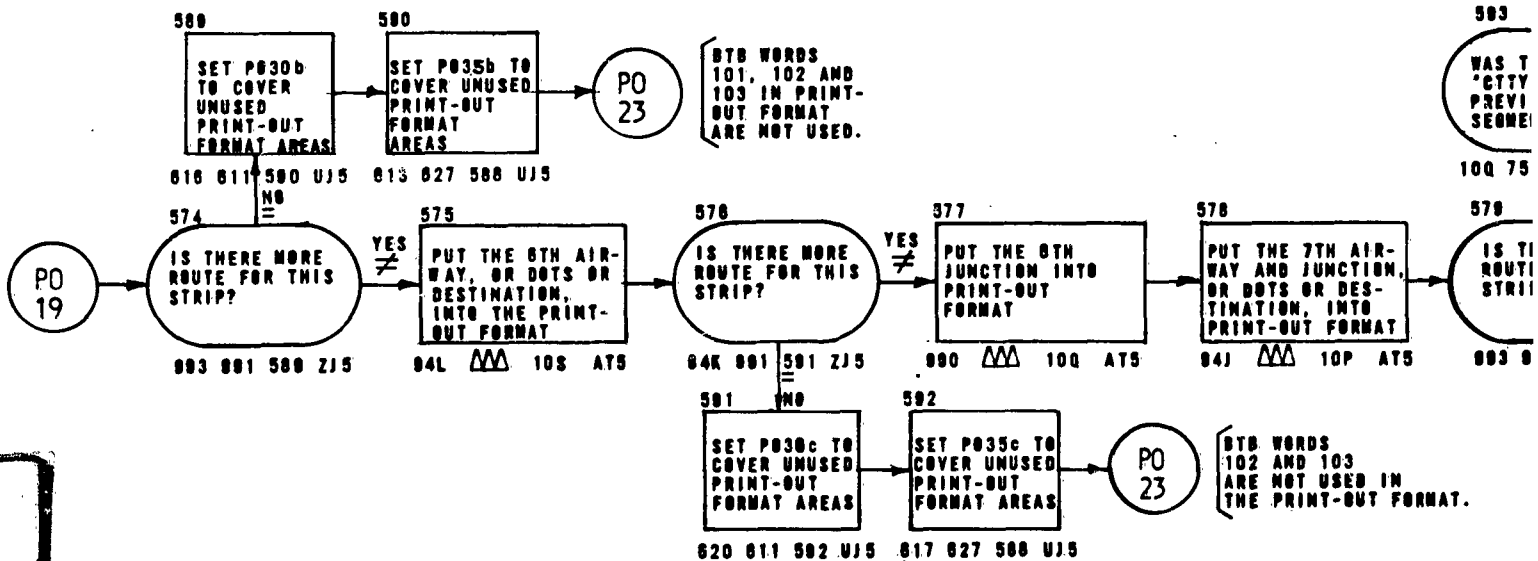
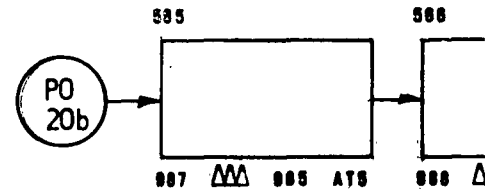






MOVE PRINT-OUT DATA ON 95D

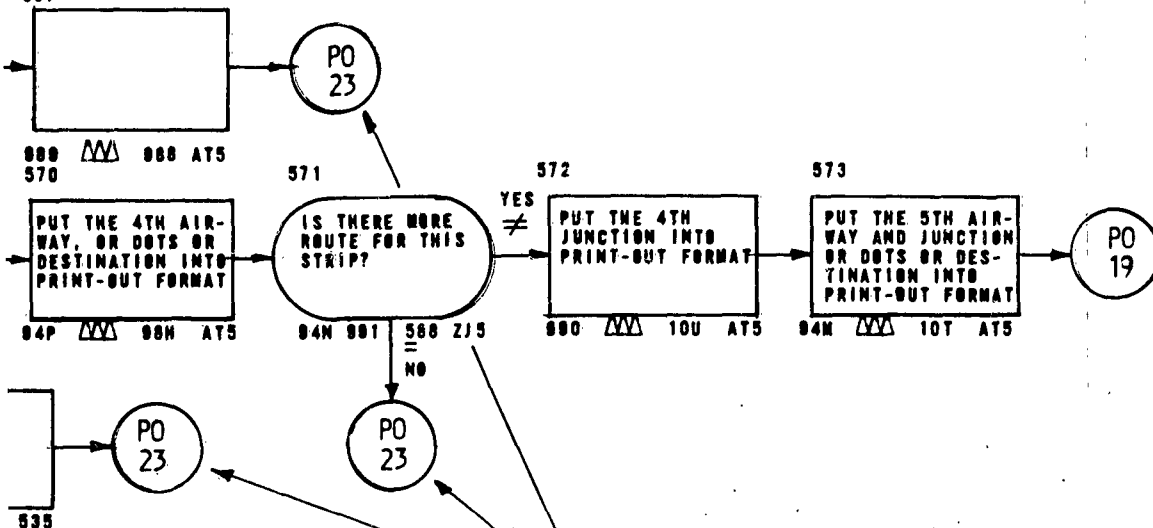
PREVIOUS FIX TIME IN 95D W0
MOVE PRINT-OUT DATA ON 95D



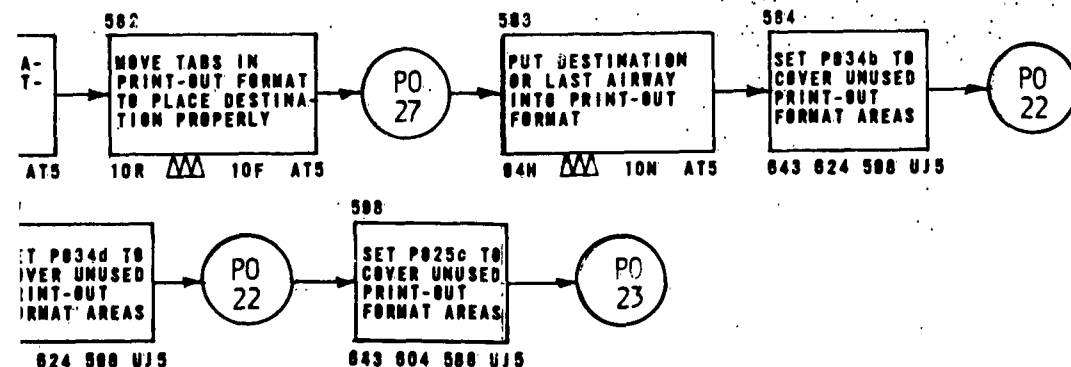
1



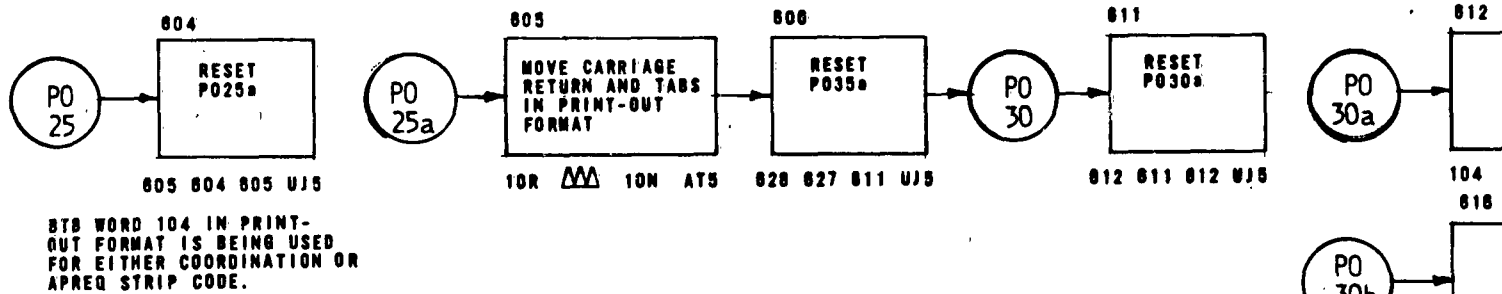
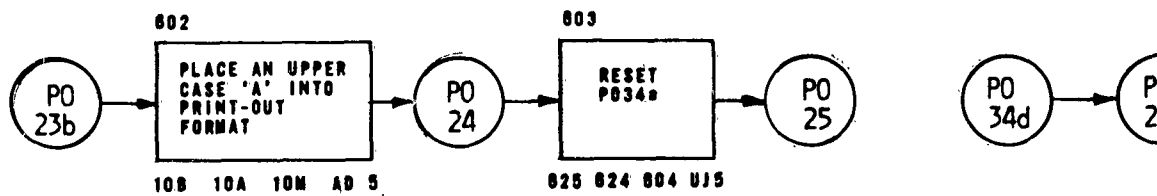
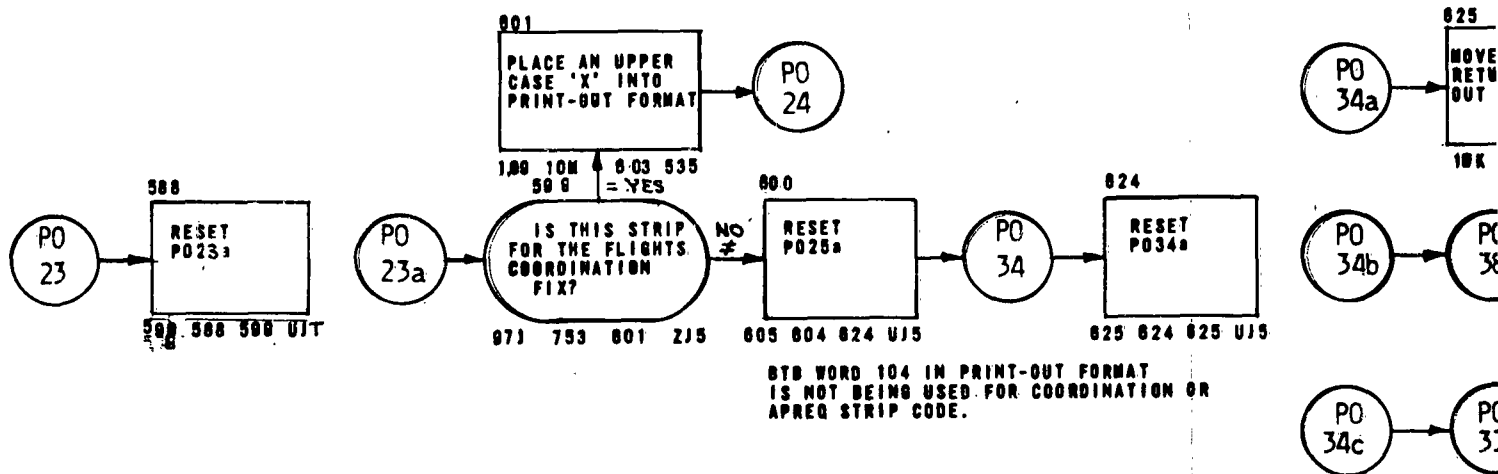
UNUSED WORD 8.
587



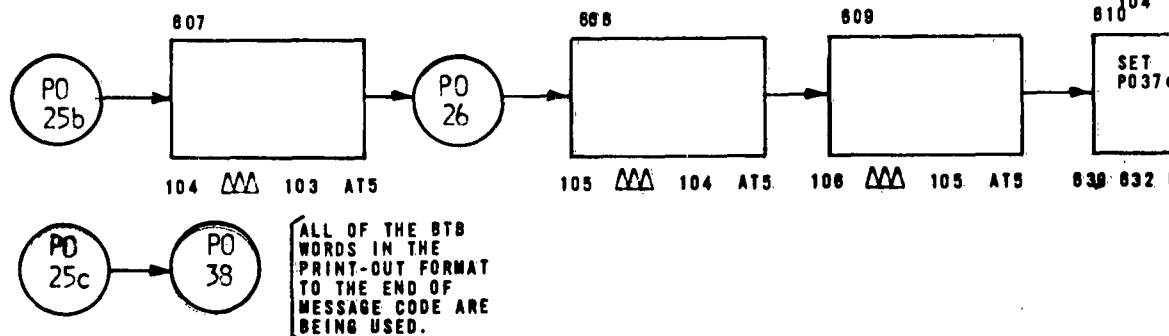
PO 23 { BTB WORD 103 IS NOT USED IN PRINT-OUT FORMAT

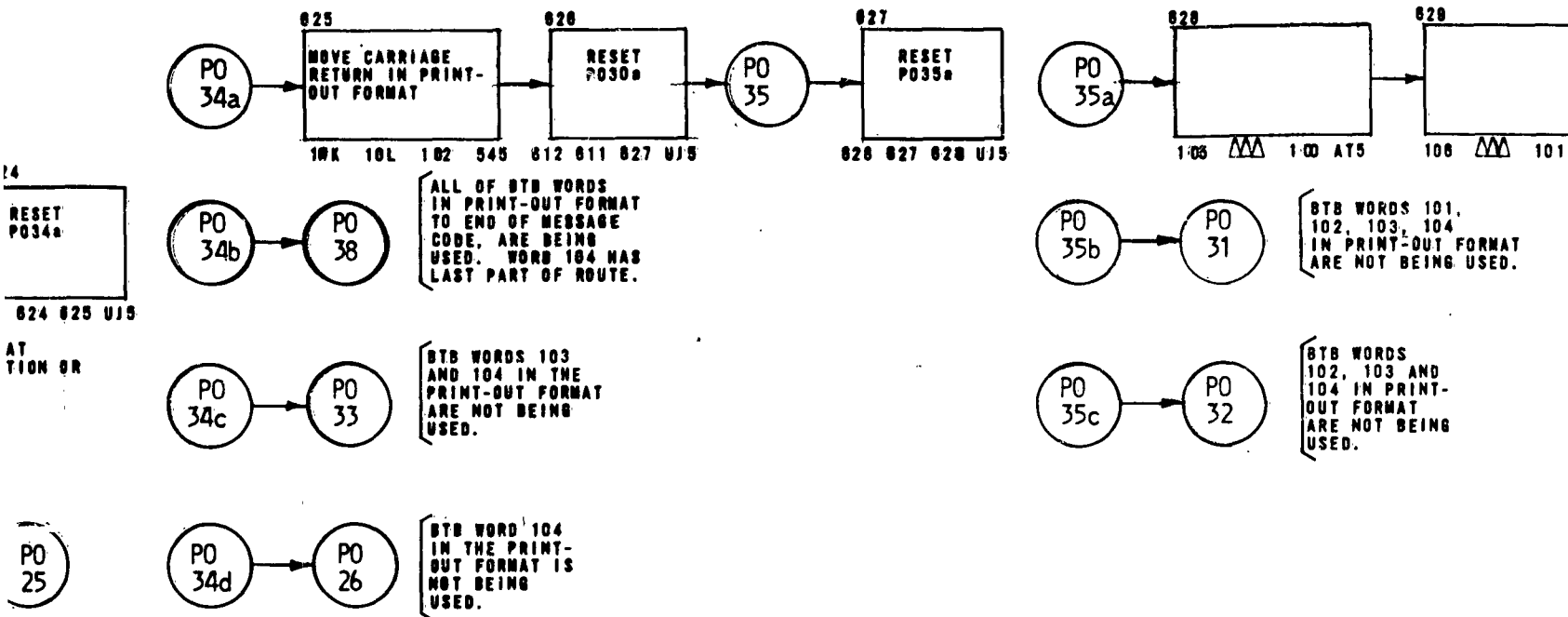


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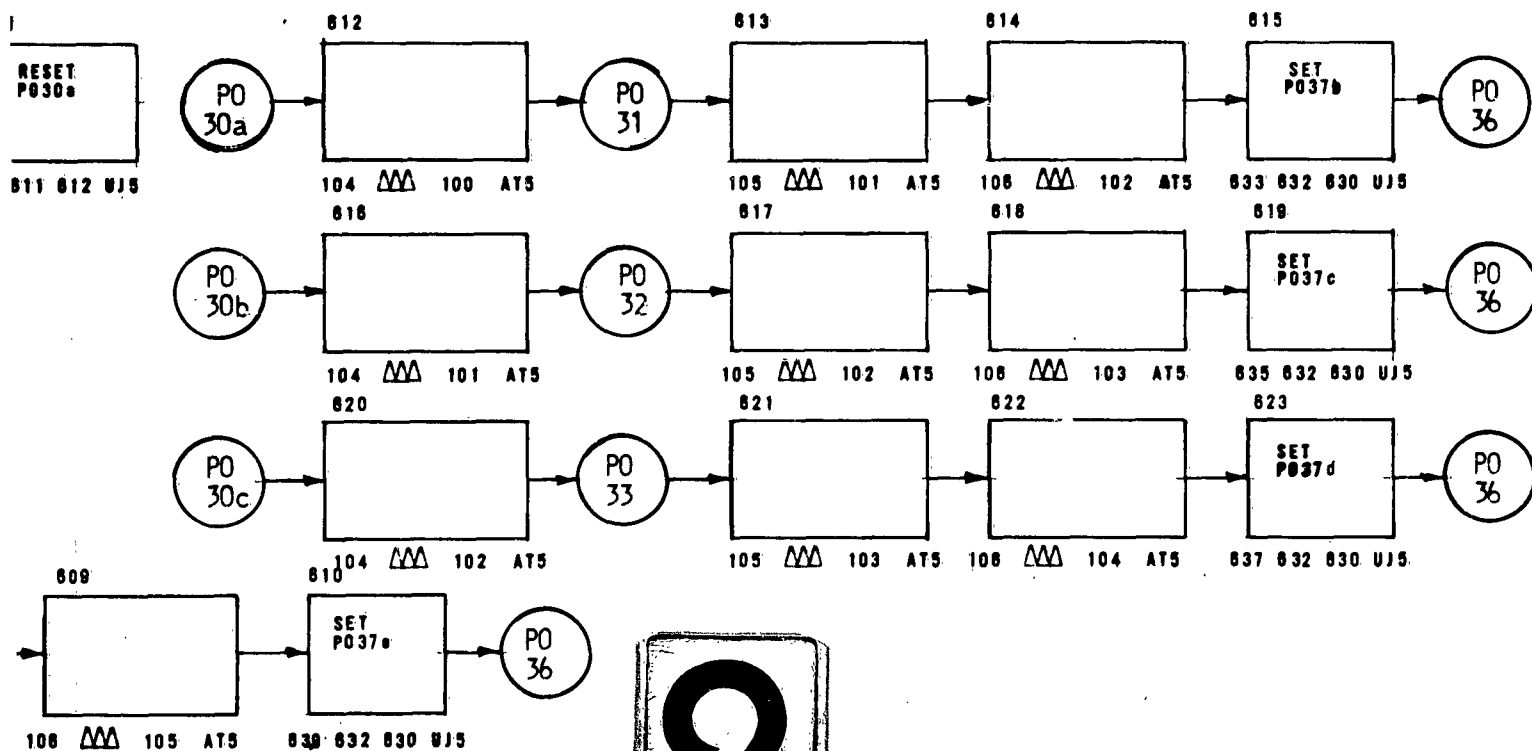


BTB WORD 103 IN THE PRINT-OUT FORMAT IS NOT BEING USED.

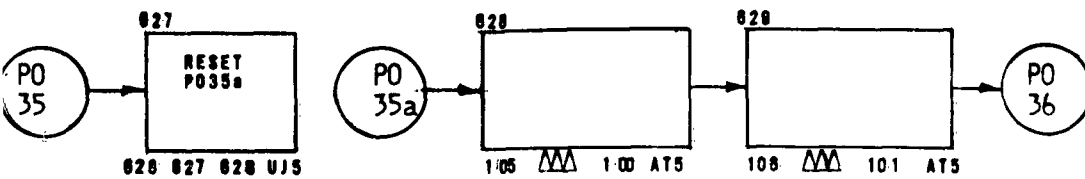




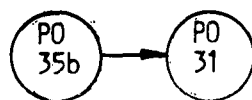
MOVE USED WORDS IN BTB PRINT-OUT FORMAT TO COVER UNUSED AREAS.



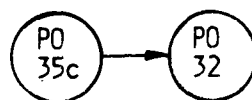
2



BTB WORDS 100, 101, 102, 103, AND 104 IN PRINT-OUT FORMAT ARE NOT BEING USED.

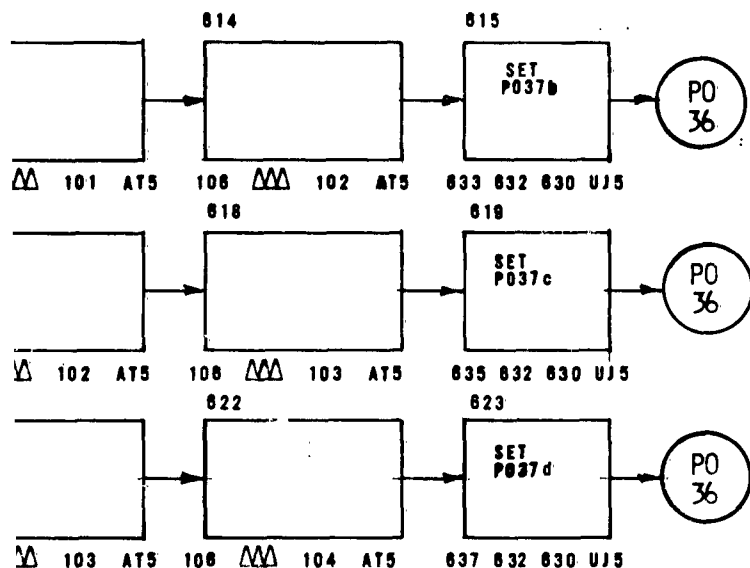


BTB WORDS 101, 102, 103, 104 IN PRINT-OUT FORMAT ARE NOT BEING USED.

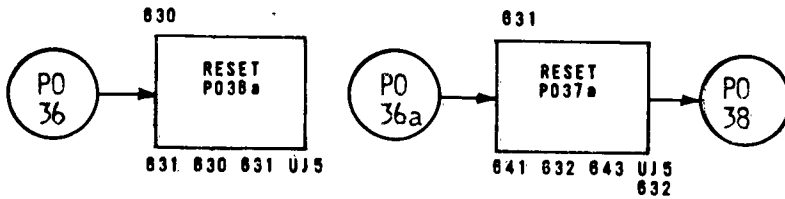


BTB WORDS 102, 103 AND 104 IN PRINT-OUT FORMAT ARE NOT BEING USED.

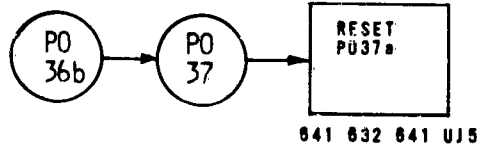
MOVE USED WORDS IN BTB PRINT-OUT FORMAT TO COVER UNUSED AREAS.



3

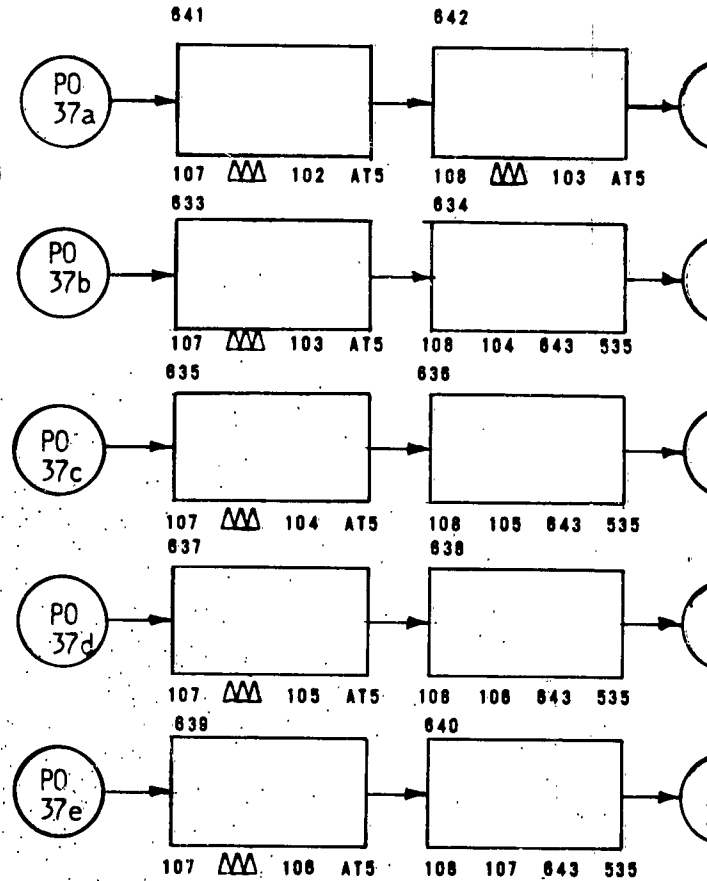


NO REMARKS
IN PRINT-
OUT FORMAT.

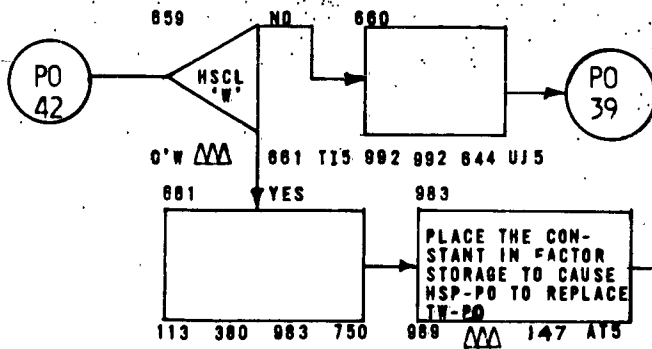


REMARKS INCLUDED IN THE
PRINT-OUT FORMAT

INCLUDE USED REMARKS
AREA OF PRINT-OUT FORMAT,
IN THE MOVEMENT OF USED
BTB WORDS TO COVER UNUSED
AREAS.



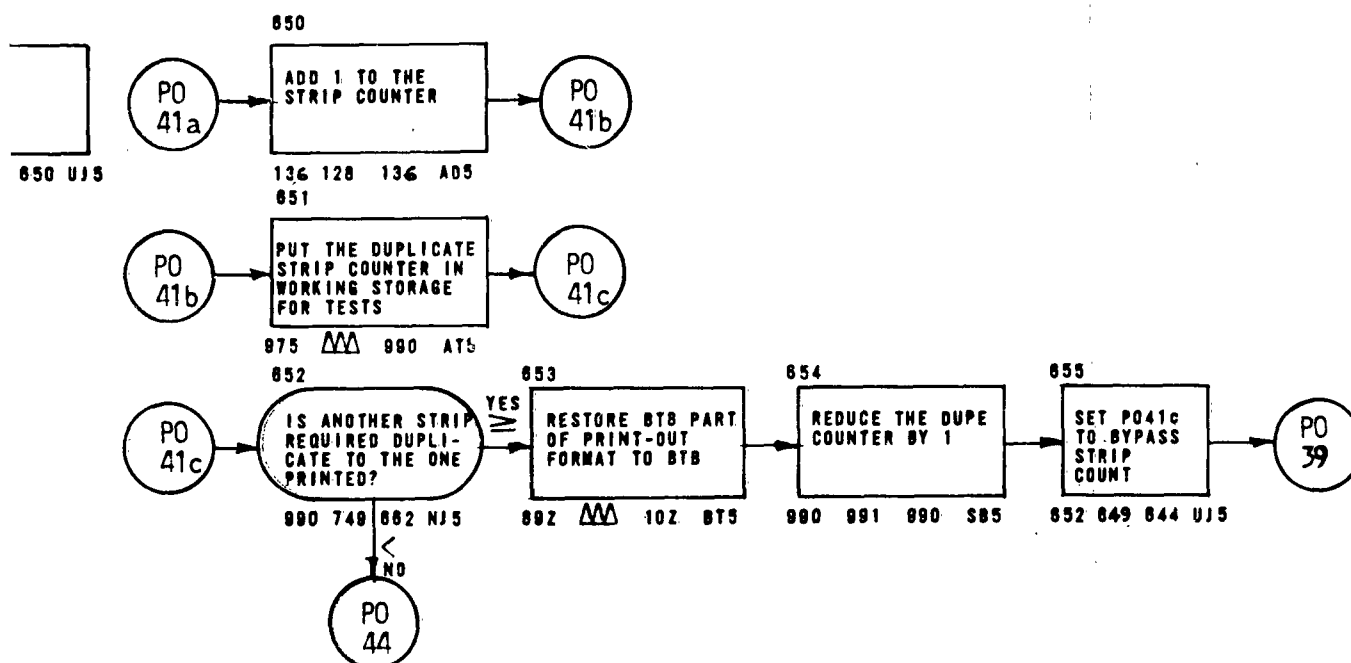
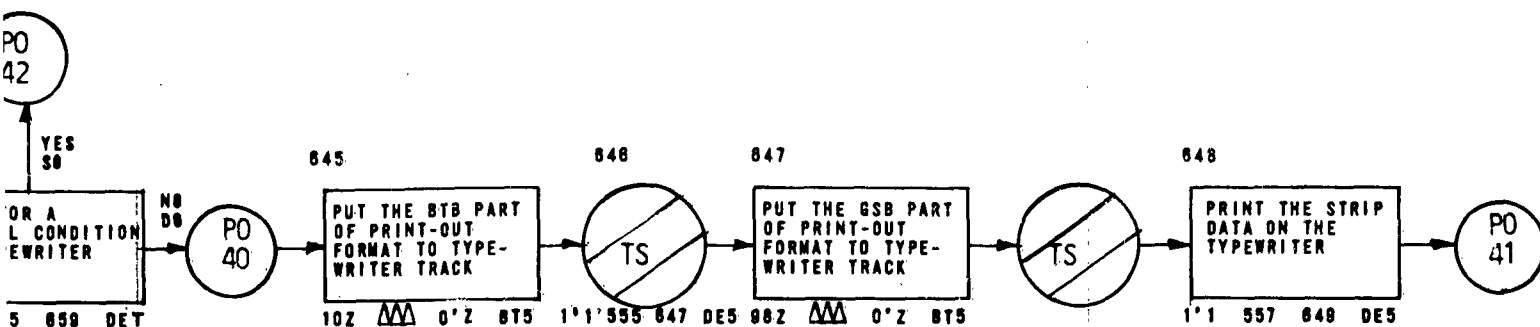
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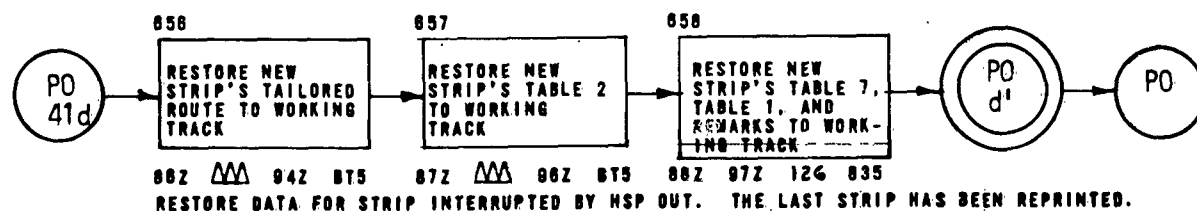
MANUAL INTERFERENCE
ON TYPEWRITER, CONTINUE
TESTING UNTIL CONDITION
IS CLEARED.

980 - AAA At HSP APD
981 - ARE SUM ERR rIA
989 - 76Z A01 135 000

HSCL-W ON TYPEWRITER INDICATES THAT THE HSP IS NOW IN OPERATION AND READY TO PRINT STRIPS.

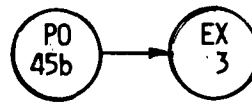
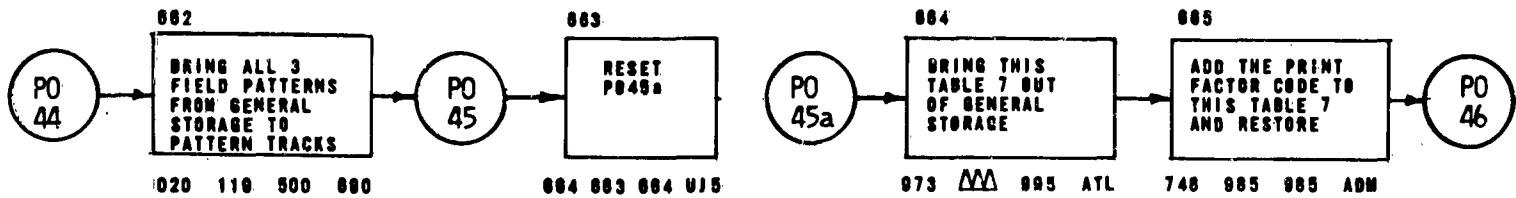


3

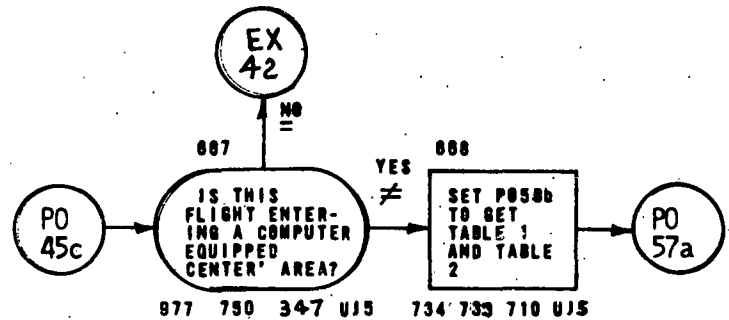


REENTER THE
TYPEWRITER PRINT-OUT
ROUTINE AT POINT
CORRESPONDING TO THE
HSP ENTRY POINT
ORIGINALLY USED BY
THIS NEW STRIP.

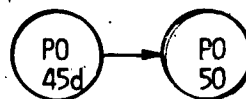
REENTER THE HSP PRINT-OUT
ROUTINE AT POINT CORRESPONDING
TO THIS STRIP'S ORIGINAL TYPEWRITER
PRINT-OUT ENTRY POINT.



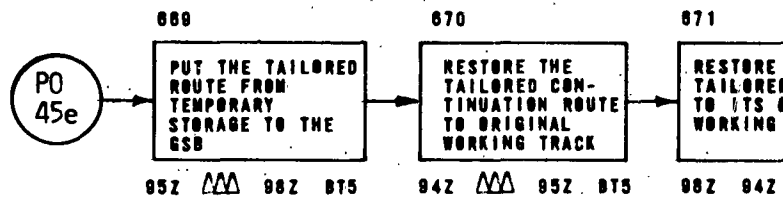
'X' MESSAGE ON PROPOSED DEPARTURE. RIDER STRIP IS PRINTED. APREQ STRIP ON A PROPOSED DEPARTURE FROM ANOTHER CENTER.



'X' MESSAGE ON A PREVIOUSLY COMPLETED ENROUTE FLIGHT PLAN. RIDER STRIP HAS JUST BEEN PRINTED.

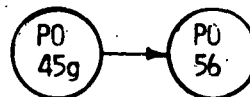
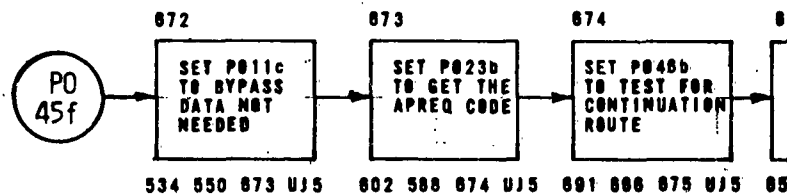


PROPOSED DEPARTURE WITH THE CONTINUATION ROUTE ON HAND WHEN ORIGINAL STRIPS WERE PRINTED. RIDER STRIP HAS JUST BEEN PRINTED



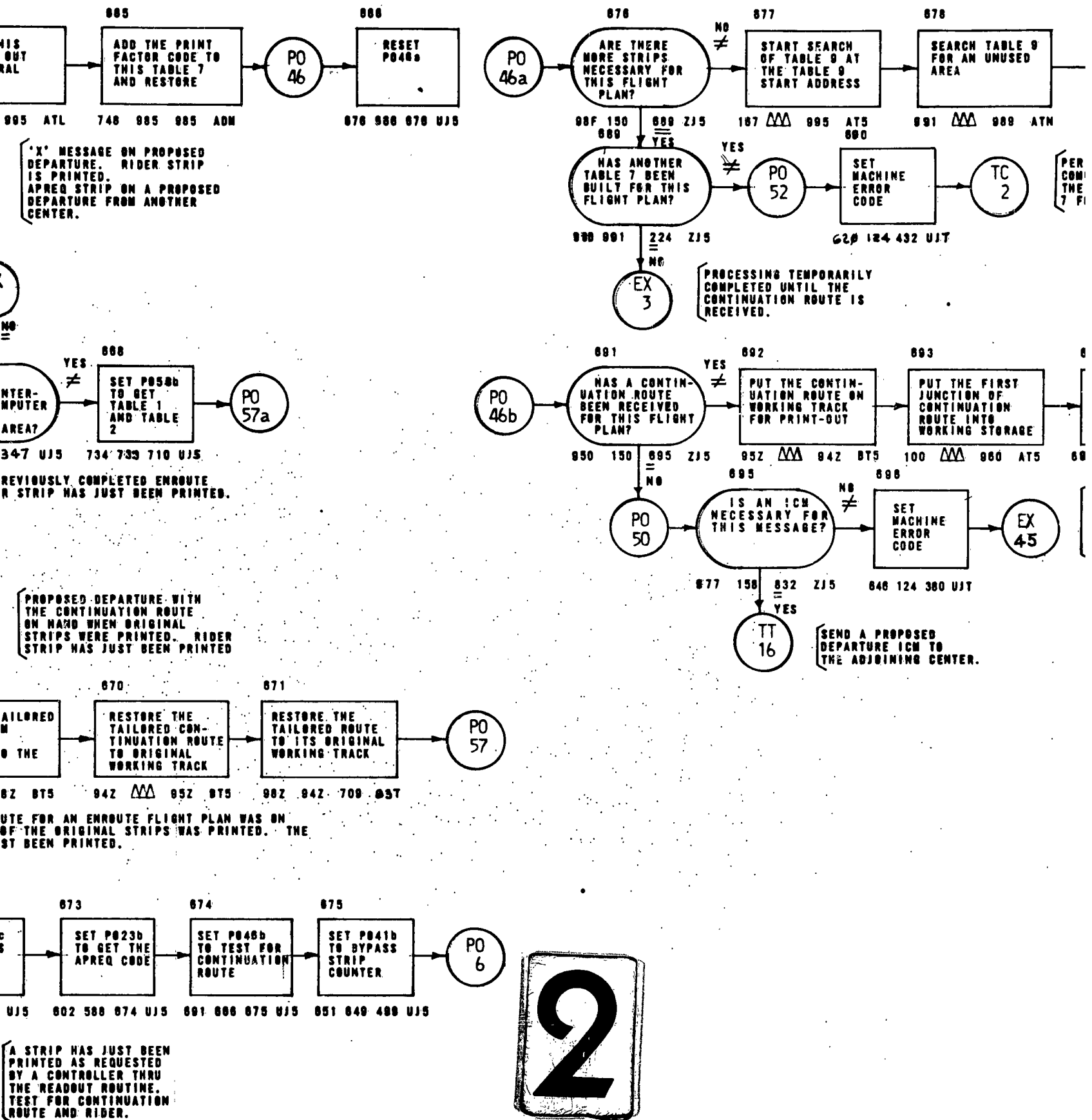
THE CONTINUATION ROUTE FOR AN ENROUTE FLIGHT PLAN WAS ON HAND WHEN THE LAST OF THE ORIGINAL STRIPS WAS PRINTED. THE RIDER STRIPS HAS JUST BEEN PRINTED.

PROPOSED DEPARTURE

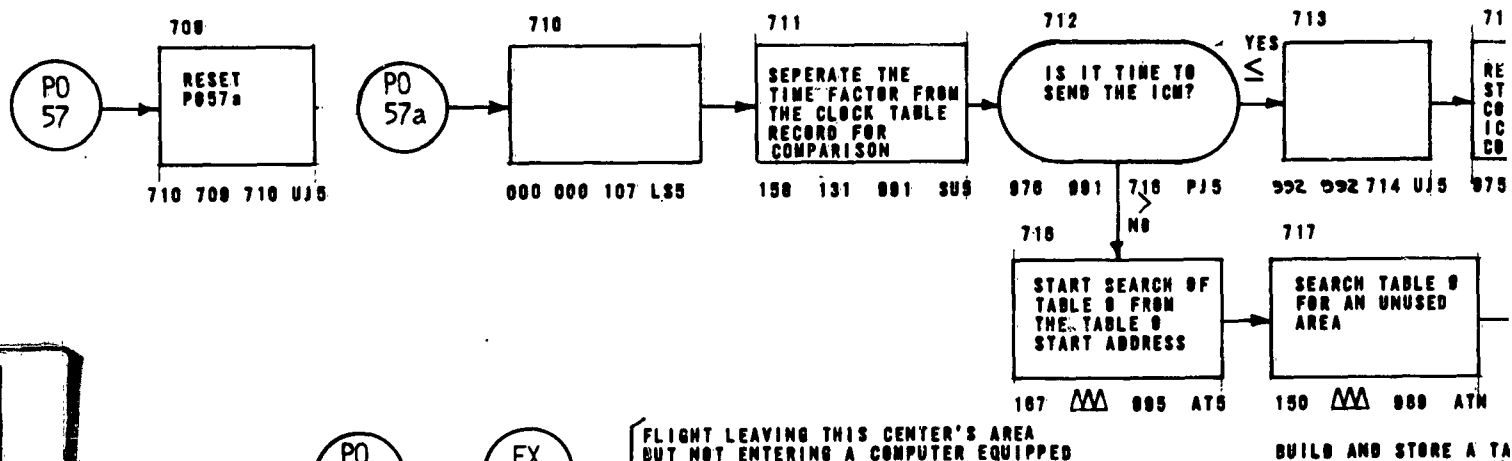
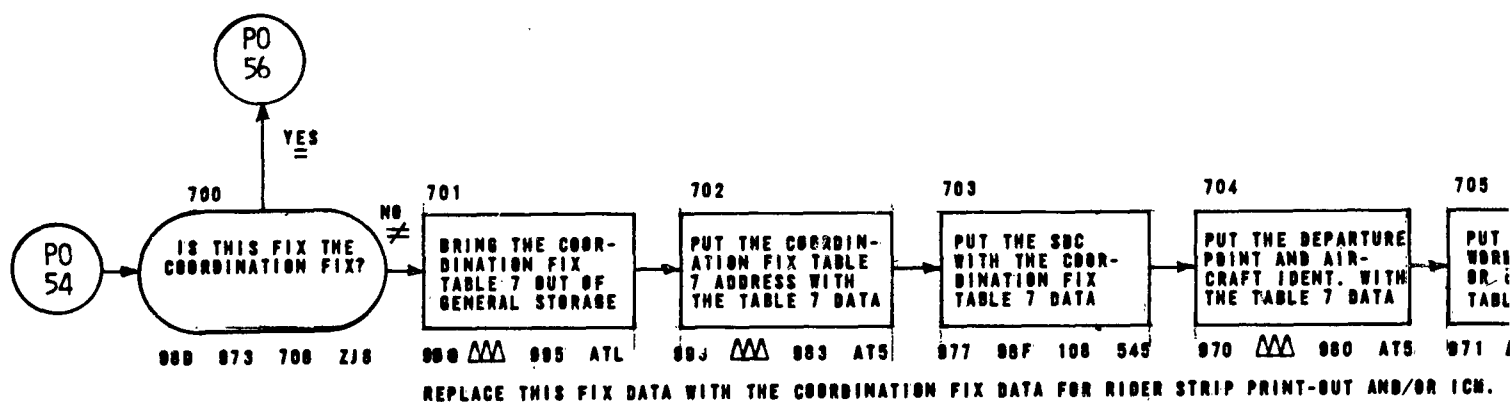
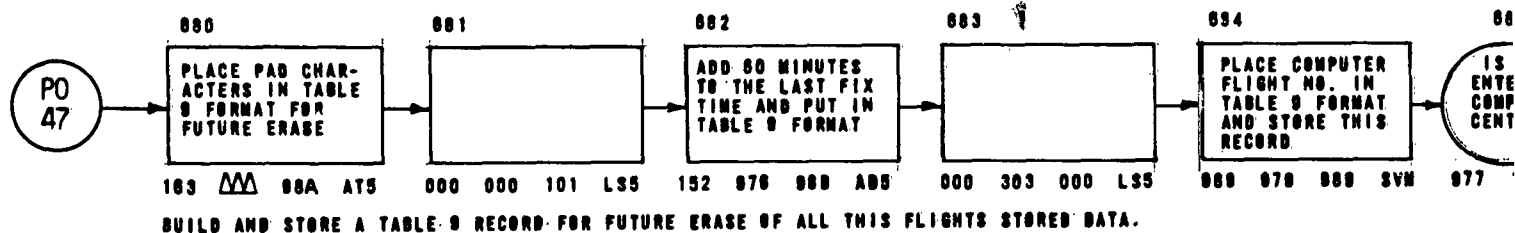


A STRIP HAS JUST BEEN PRINTED AS REQUESTED BY A CONTROLLER THRU THE READOUT ROUTINE. TEST FOR CONTINUATION ROUTE AND RIDER.

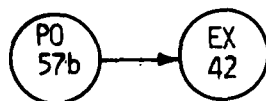




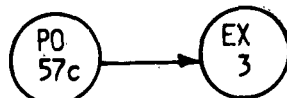




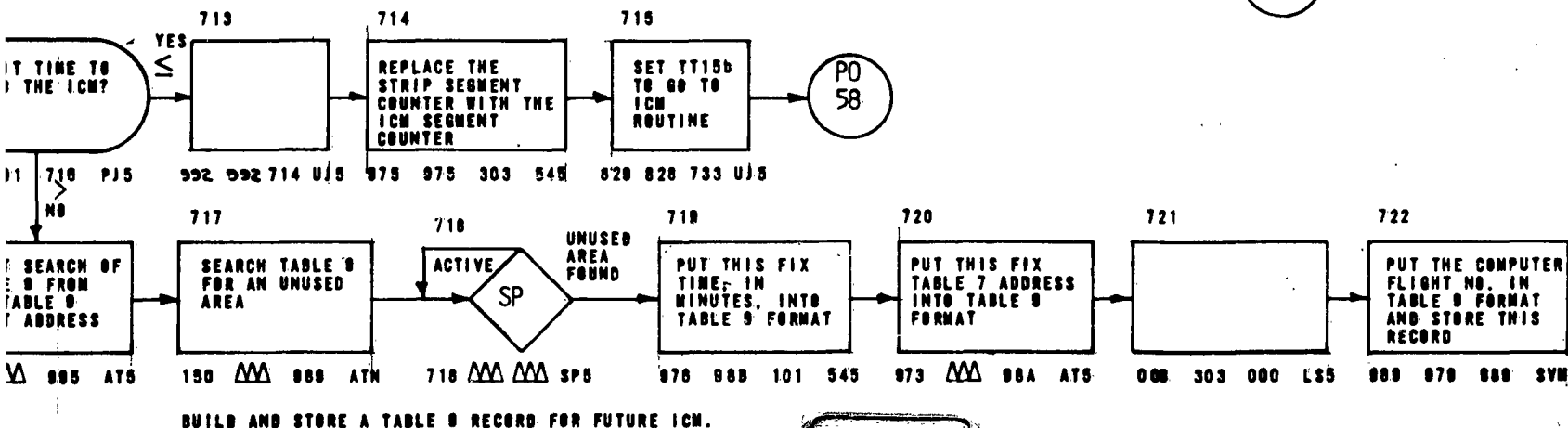
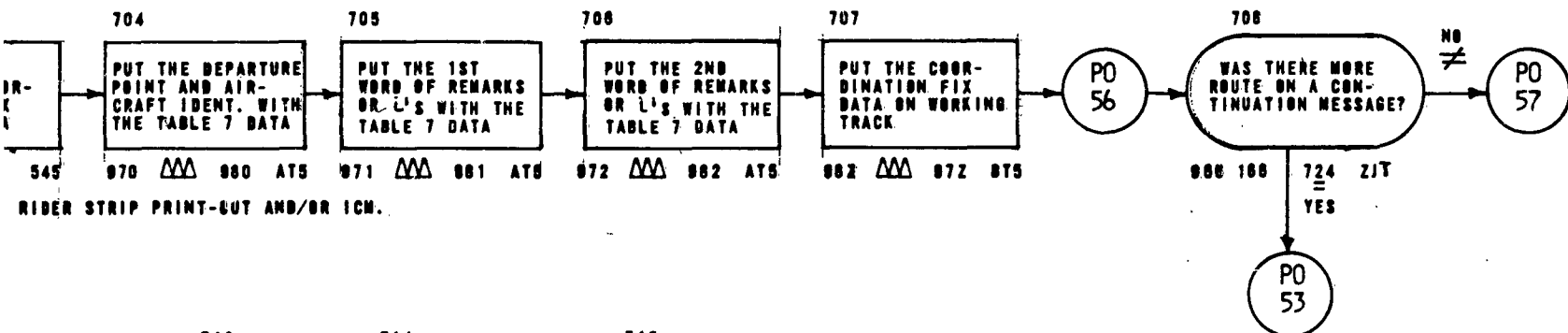
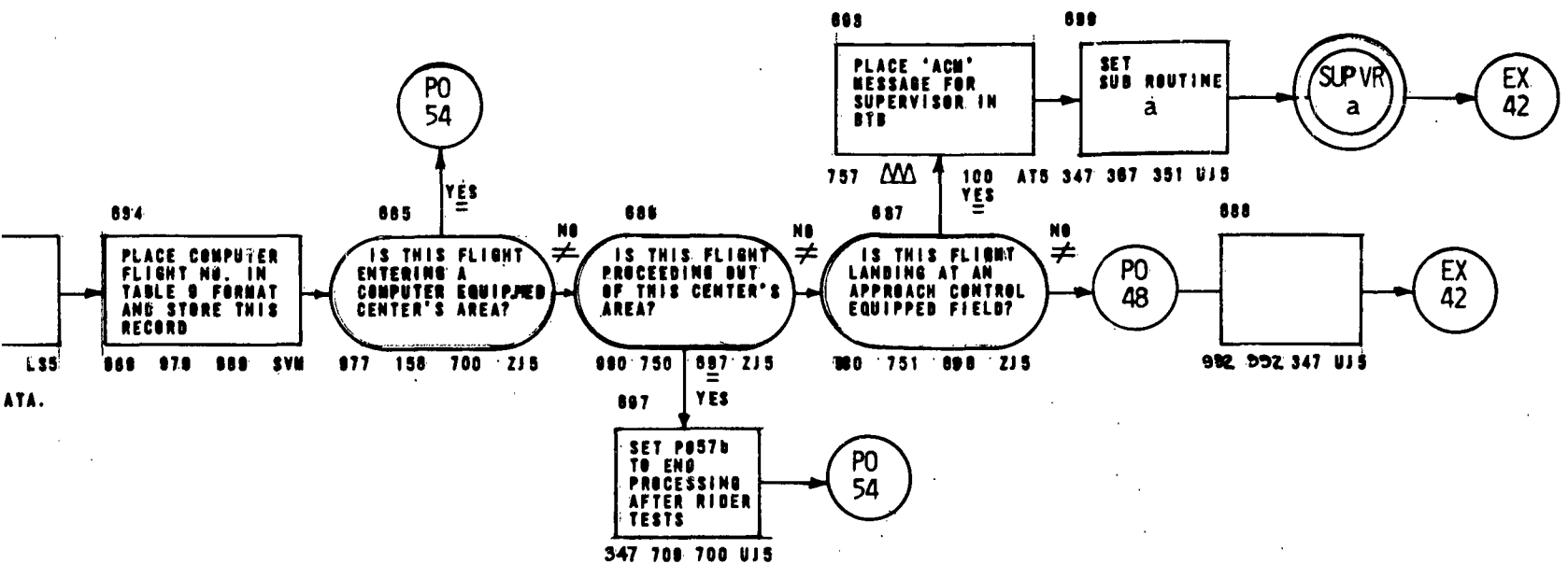
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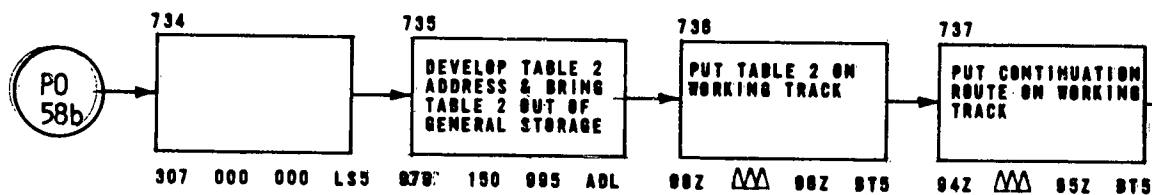
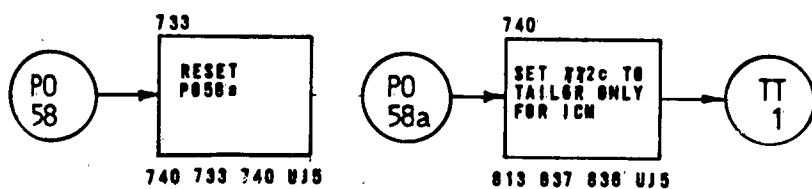
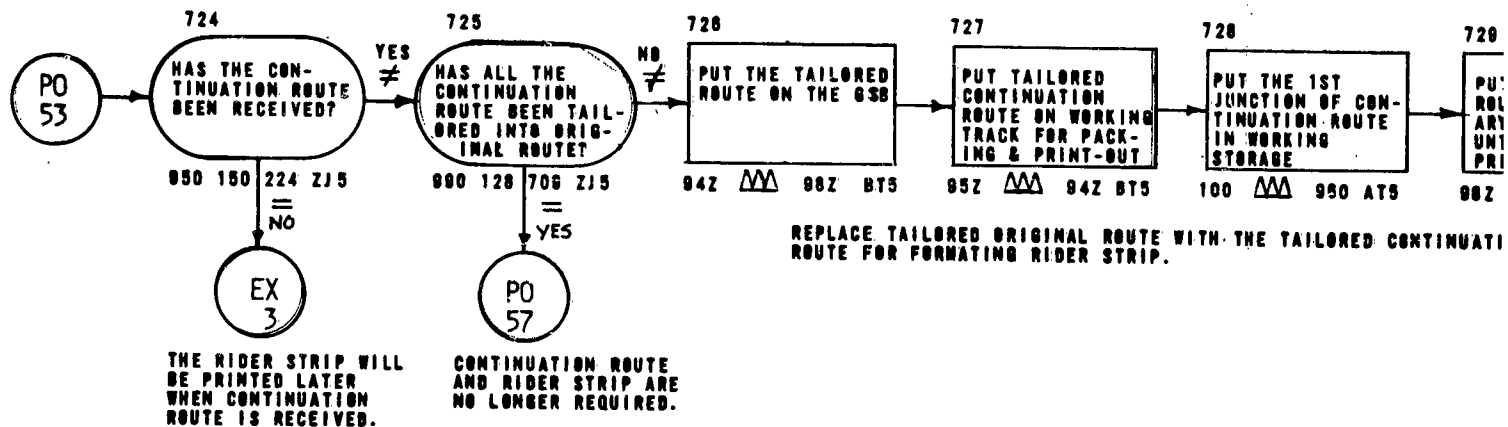


FLIGHT LEAVING THIS CENTER'S AREA BUT NOT ENTERING A COMPUTER EQUIPPED CENTER'S AREA. ALL STRIPS, INCLUDING RIDER STRIP WHEN CONTINUATION ROUTE WAS ON HAND AT THIS TIME, HAVE BEEN PRINTED.



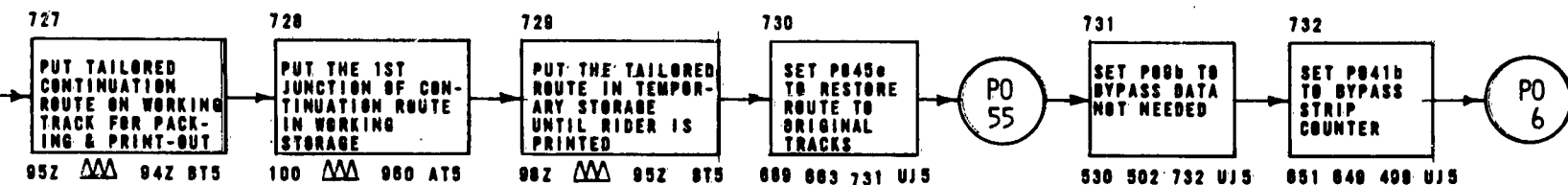
STRIP REQUESTED FOR REPRINT HAS BEEN PRINTED, INCLUDING A RIDER STRIP IF THE ROUTE HAD A CONTINUATION PORTION.



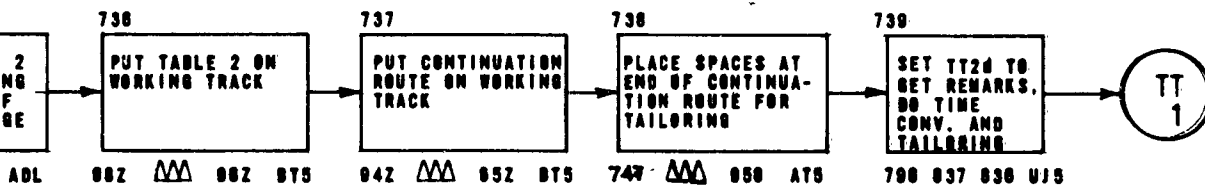


CONTINUATION ROUTE RECEIVED AFTER ALL ORIGINAL STRIPS HAD BEEN PRINTED WITHOUT THE CONTINUATION ROUTE PORTION. THE RIDER STRIP WAS JUST PRINTED WITH ALL OF THE CONTINUATION ROUTE PORTION. BOTH ROUTES MUST NOW BE TAILORED TOGETHER FOR ICM.

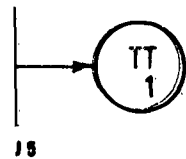
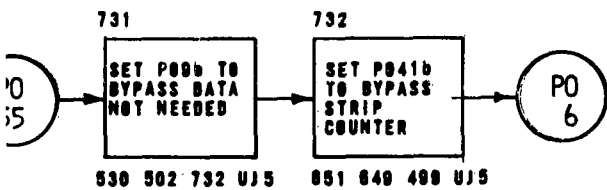




LORED ORIGINAL ROUTE WITH THE TAILORED CONTINUATION
ORINATING RIDER STRIP.



AL STRIPS HAD BEEN PRINTED WITHOUT THE
WAS JUST PRINTED WITH ALL OF THE CONTINUATION
ORED TOGETHER FOR ICM.



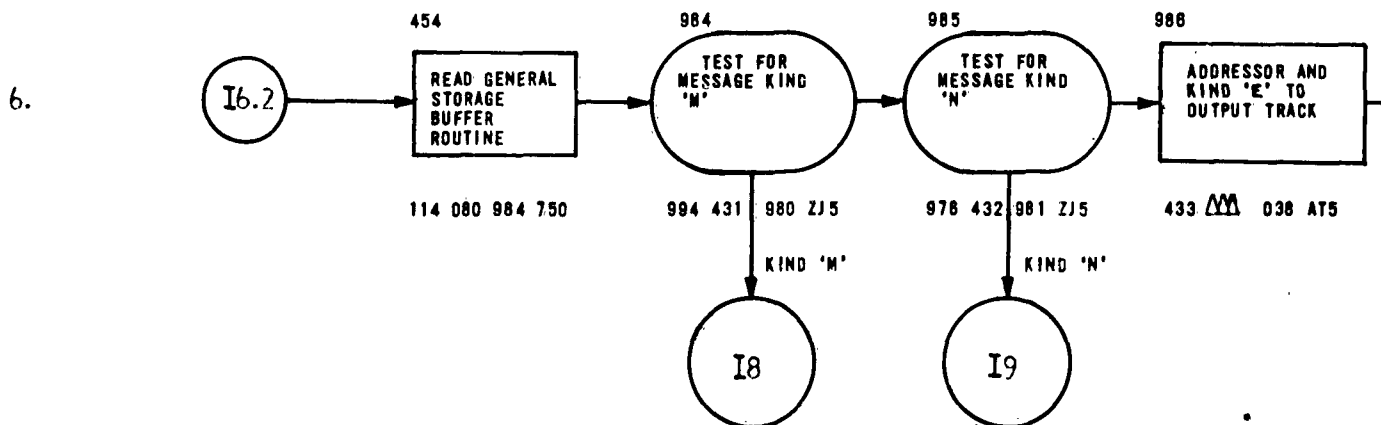
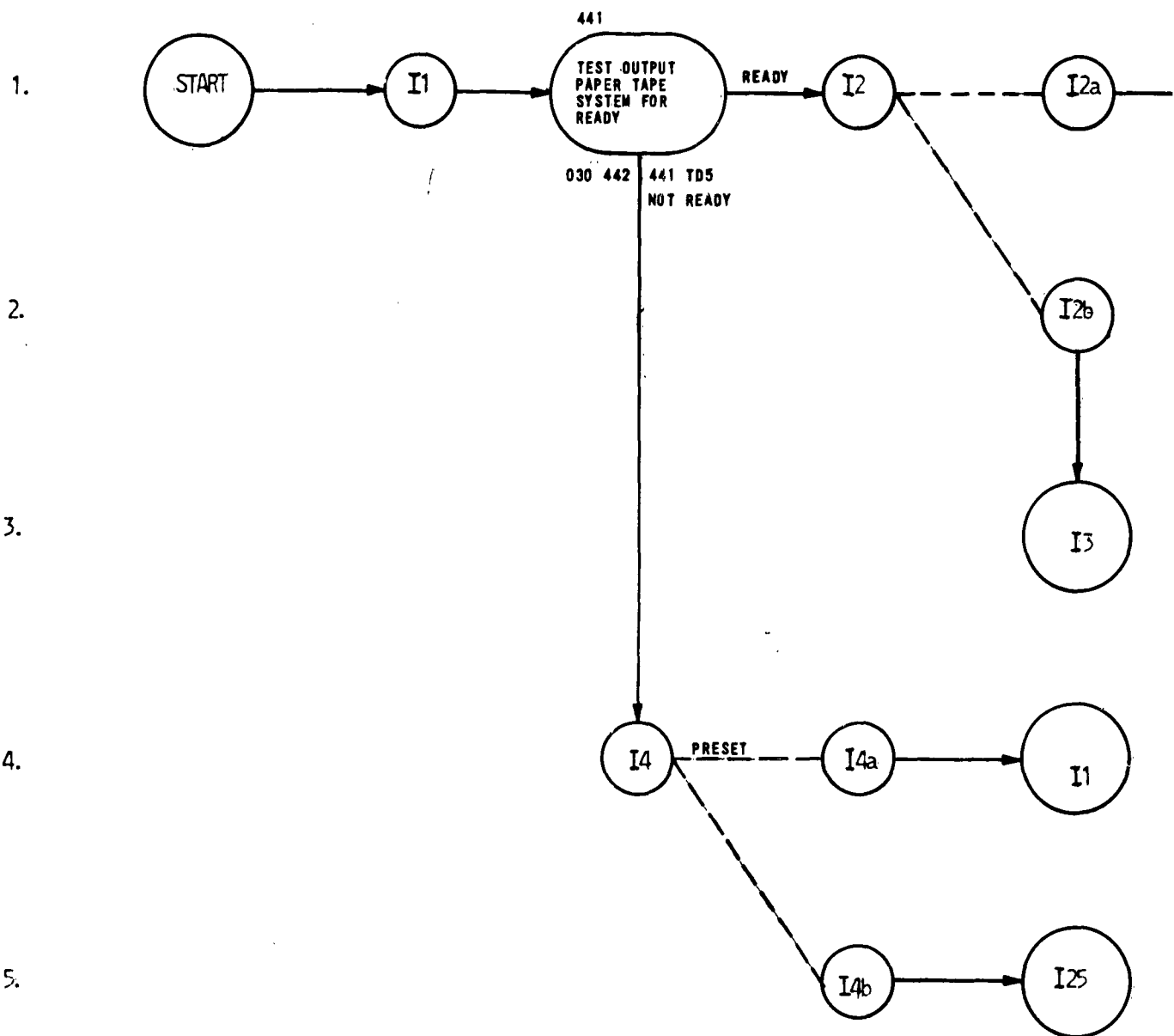
128	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	1	Δ	1 constant.
131	X	X	X	X	X	X	∅	∅	∅	∅	∅	∅	The current Clock Table record for this ten minute period.
136	-not used -					r	X	X	X	X	X	r	
150	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Strips printed counter.
152	∅	∅	∅	∅	∅	∅	∅	∅	∅	6	∅	∅	Spaces comparator.
158	i	Z	Δ	i	i	i	i	i	i	i	i	i	Time update constant.
163	i	i	i	i	i	i	i	i	SDC comparator.
166	i	i	i	i	i	C	T	T	Y	i	i	i	Pads constant.
167	Δ	Δ	Δ	Δ	1	1	2	2	∅	∅	∅	Δ	Continuation route comparator.
747	.	.	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Table 9 starting address..
748	i	i	i	i	i	i	i	i	i	i	P	i	Route fill constant.
749	i	i	i	i	i	i	i	i	i	1	i	i	Print constant.
750	O	V	E	R	i	i	i	i	i	i	i	i	Duplicate strip comparator. and update constant.
752	Δ	Δ	Δ	Δ	i	i	i	i	i	i	?	i	
753	i	i	.	i	i	i	i	i	i	i	i	i	OVER comparator.
754	i	i	i	i	i	i	i	i	C	T	T	Y	Color code comparator.
755	P	i	i	i	i	i	i	i	i	i	i	i	Coordination fix comparator.
756	i	i	i	i	i	i	i	i	Δ	Δ	Δ	Δ	Continuation route comparator.
													Proposed comparator.
													End of route comparator.

The remarks beside the above constants and factors define the prime purpose as used in this routine. The flow charts will define any secondary usage of these locations.

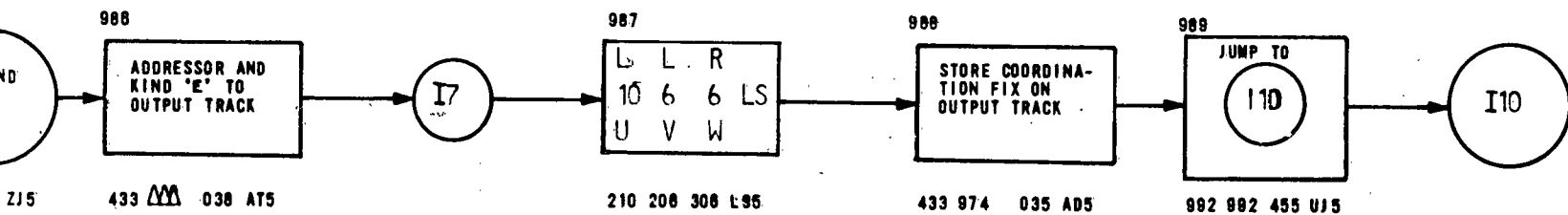
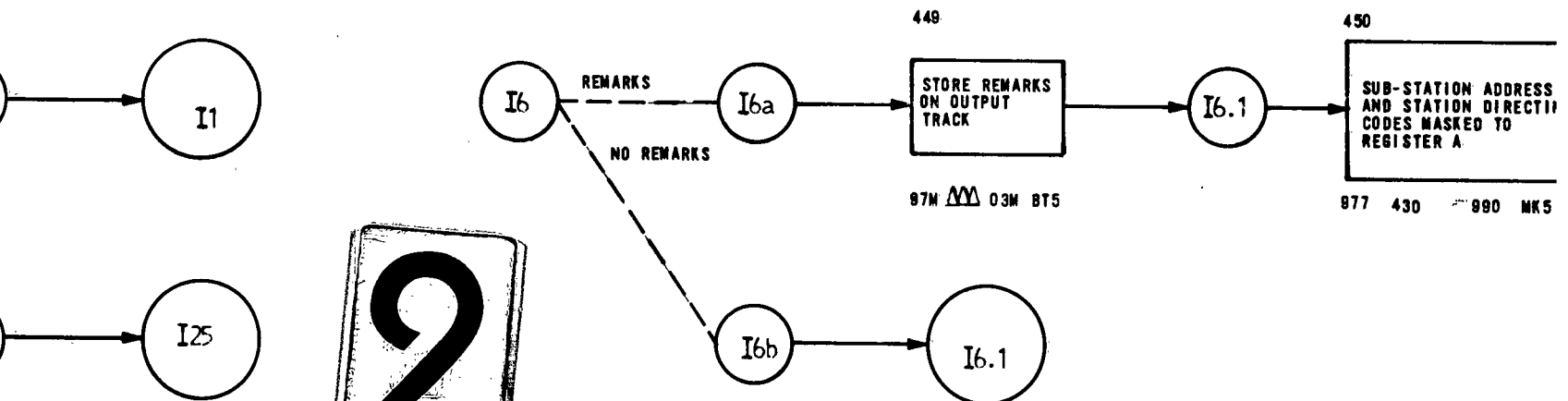
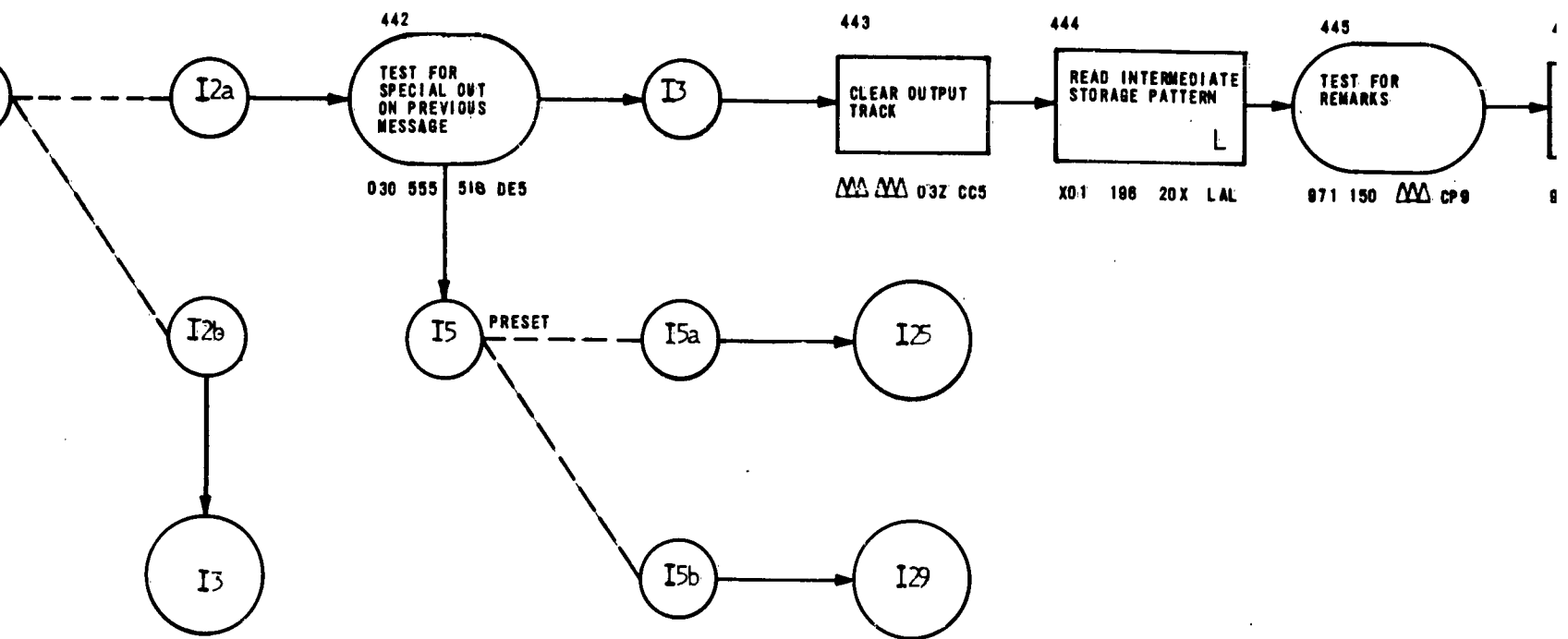
APPENDIX XI

INTERCENTER MESSAGE ROUTINE

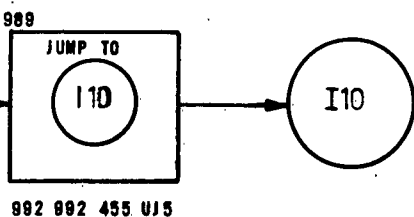
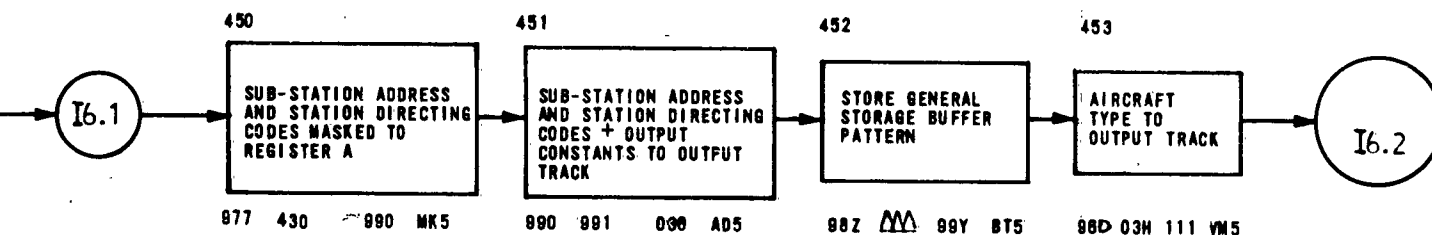
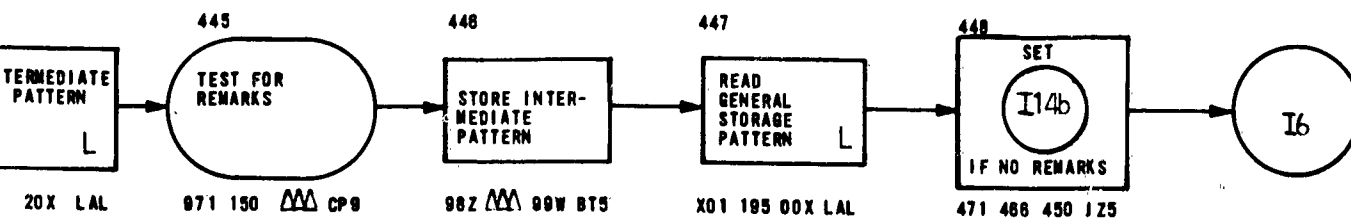
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1



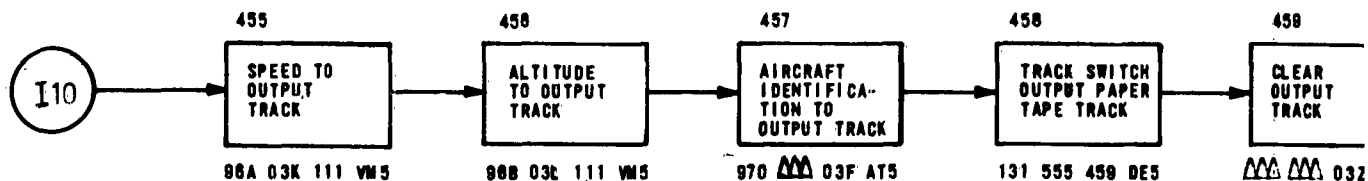
ID: 'M'



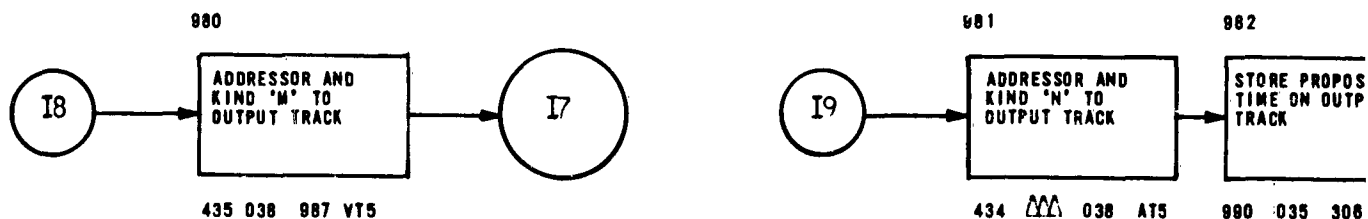
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LINE NO.

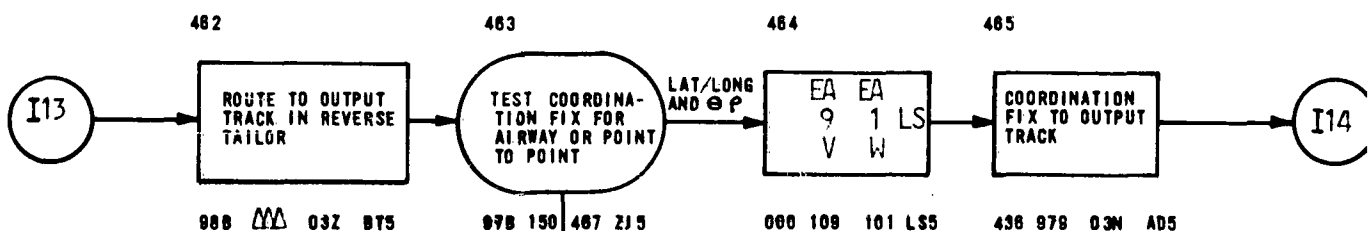
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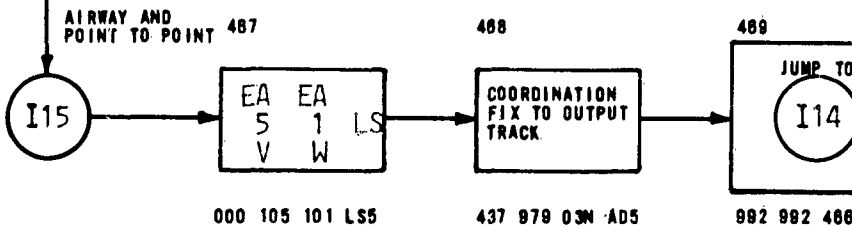
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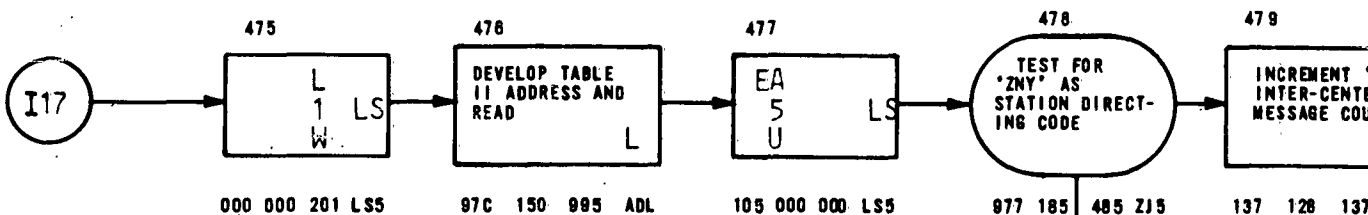
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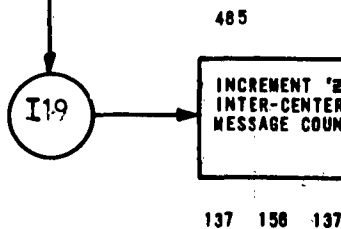
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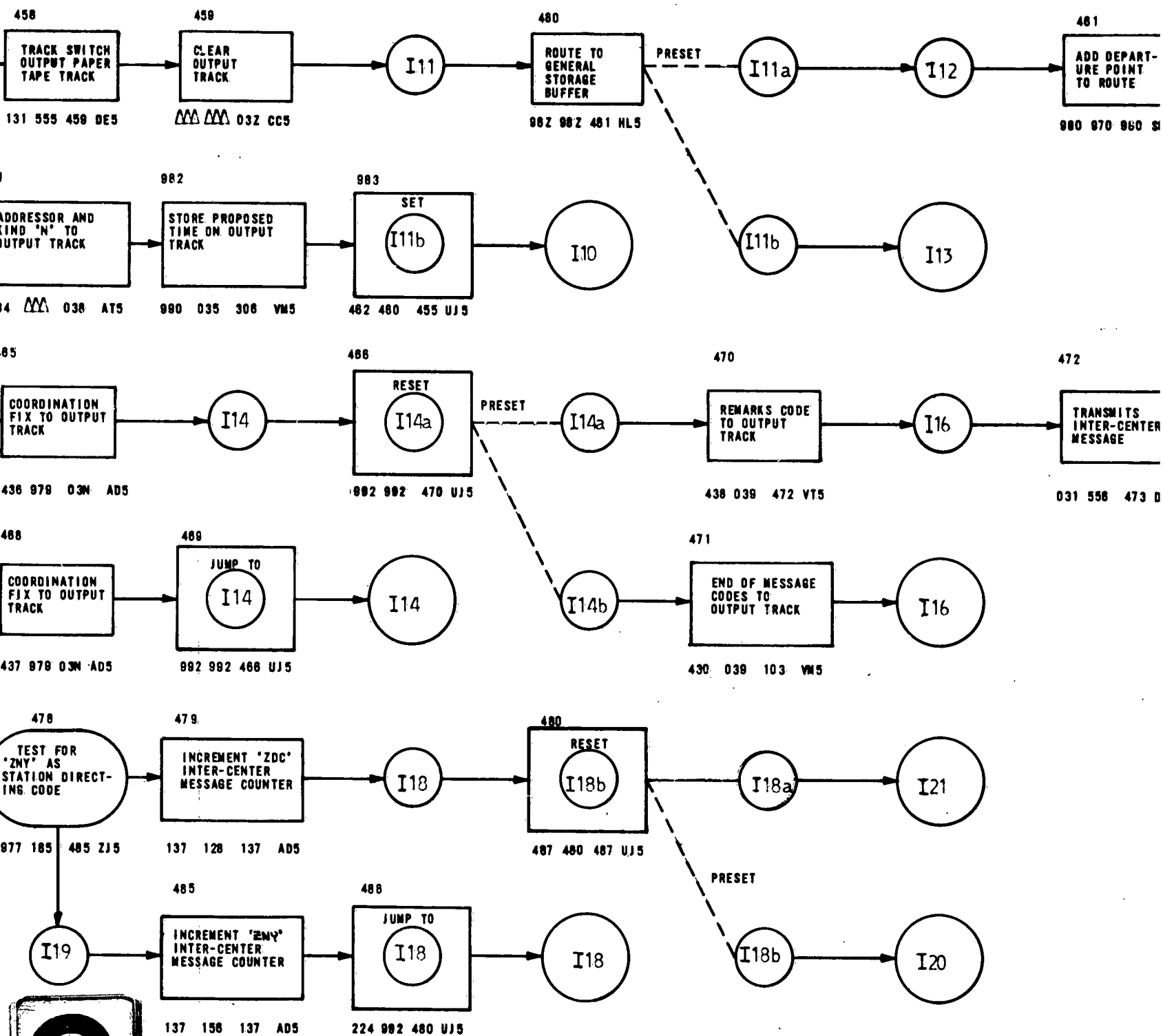


5.

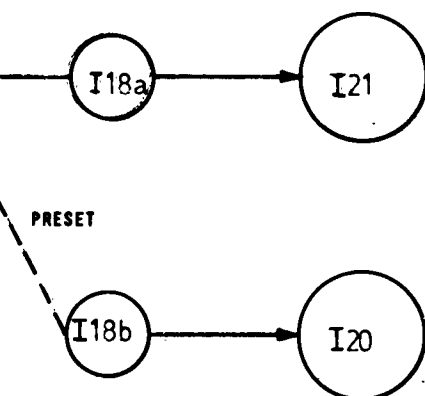
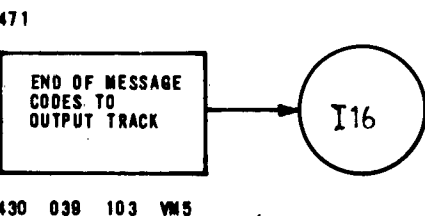
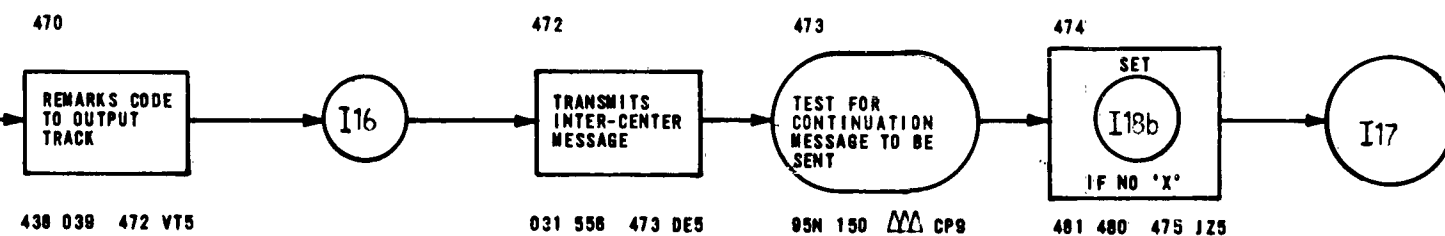
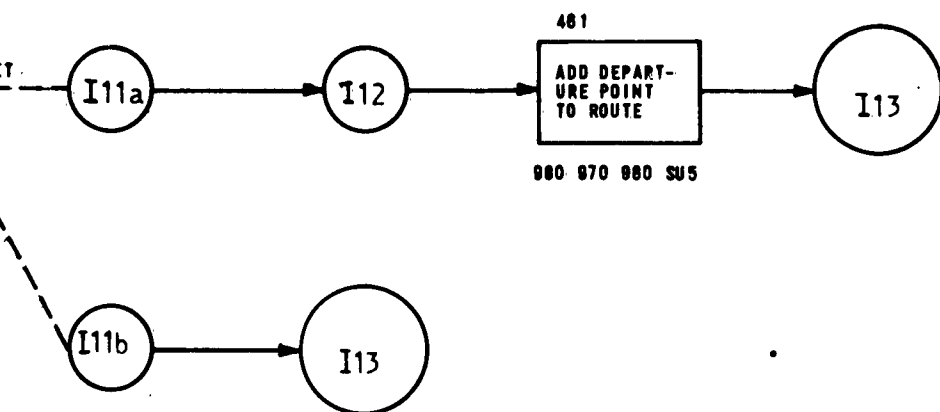


6.





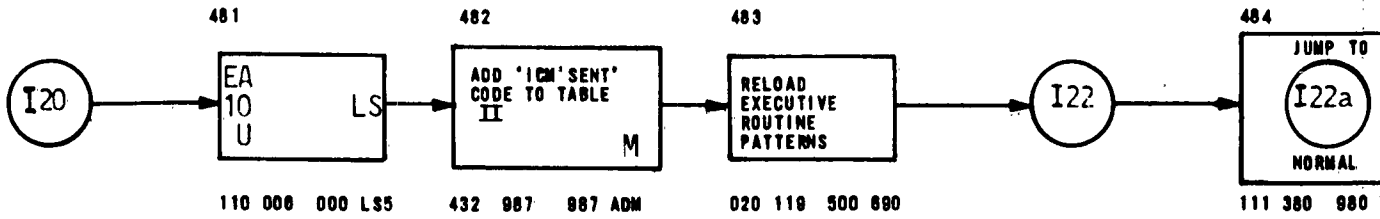
2



3

LINE NO.

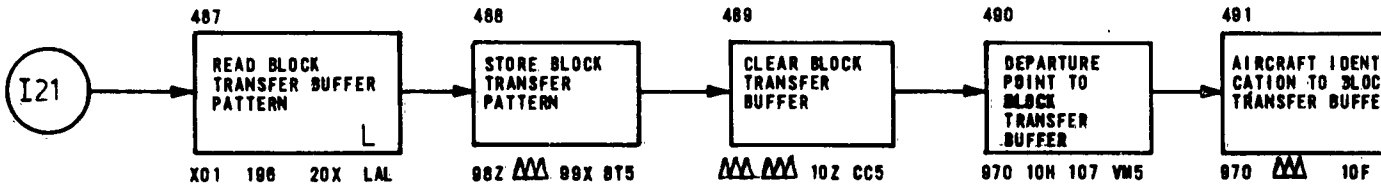
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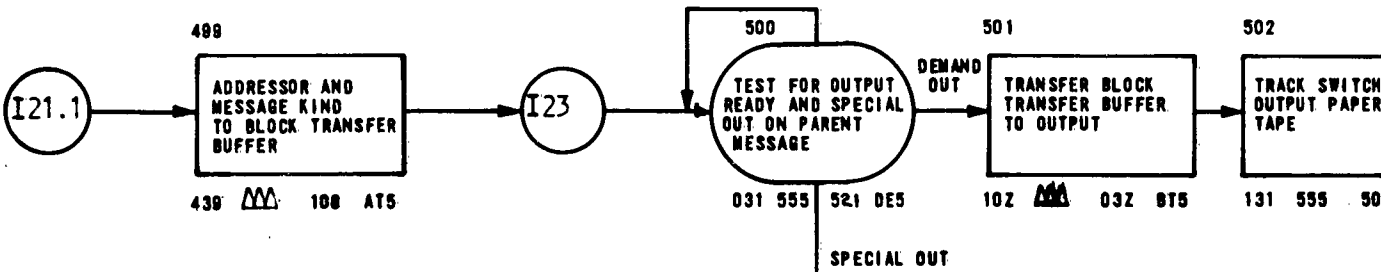
2.

3.

4.



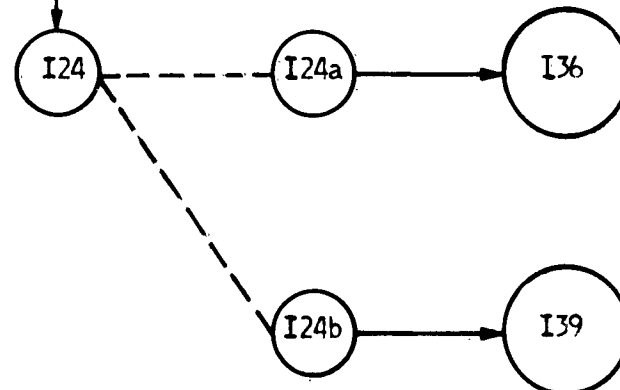
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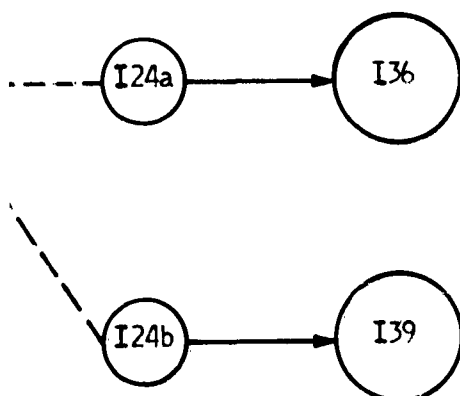
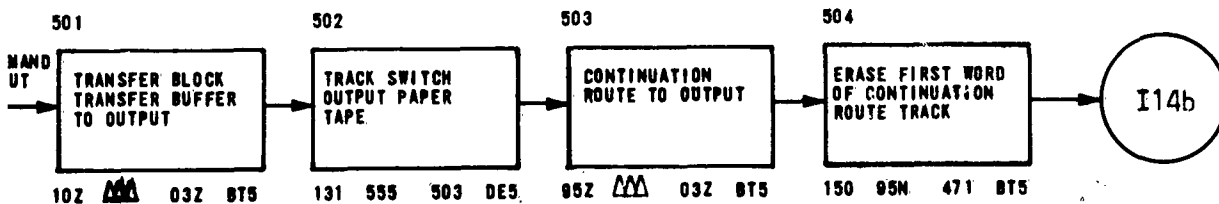
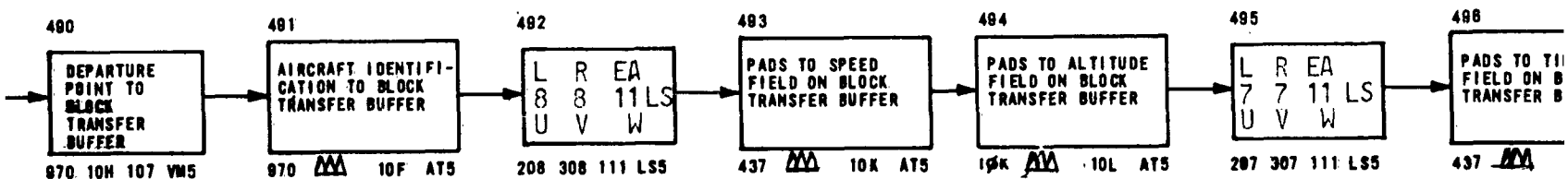
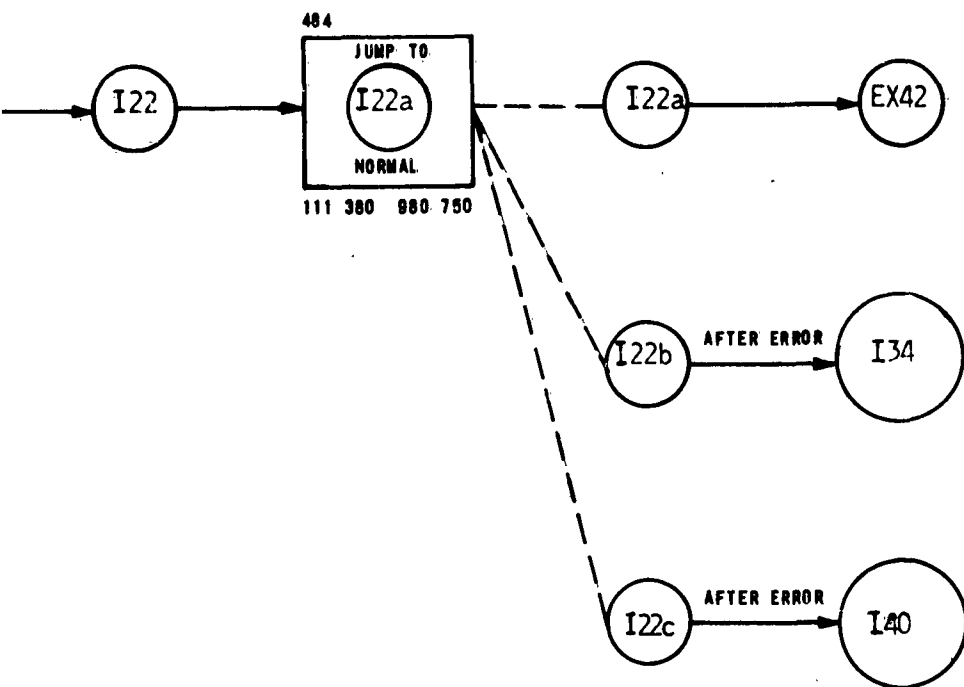


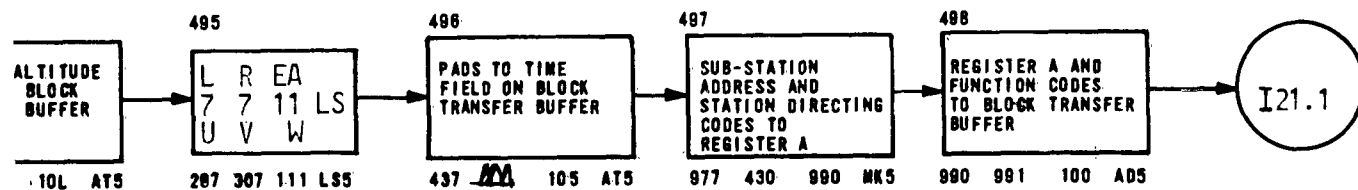
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7.

1





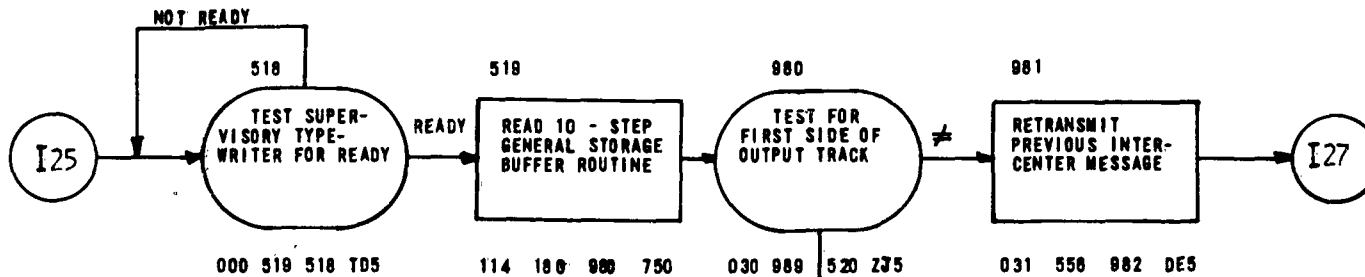


14b

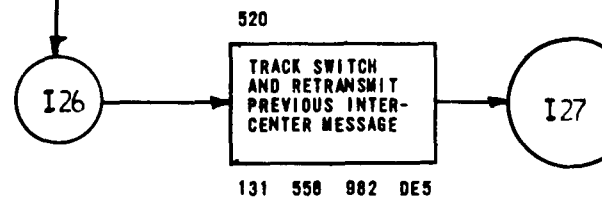
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LINE NO.

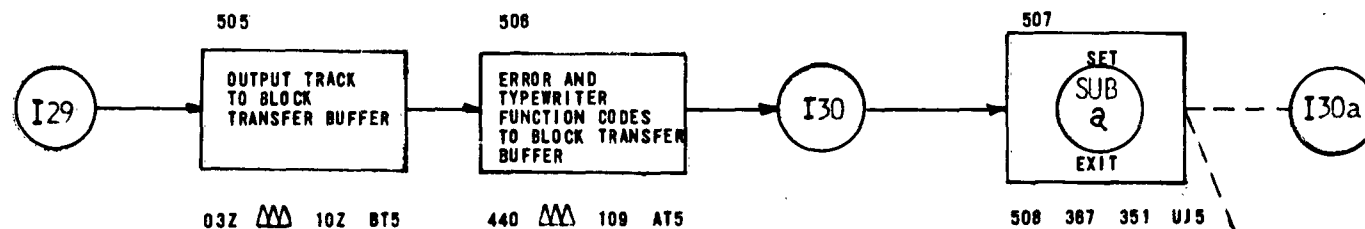
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2.

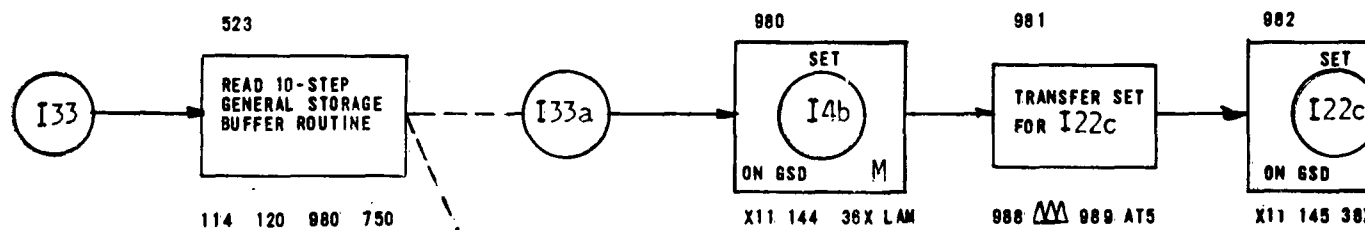


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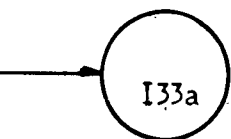
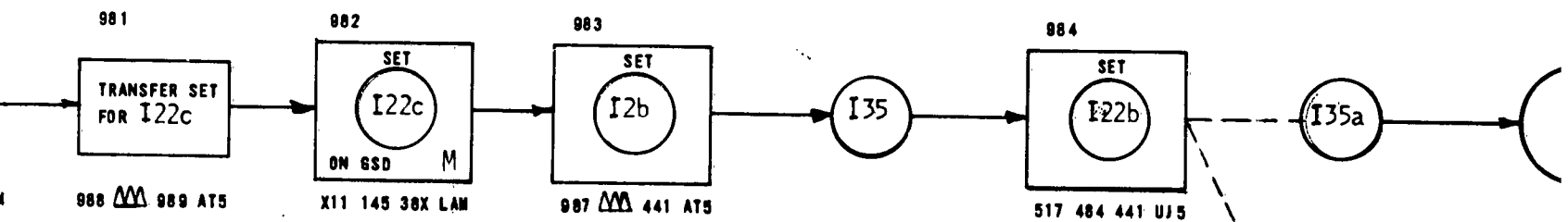
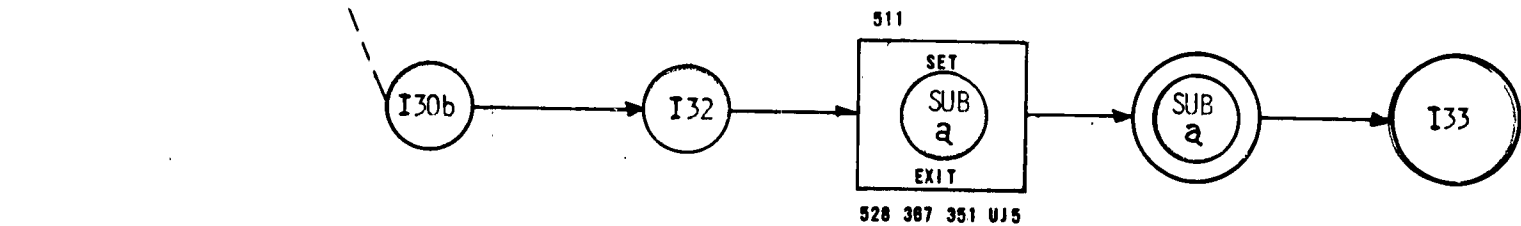
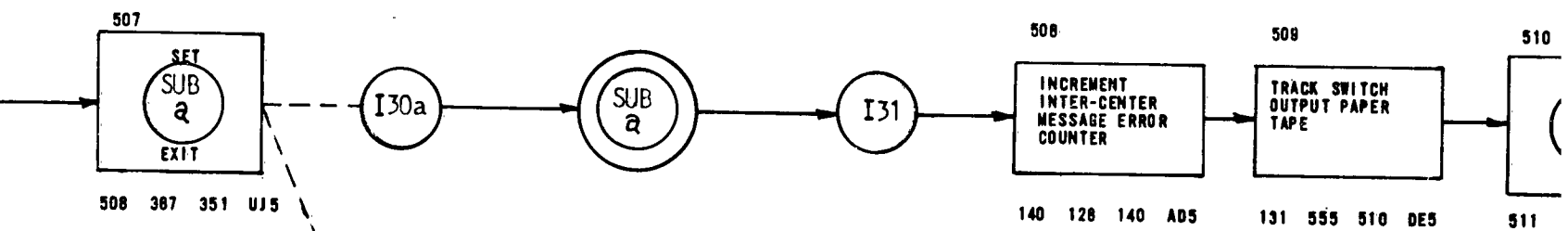
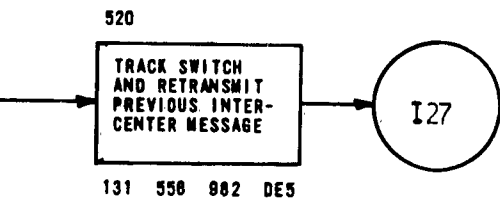
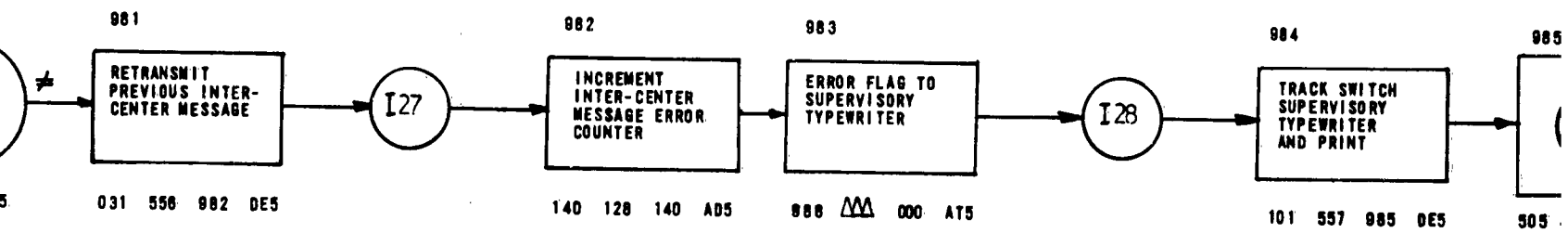
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5.

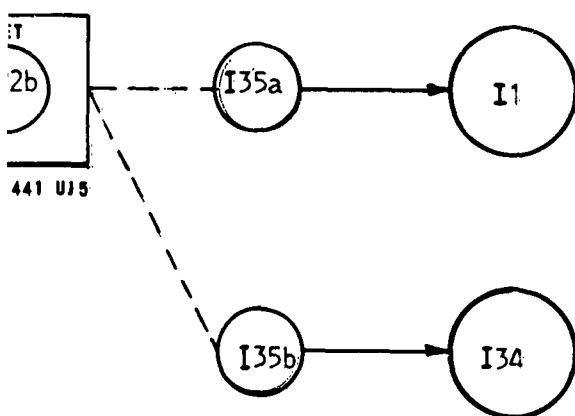
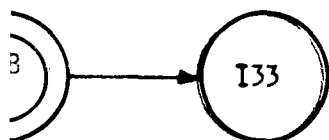
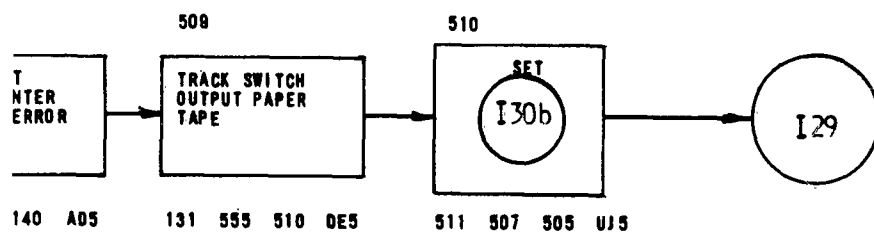
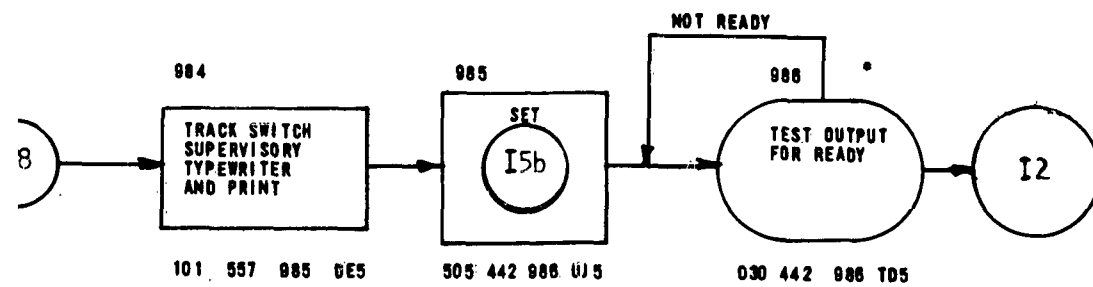


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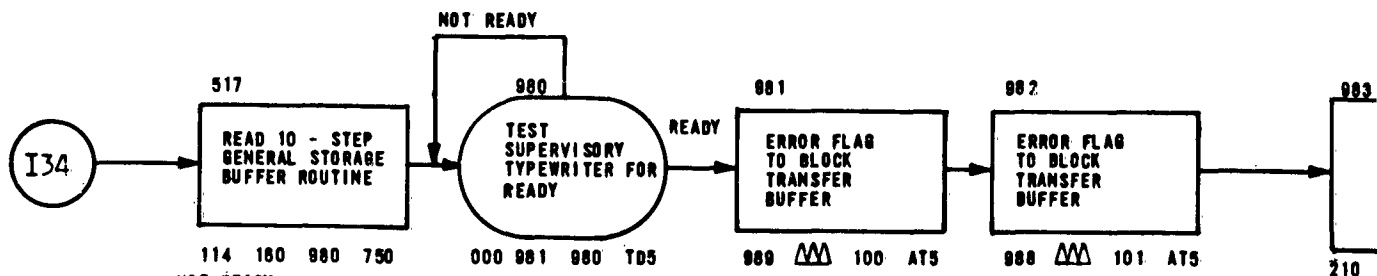


2

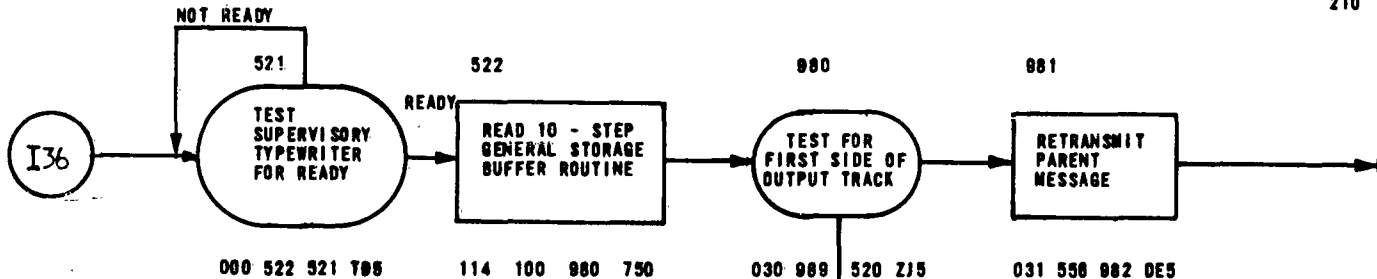


LINE NO.

1.



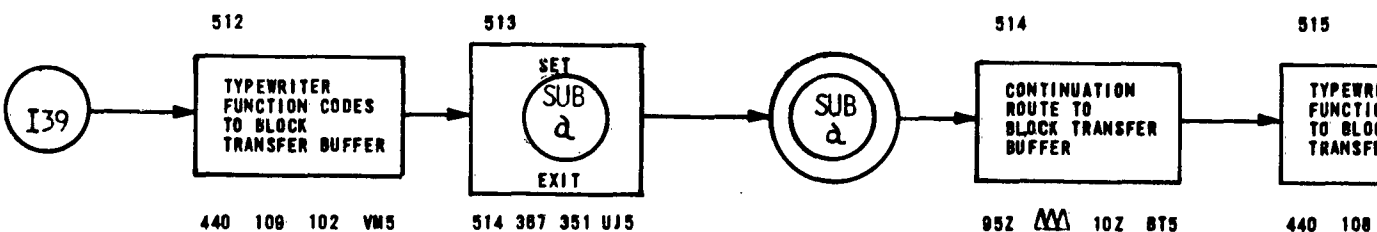
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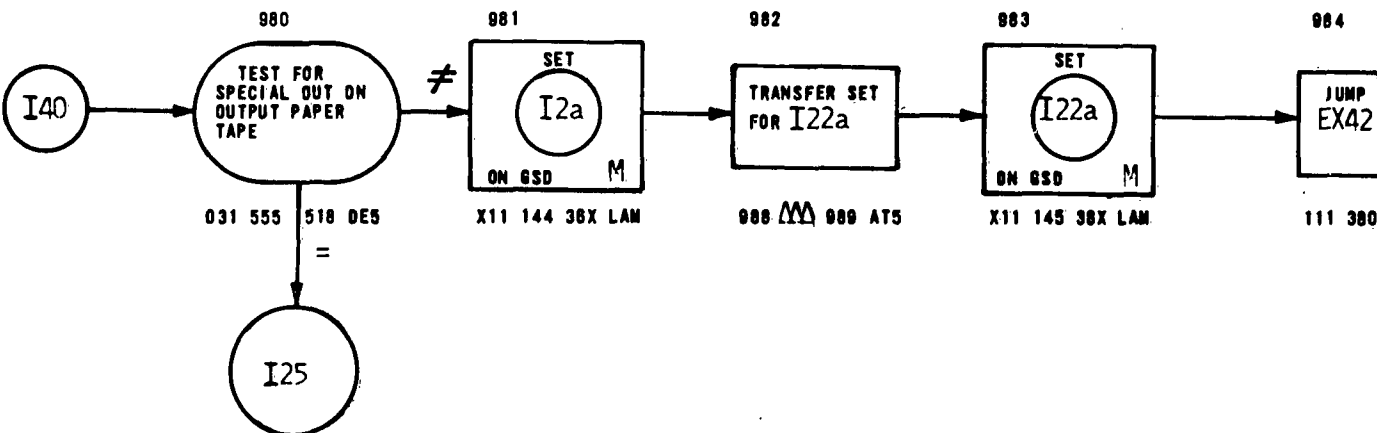
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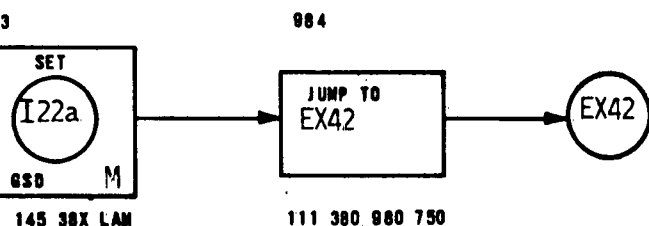
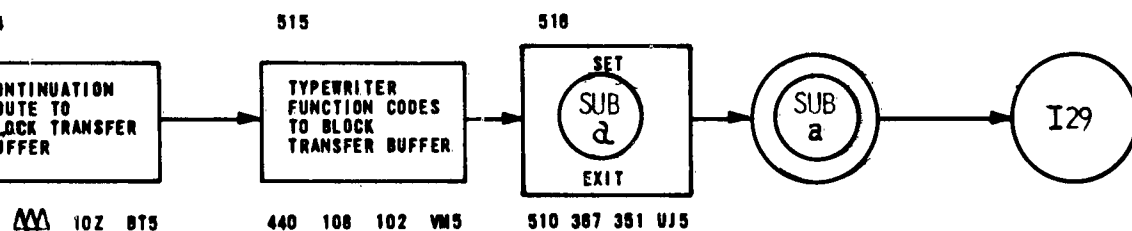
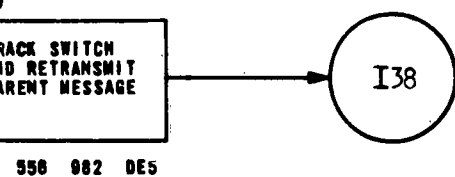
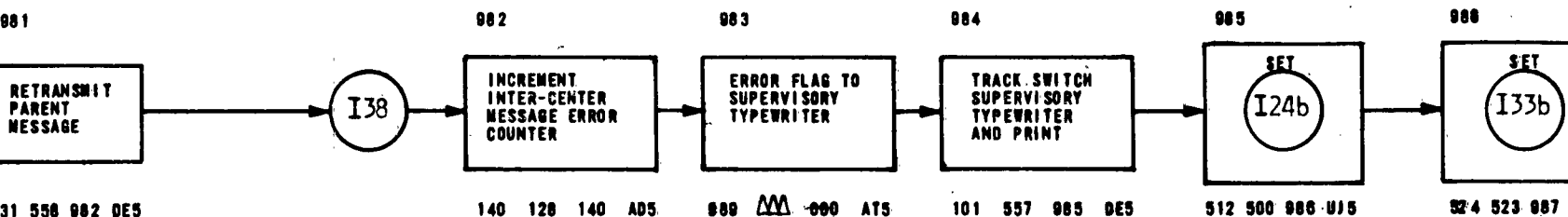
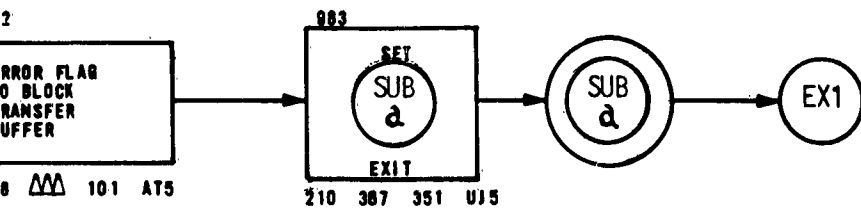
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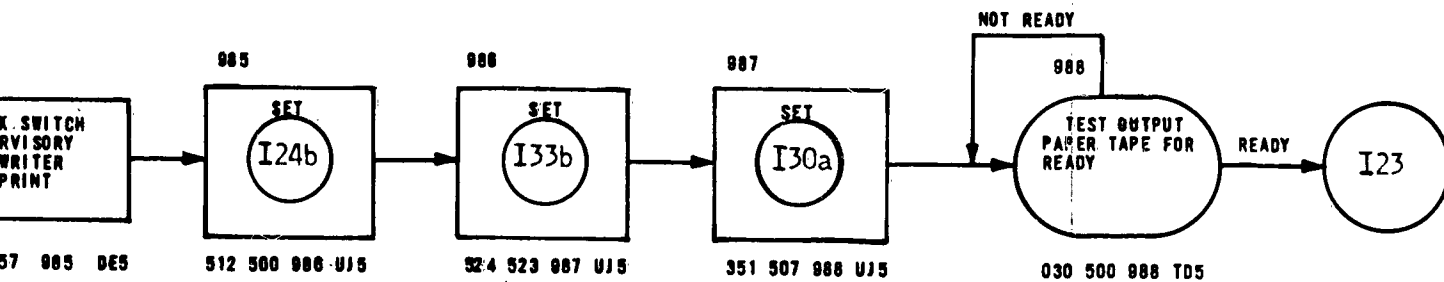
5.



1



2



CONSTANTS FACTORS AND WORKING STORAGE FOR INTER-CENTER MESSAGE ROUTINE.

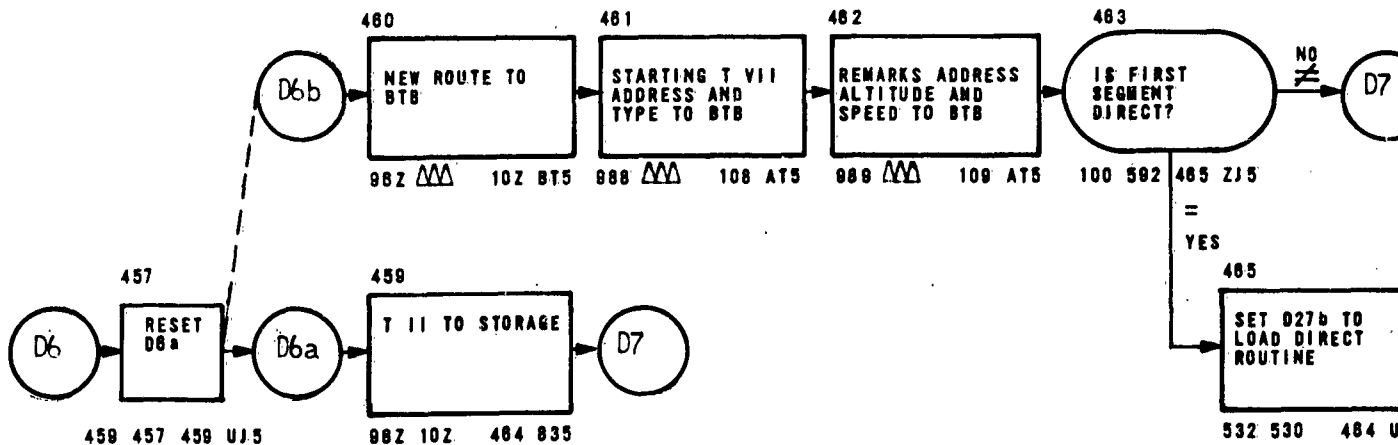
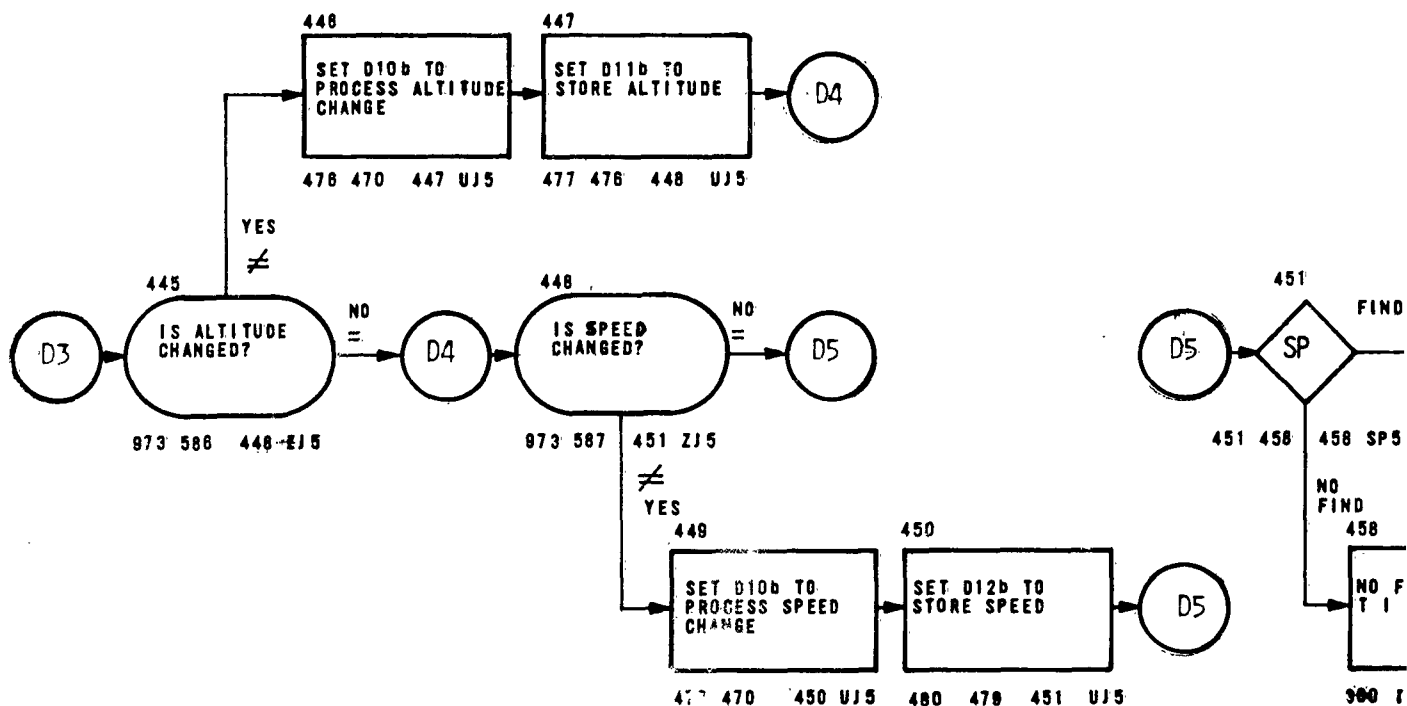
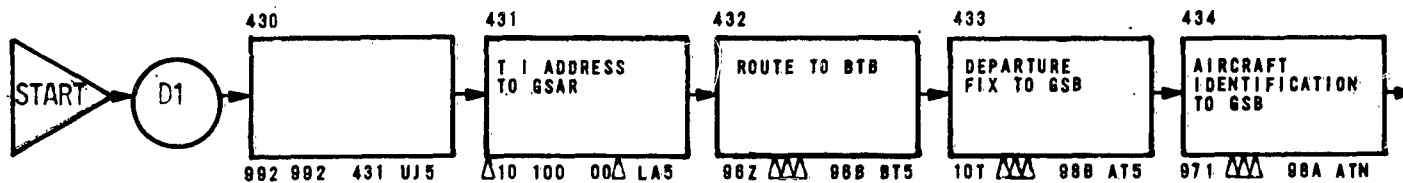
ON HIGH SPEED DRUM:

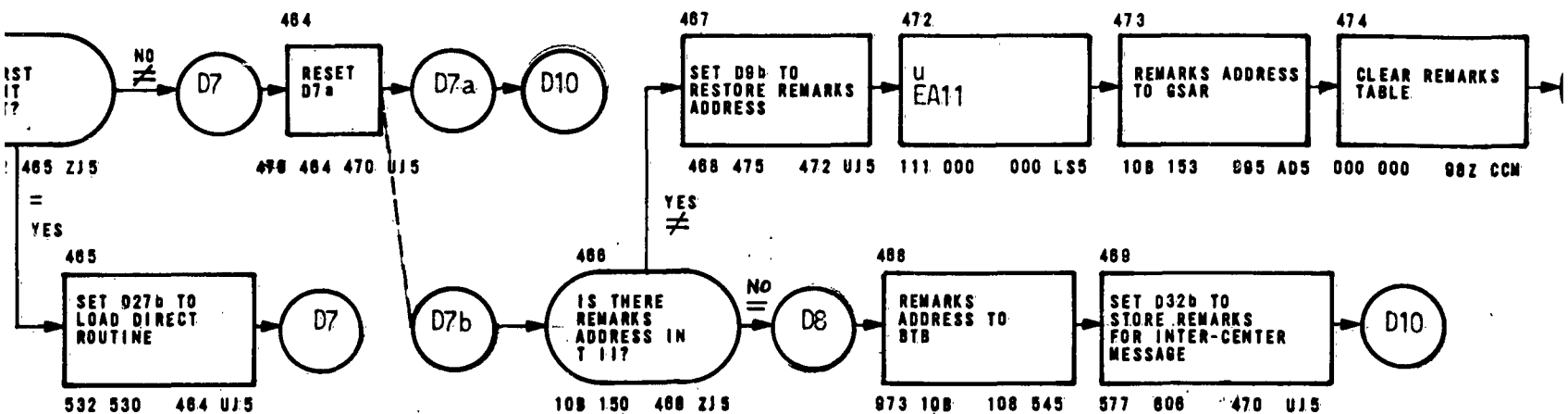
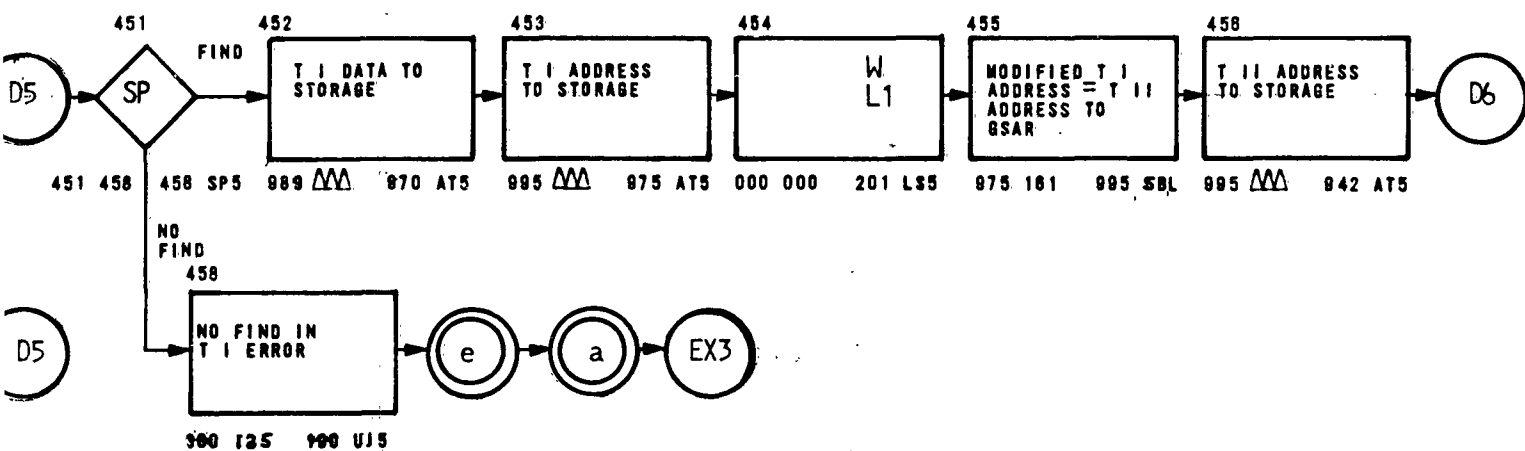
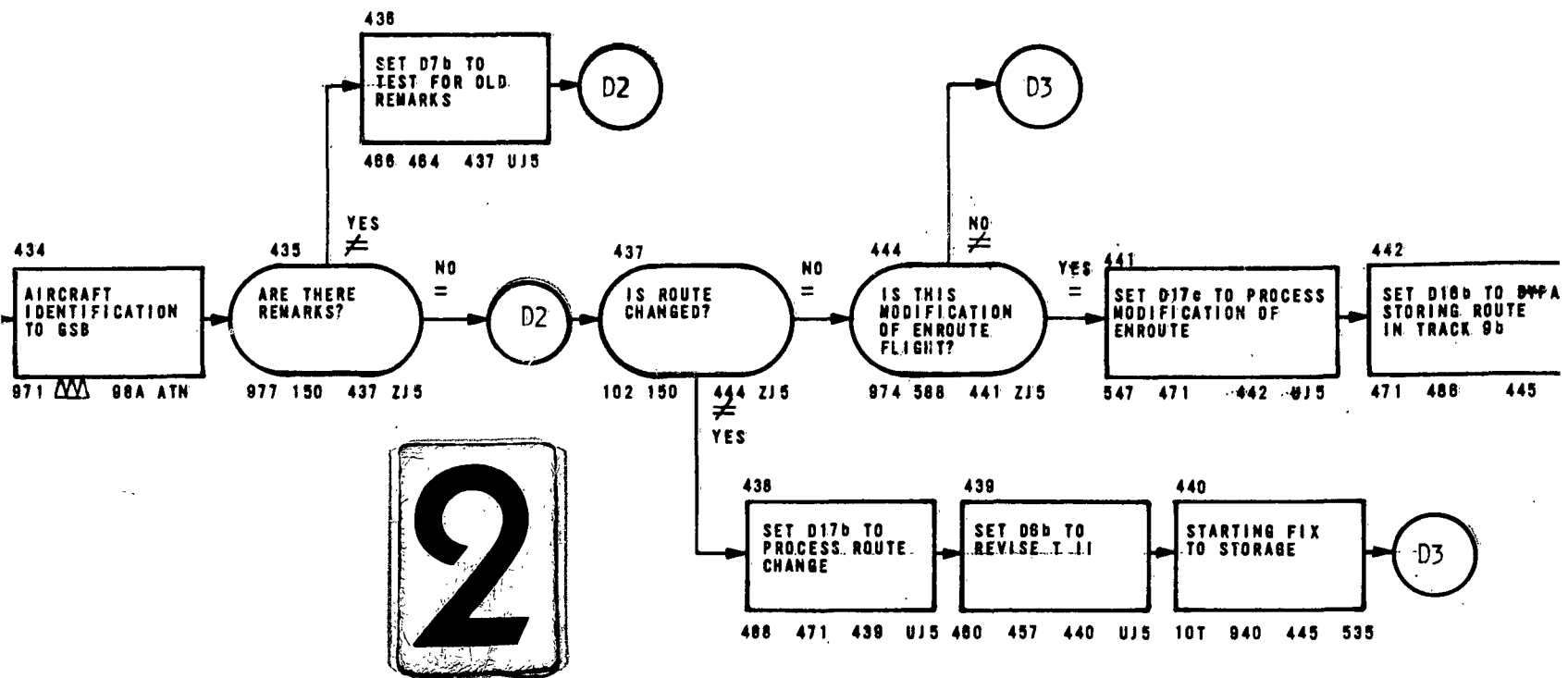
1	5	0		Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	COMPARATOR
4	3	0		Δ	Δ	Δ	Δ	A	A	A	A	A	?	@	%	OUTPUT CONSTANT
4	3	1		i	i	i	i	i	i	i	i	i	i	M	i	COMPARATOR
4	3	2		P	i	i	i	i	i	i	i	i	i	i	i	COMPARATOR
4	3	3		Δ	Δ	Δ	Δ	Δ	Δ	Z	A	C	Δ	E	Δ	OUTPUT CONSTANT
4	3	4		Δ	Δ	Δ	Δ	Δ	Δ	Z	A	C	Δ	N	Δ	OUTPUT CONSTANT
4	3	5		Δ	Δ	Δ	Δ	Δ	Δ	Z	A	C	Δ	M	Δ	OUTPUT CONSTANT
4	3	6		i	i	i	i	i	i	i	i	i	.	.	i	"PAD" FILLER
4	3	7		i	i	i	i	i	"PAD" FILLER
4	3	8		?	@	\$	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	"REMARKS" OUTPUT CONSTANT
4	4	9		Δ	Δ	Δ	Δ	Δ	Δ	Z	A	C	Δ	X	Δ	OUTPUT CONSTANT
4	4	0		/	?	r	I	Δ	C	Δ	M	Δ	E	r		ERROR CONSTANT

ON GENERAL STORAGE DRUM

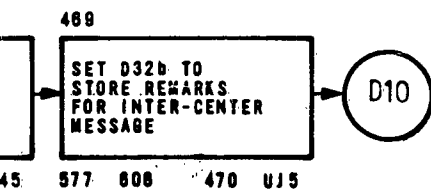
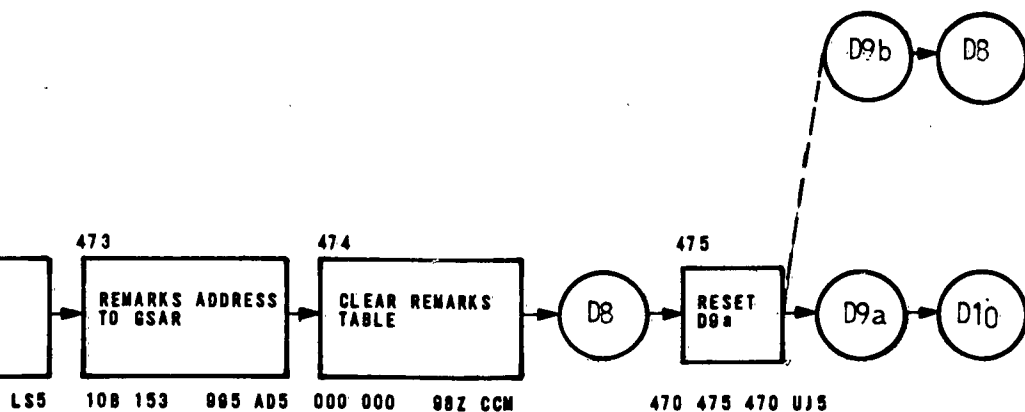
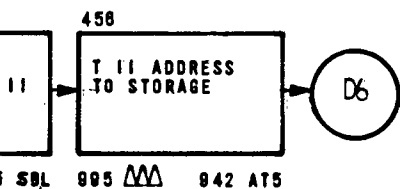
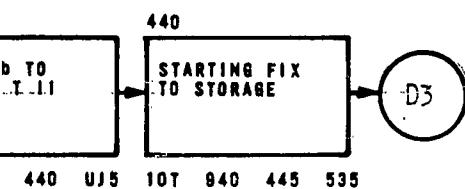
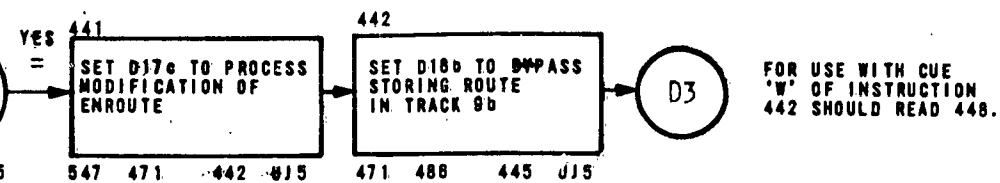
9	8	9		/	?	r	I	Δ	C	Δ	M	Δ	E	r		CONSTANT FOR I36
9	8	7		0	3	0	4	4	3	4	4	1	T	D	5	} CONSTANTS FOR I35
9	8	8		1	1	4	1	4	0	9	8	0	7	5	0	
9	8	9		0	3	0	4	4	2	5	2	2	T	D	5	
9	8	8		1	1	1	3	8	0	9	8	0	7	5	0	} CONSTANTS FOR I40
9	8	9		0	3	0	4	4	2	4	4	1	T	D	7	
9	8	8		r	r		Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	} CONSTANTS FOR I34
9	8	9		r	r	/	?	M	Δ	E	Δ	Δ	O	U	T	
9	8	8		r	?	0	N	E	Δ	I	C	M	E	r		} CONSTANTS FOR I25
9	8	9		i	i	i	i	i	i	i	i	i	?	@	%	

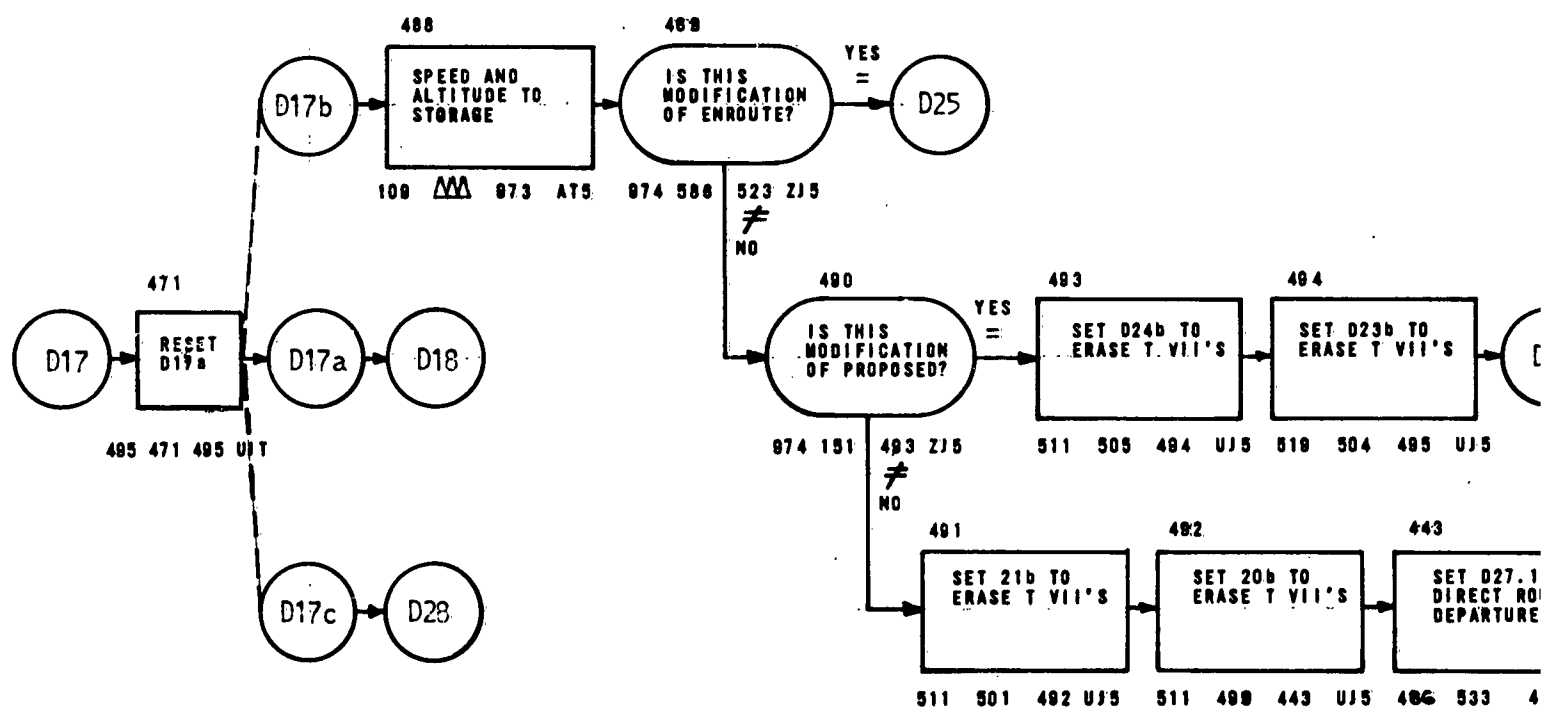
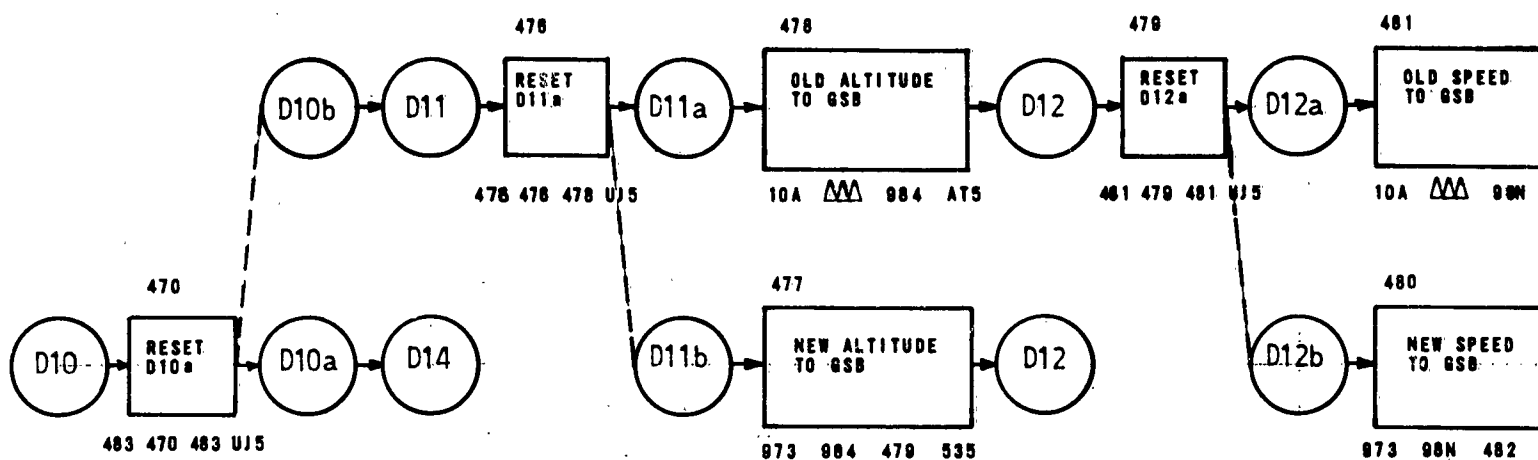
DEPARTURE/MODIFICATION ROUTINE

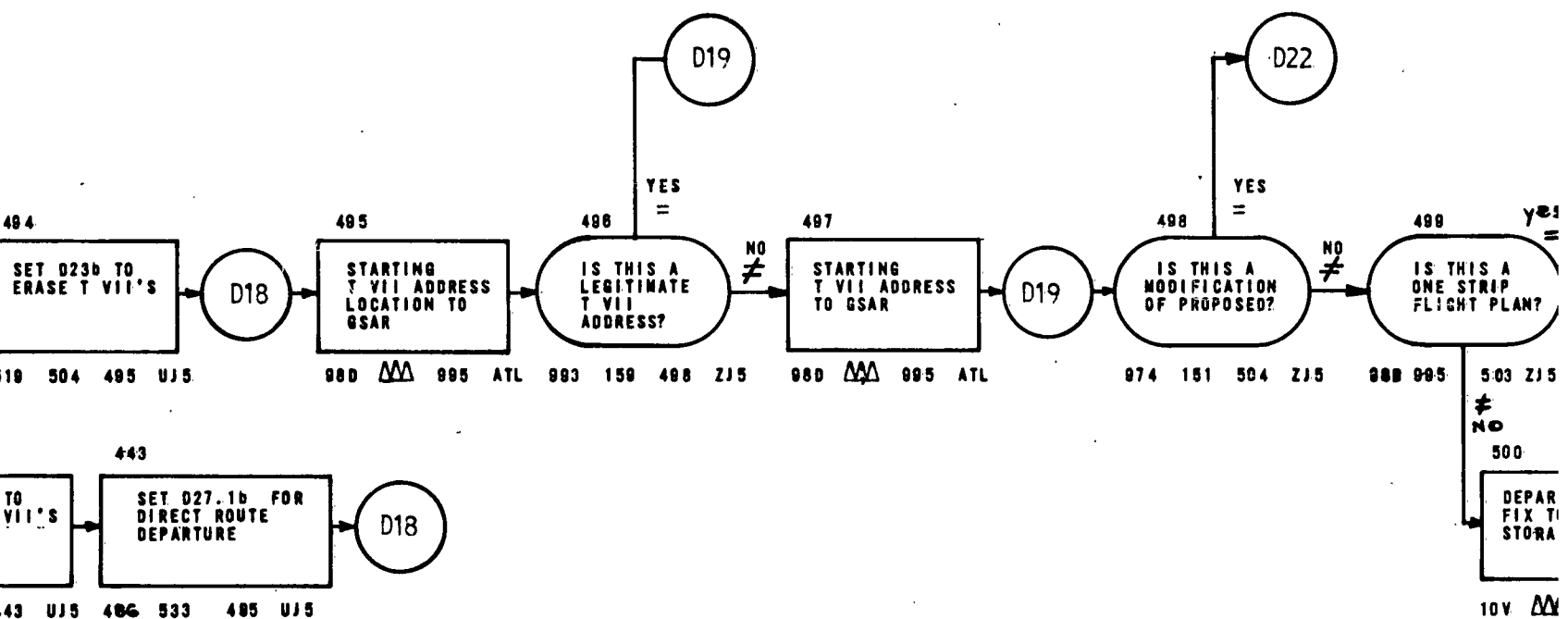
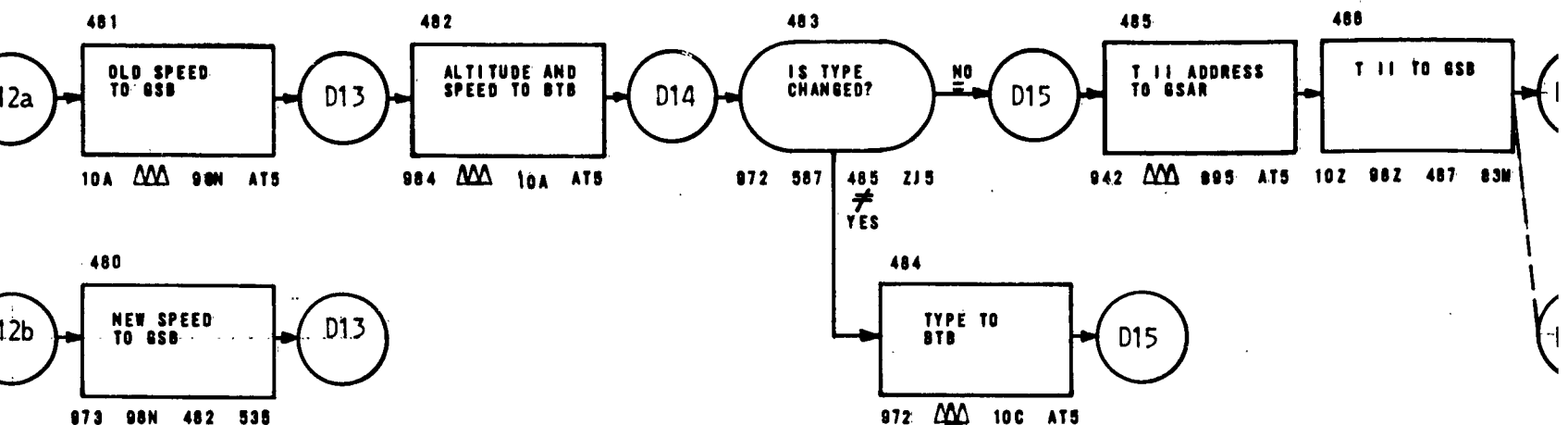




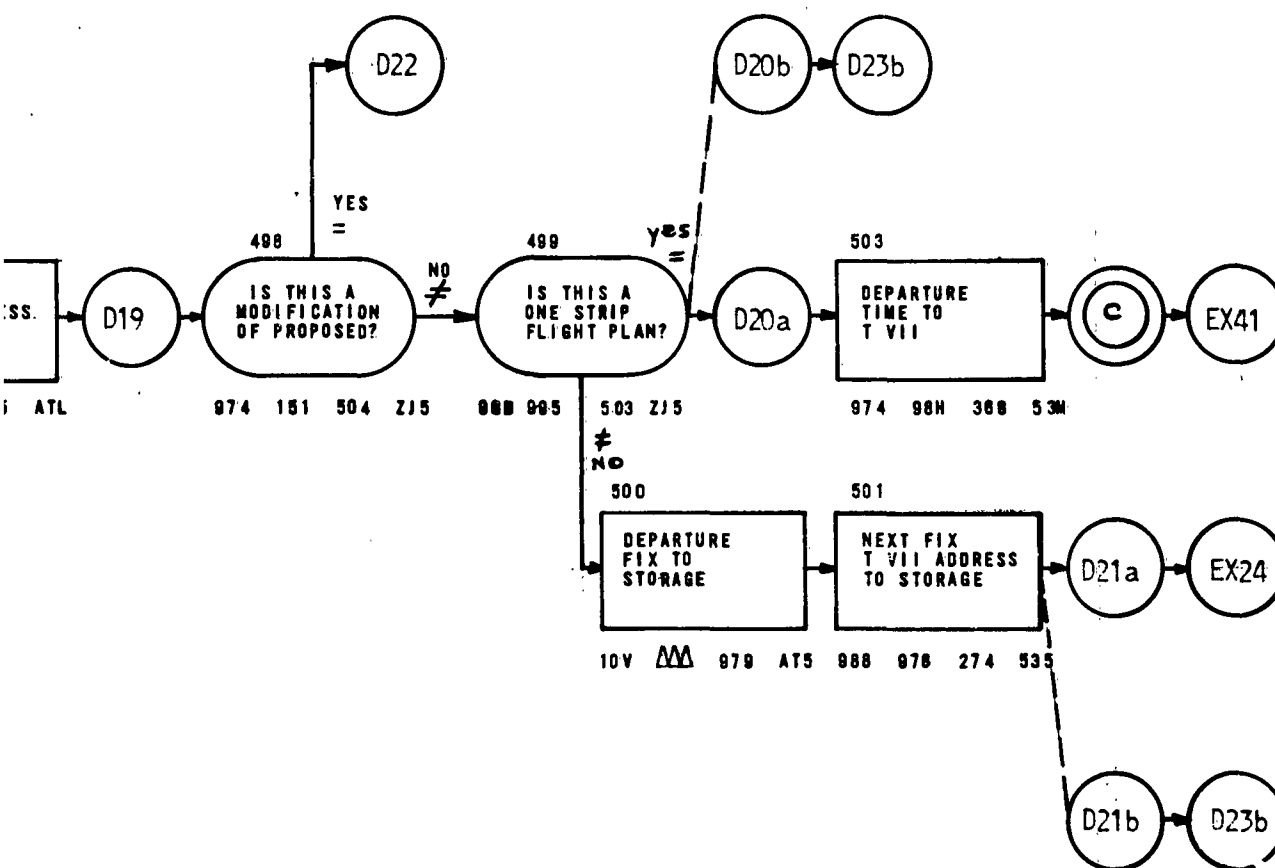
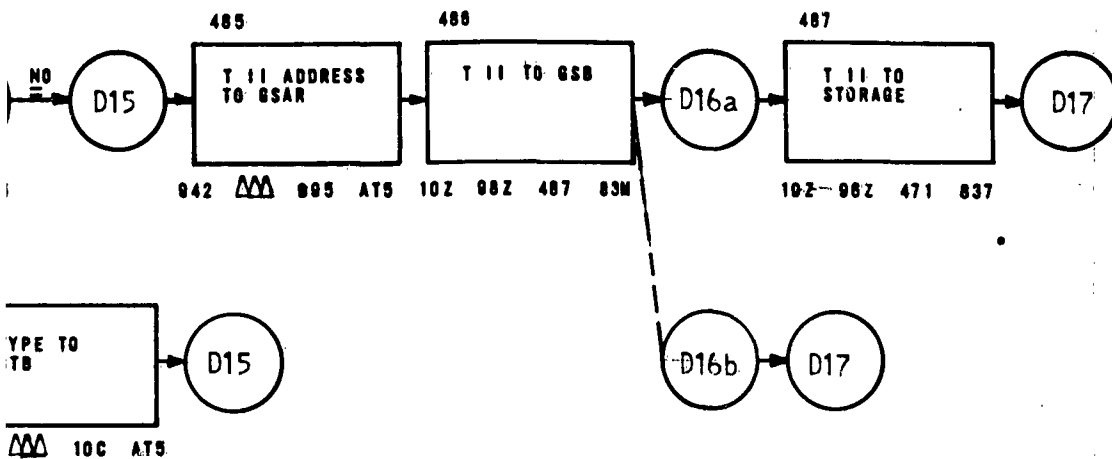
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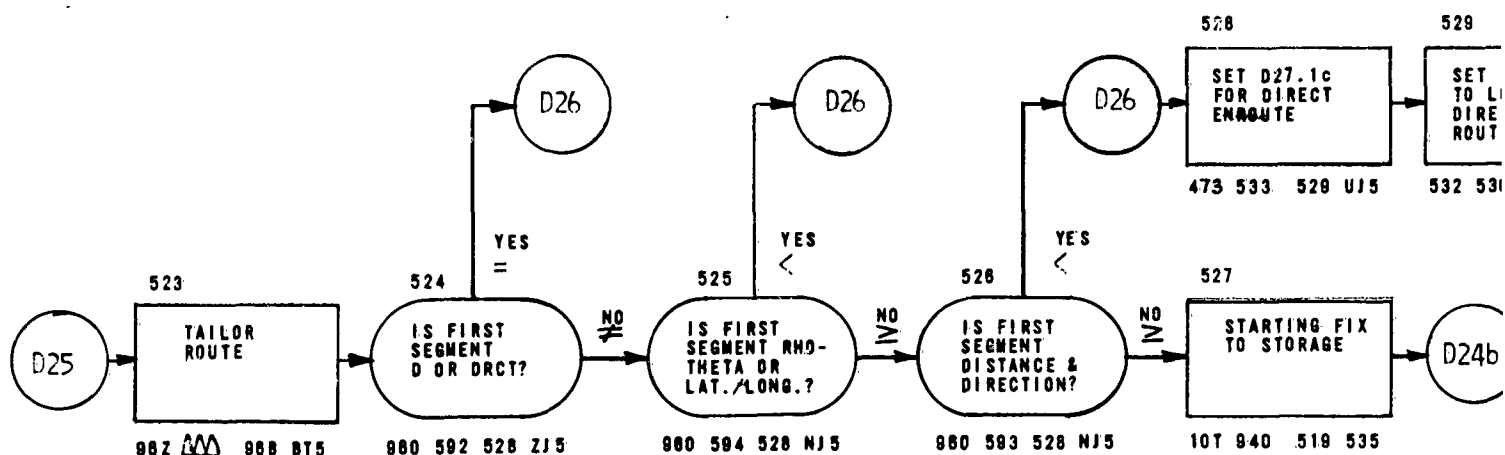
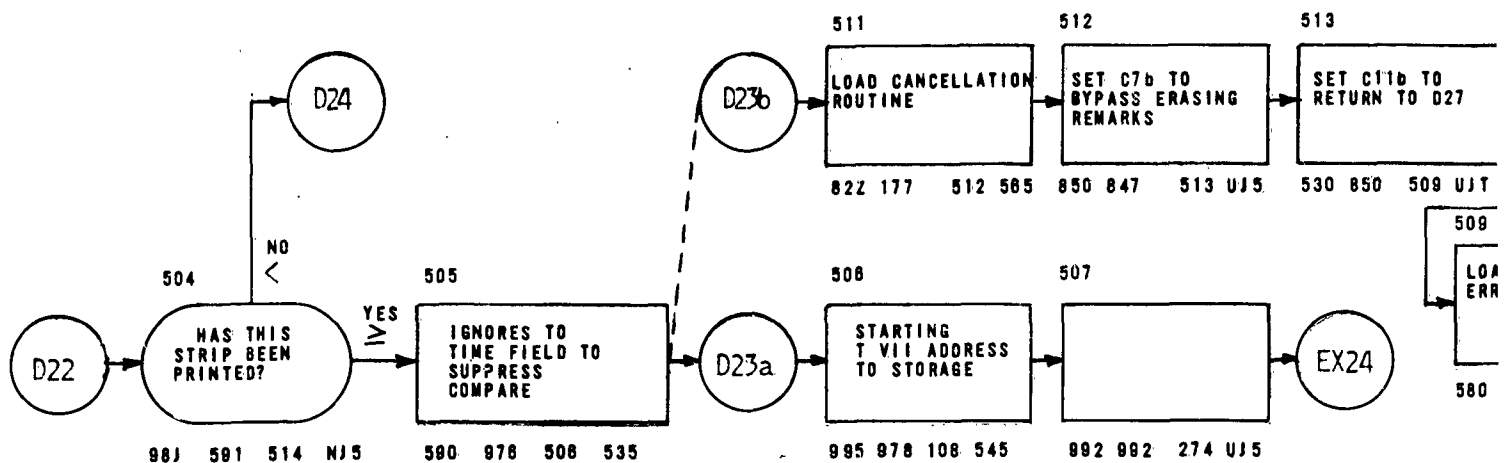




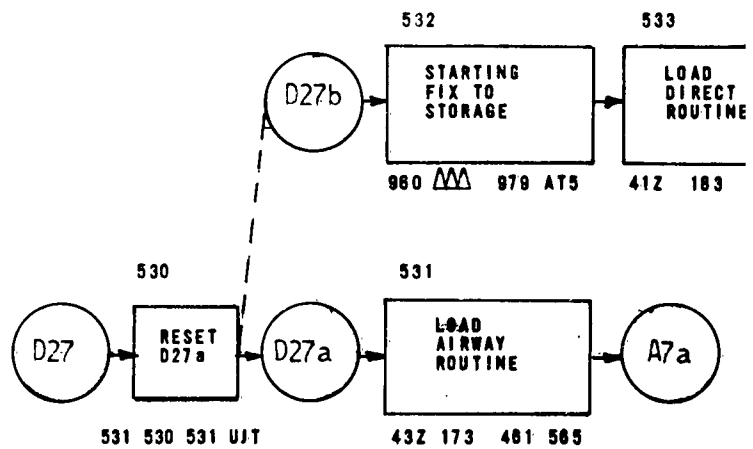
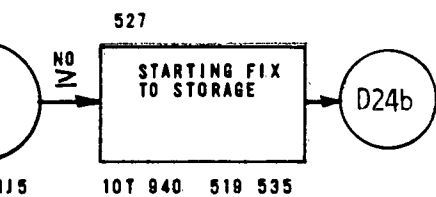
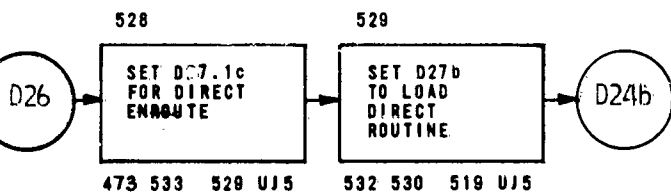
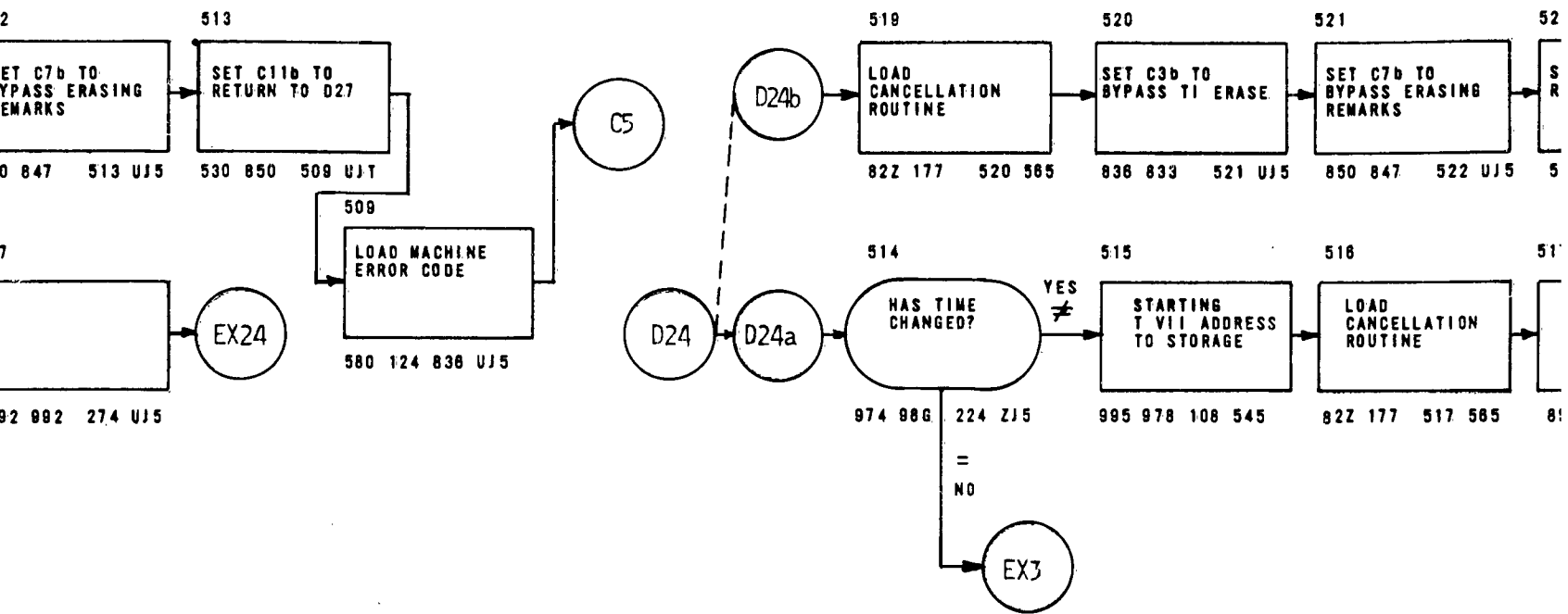


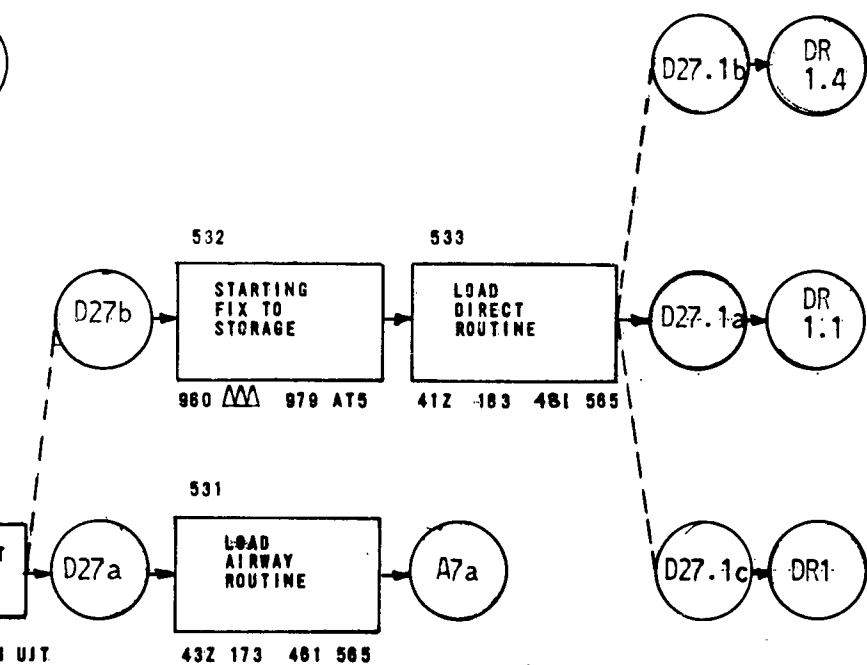
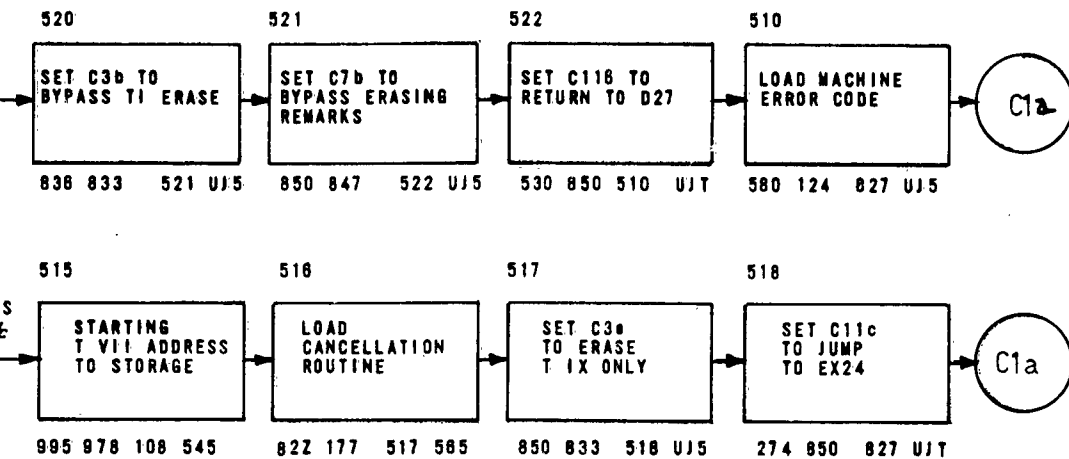
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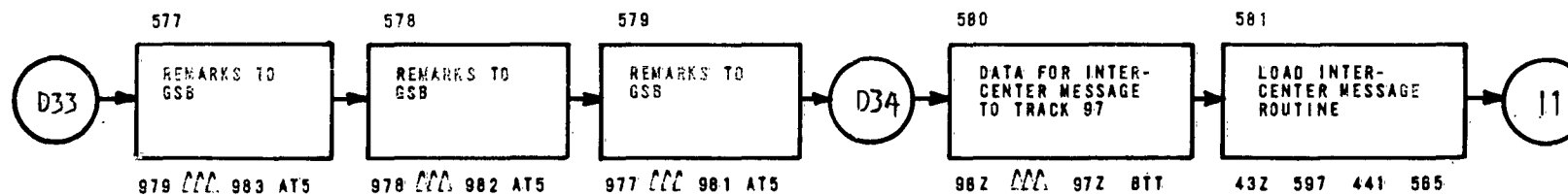
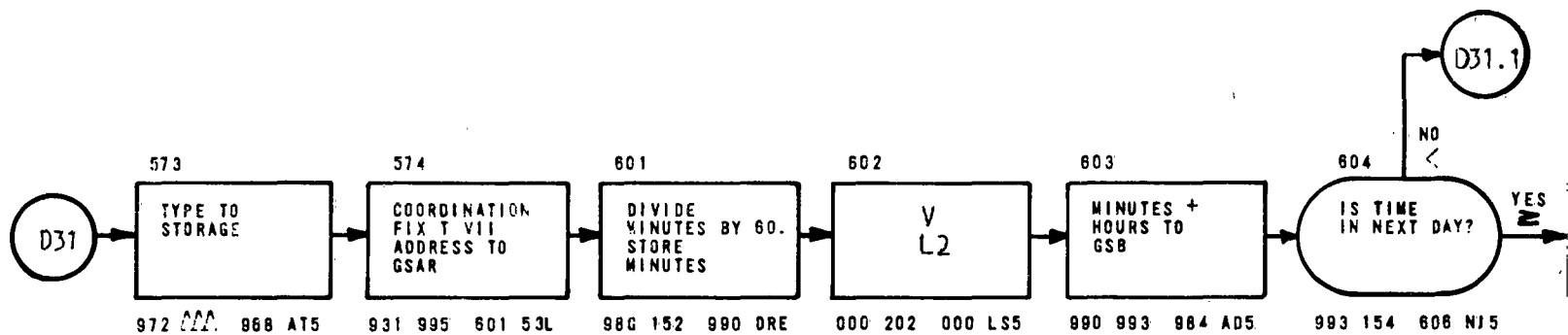
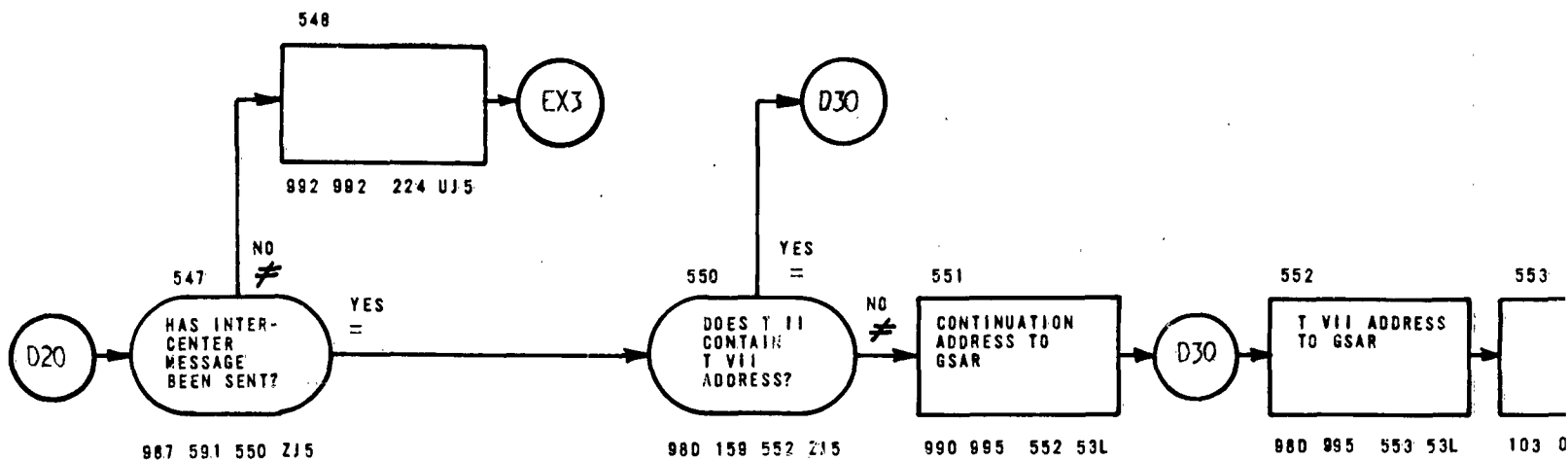




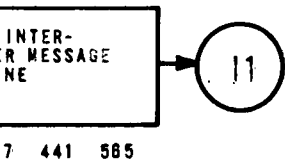
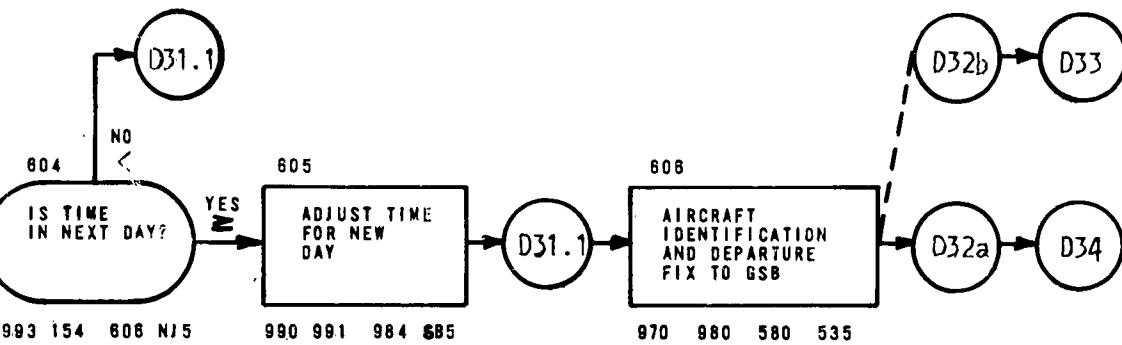
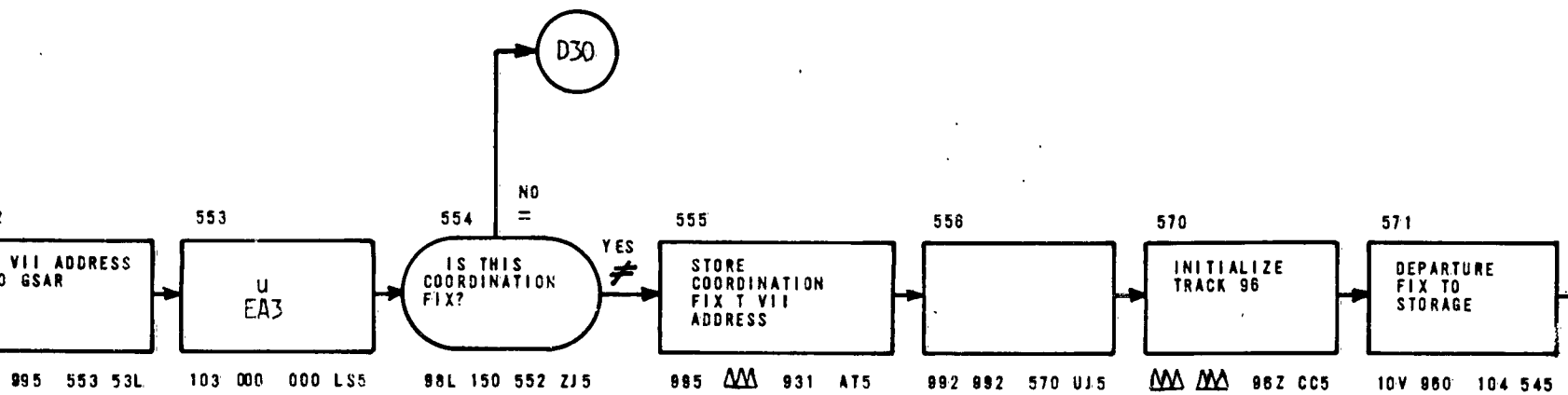
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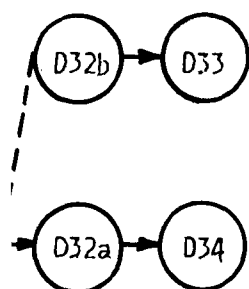
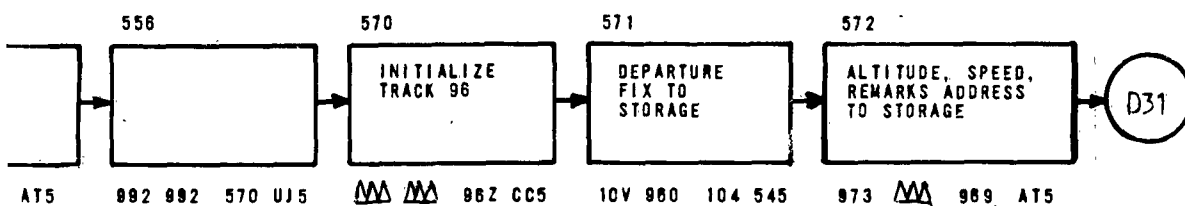


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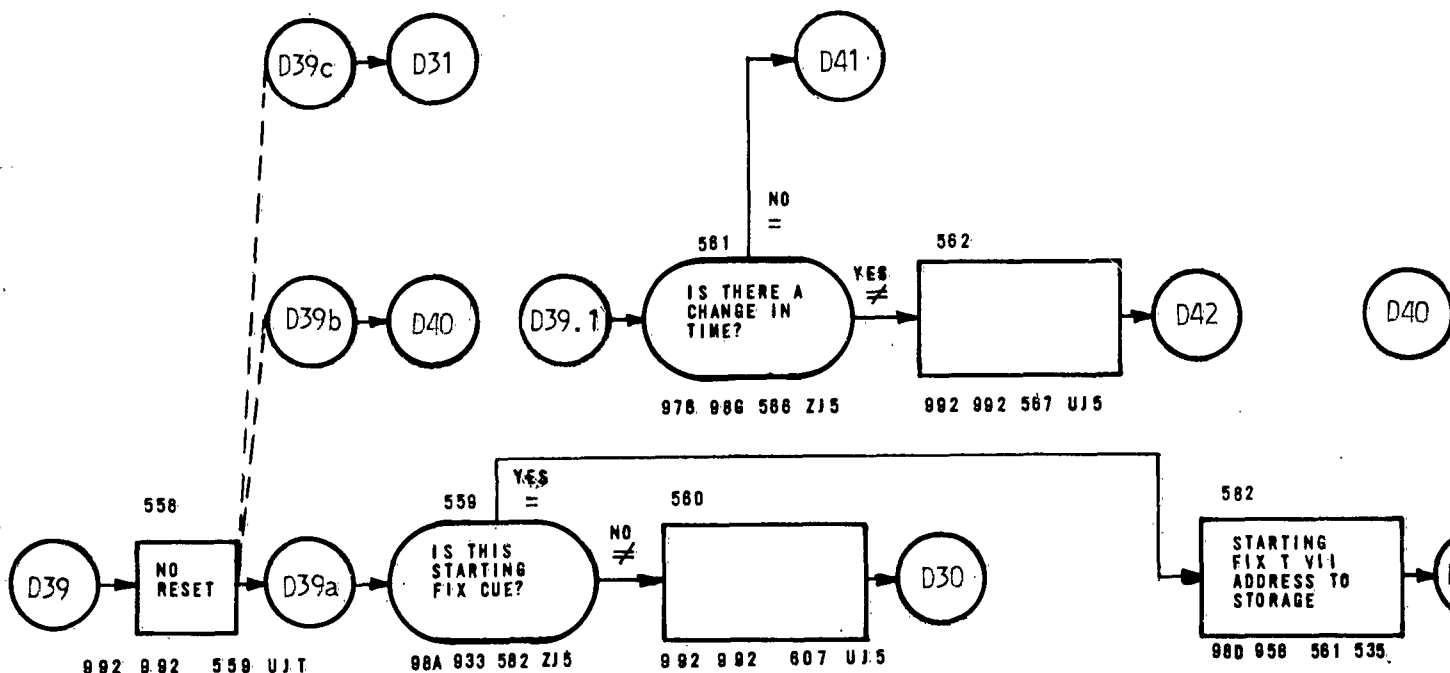
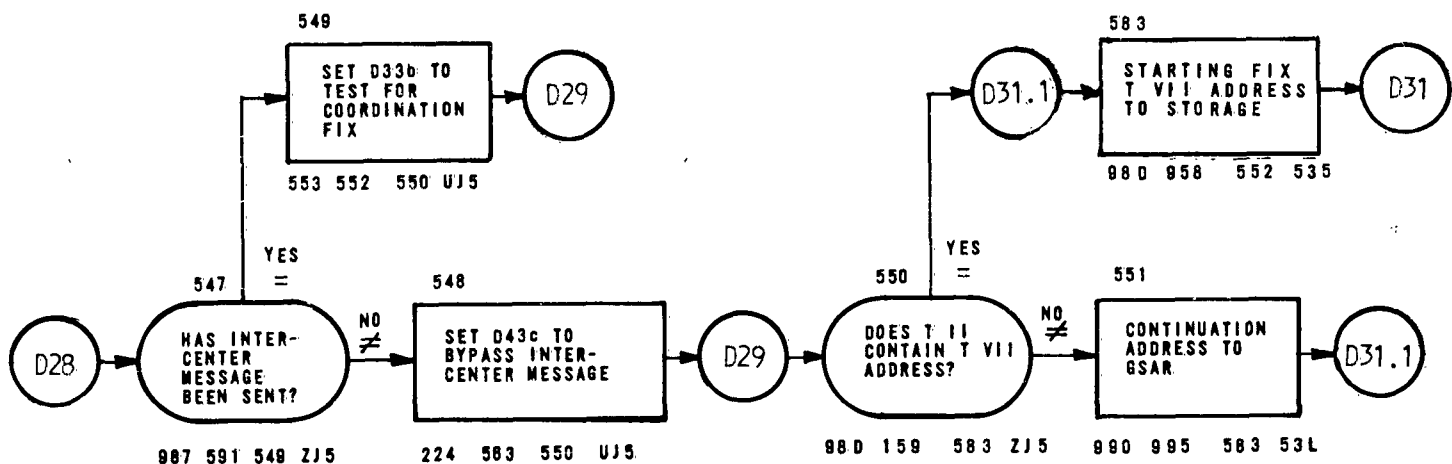
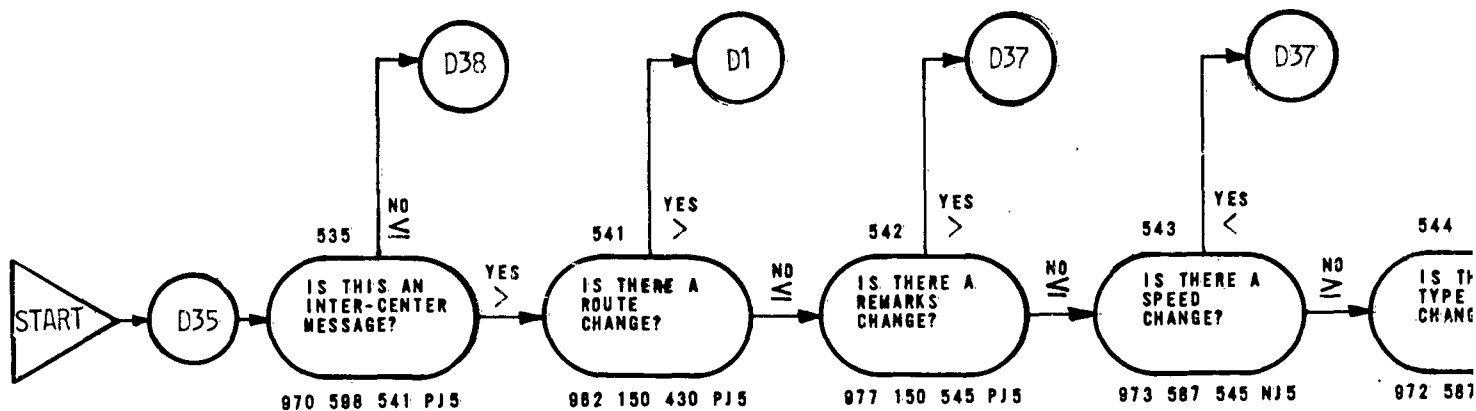
NO



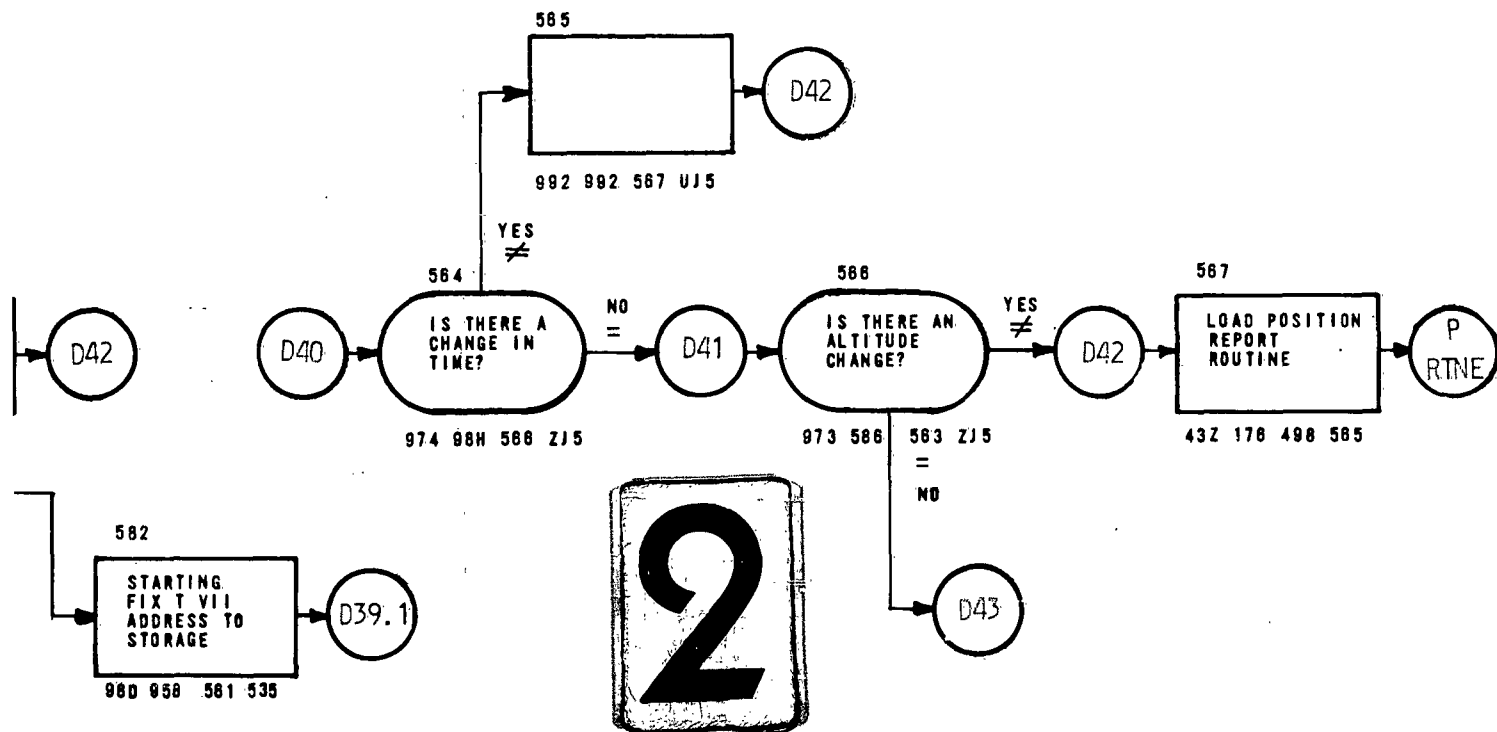
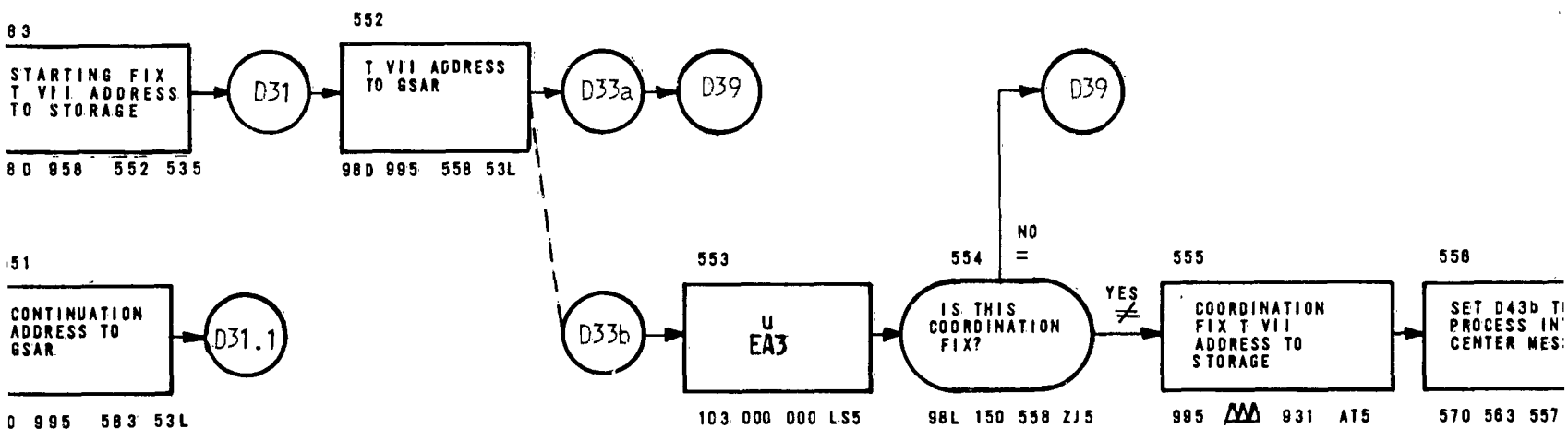
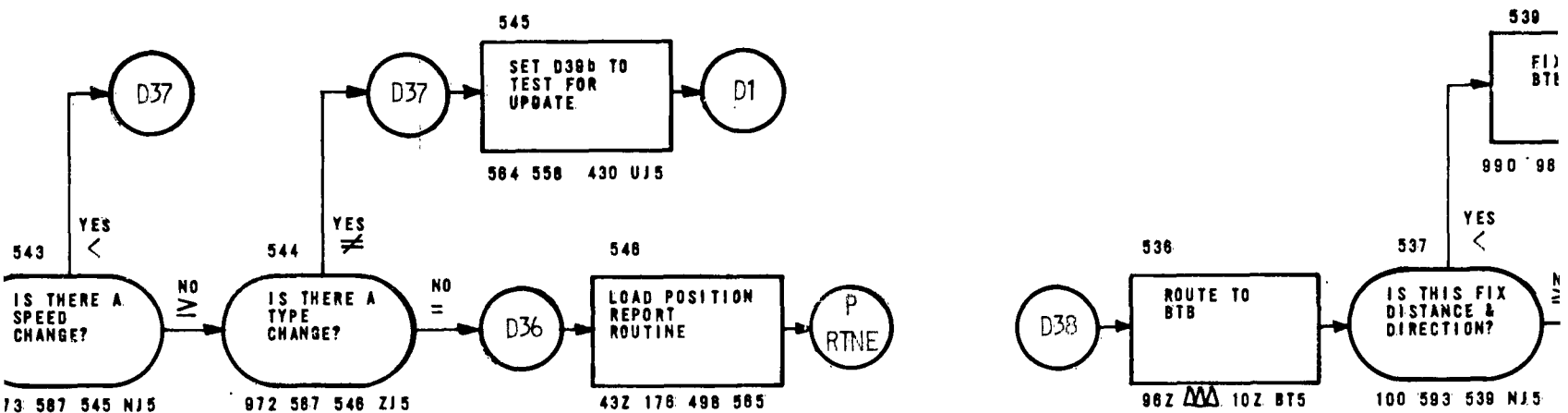


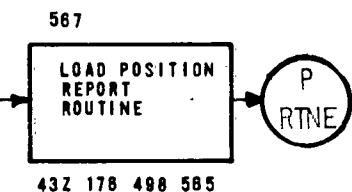
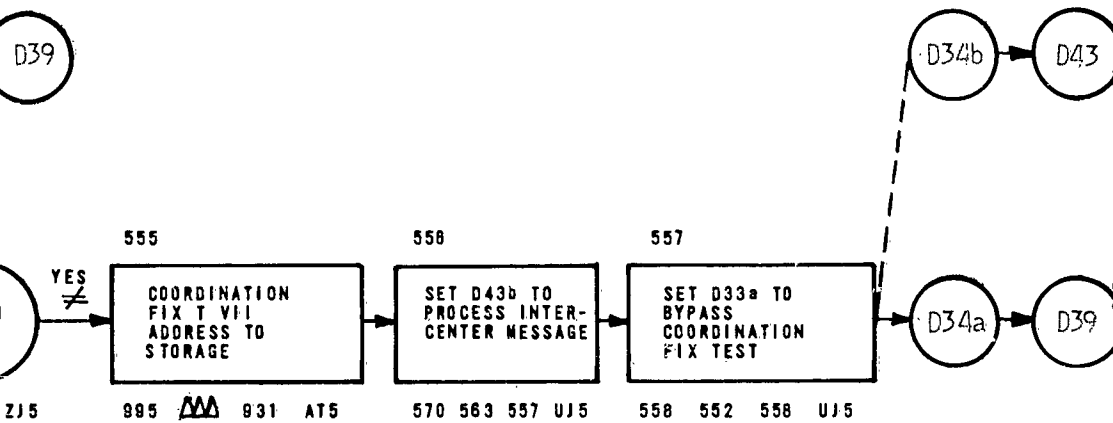
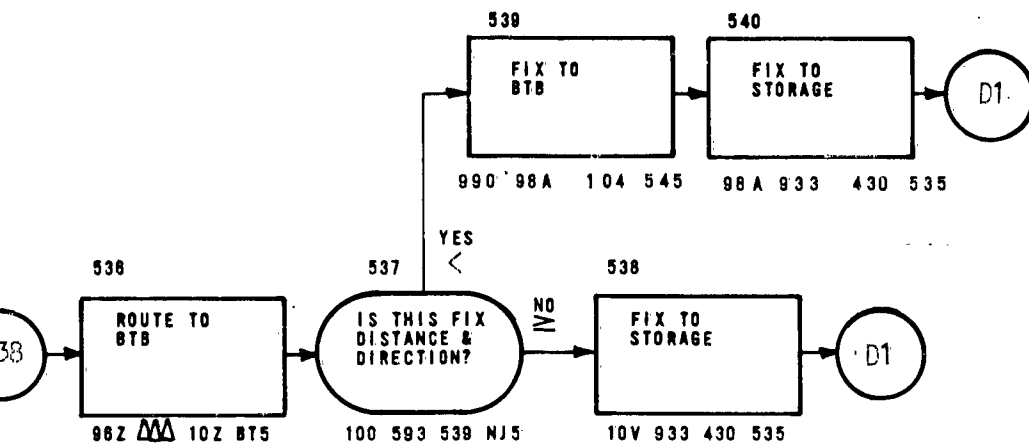
NOTE: FOR USE WITH UPDATE AND POSITION
REPORT ROUTINES, PAGES 5 & 8
REPLACE PAGE 4 . CODING
AND CONNECTORS ON THESE PAGES
ARE COMPATIBLE.





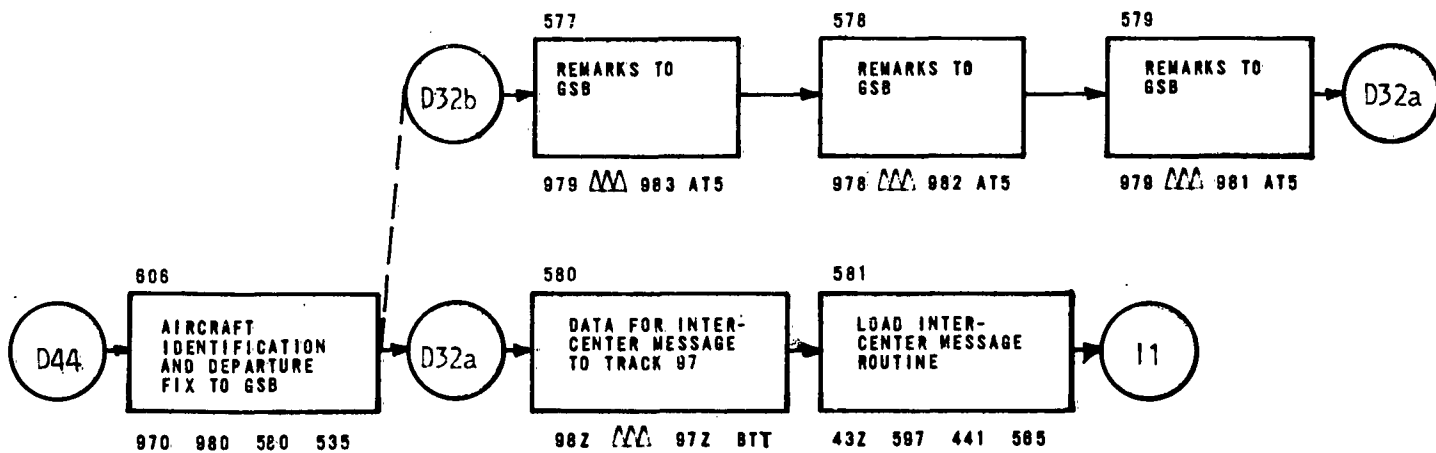
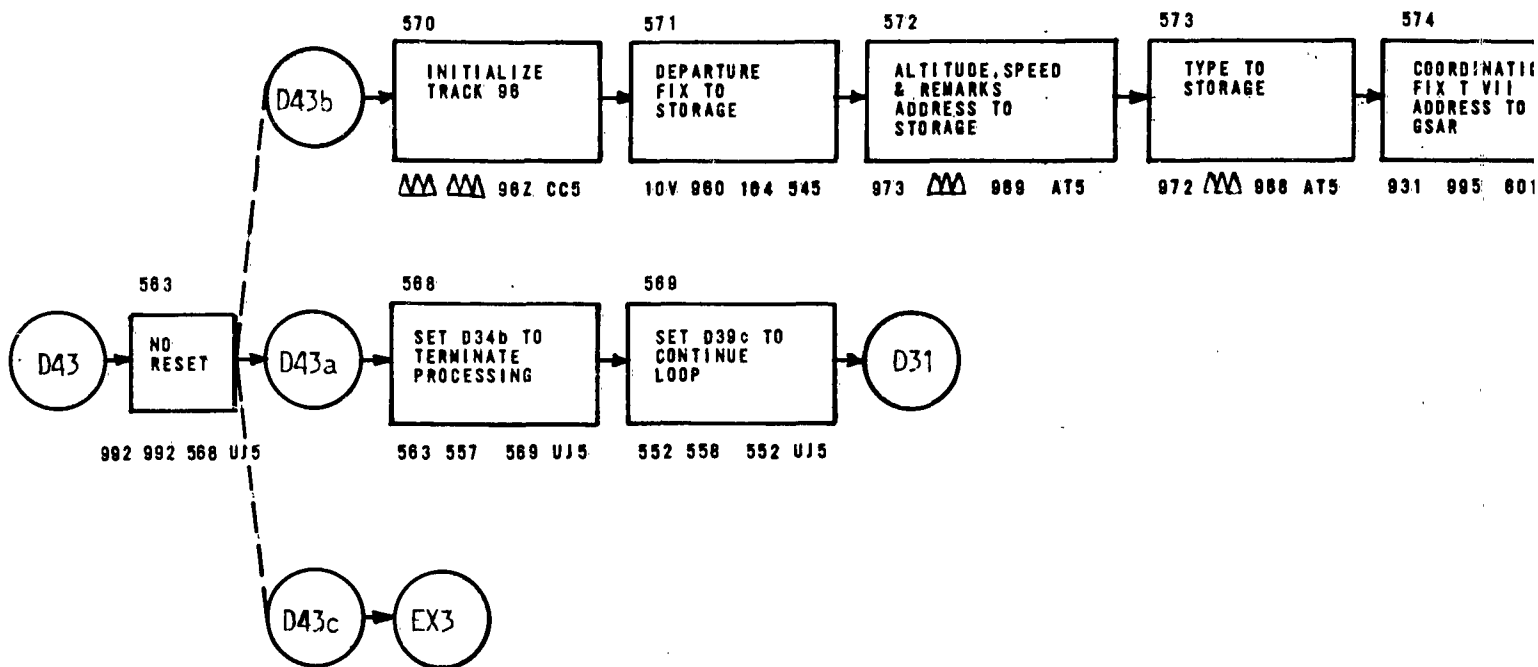
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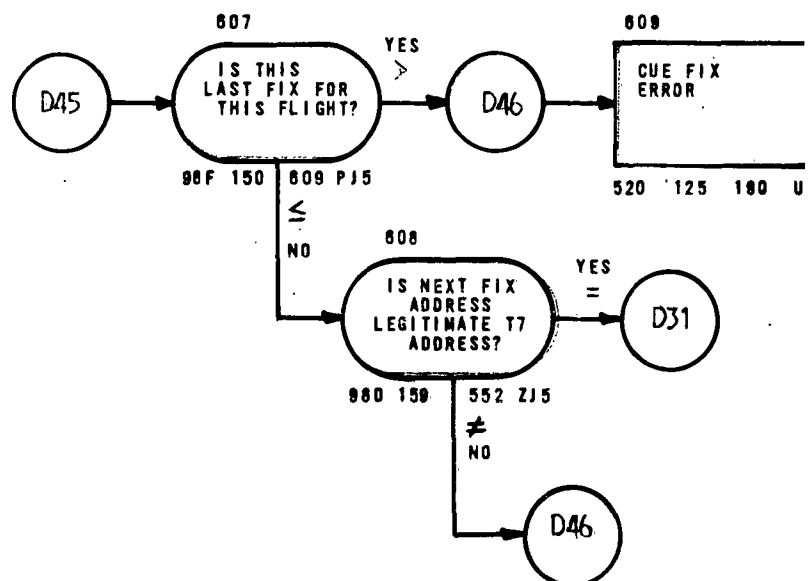
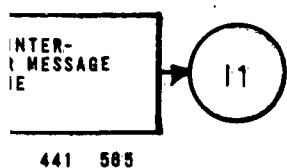
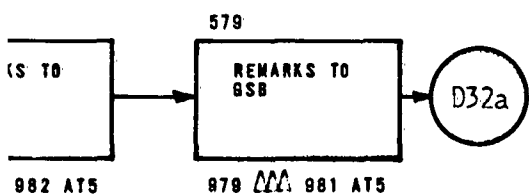
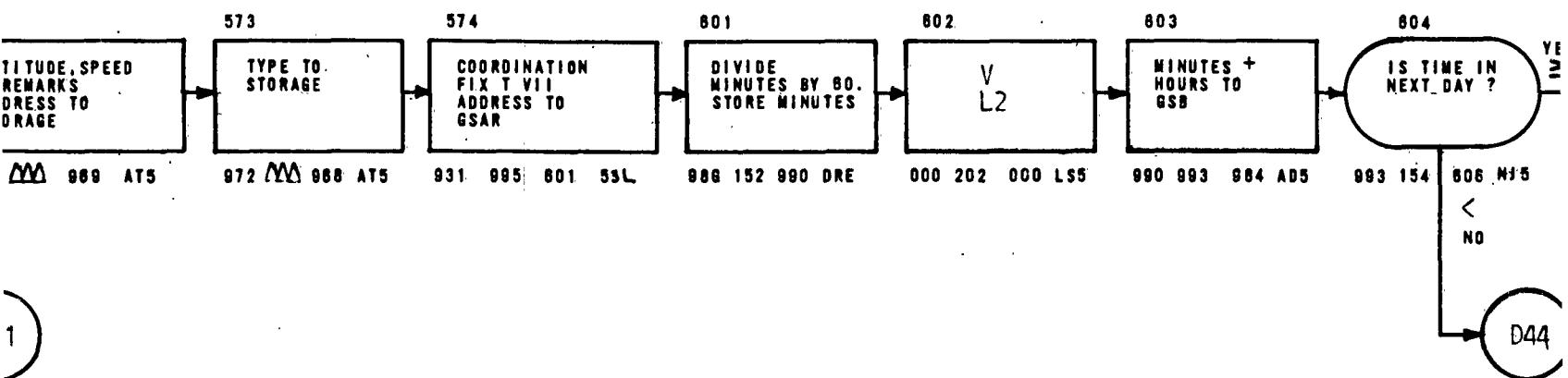


PAGES 5 & 8 REPLACE PAGE 4 WHEN
UPDATING ROUTINES ARE IN USE.

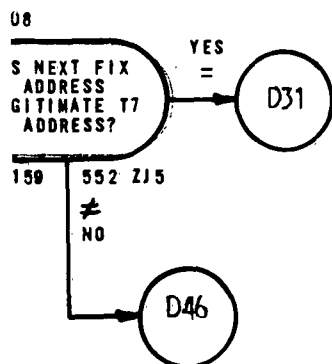
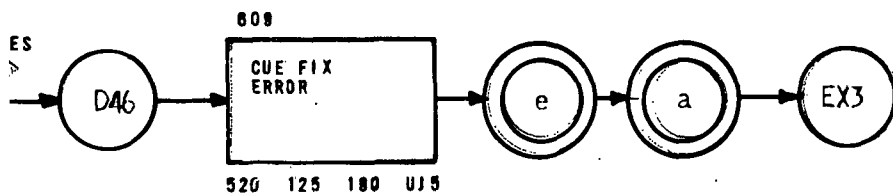
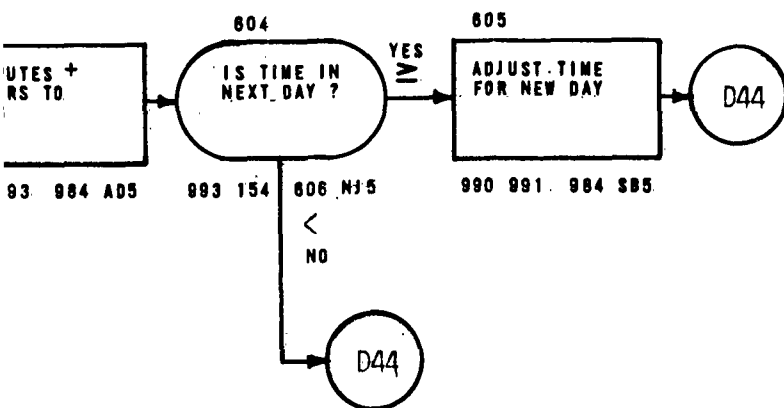




1



2



PAGES 5 & 6 REPLACE PAGE 4 FOR
OPERATION WITH UPDATING ROUTINES.



WORKING STORAGE LOCATIONS

931	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	COORDINATION FIX T VII ADDRESS.
933	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	X	X	X	X	ROUTE CHANGE STARTING FIX.
940	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	X	X	X	X	FLIGHT PLAN STARTING FIX.
942	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	T II ADDRESS.
958	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	STARTING T VII ADDRESS FOR

UPDATE.

NOTE 1: THE REMARKS BESIDE THE CONSTANTS, FACTORS AND WORKING STORAGES DEFINE THE PRIME PURPOSE AS USED IN THIS ROUTINE. THE FLOW CHARTS WILL DEFINE ANY SECONDARY USAGE OF THESE LOCATIONS.

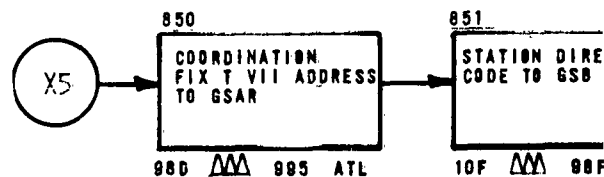
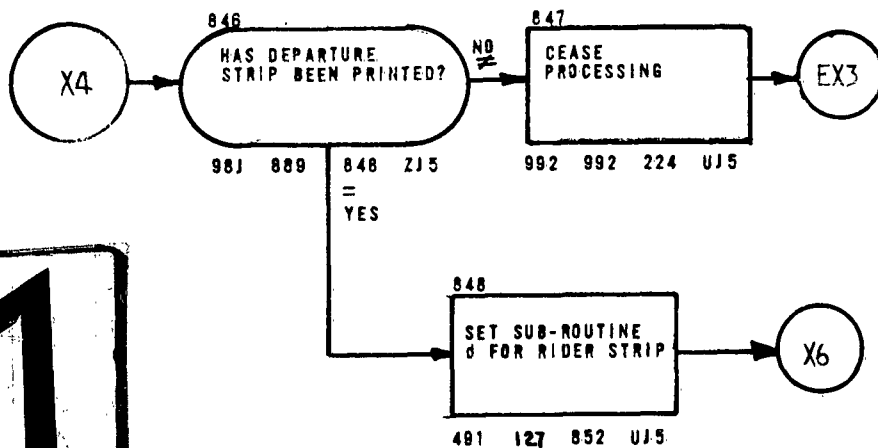
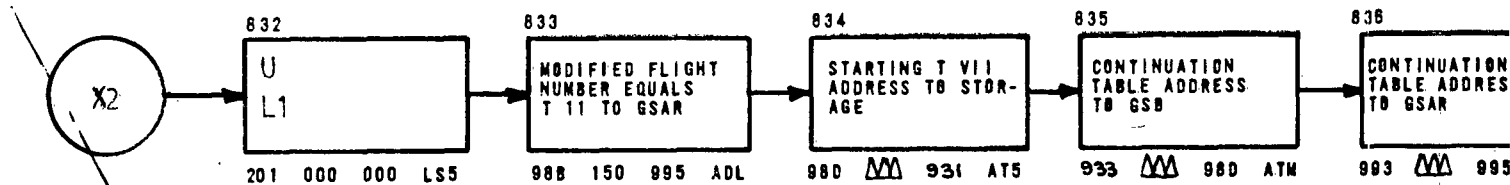
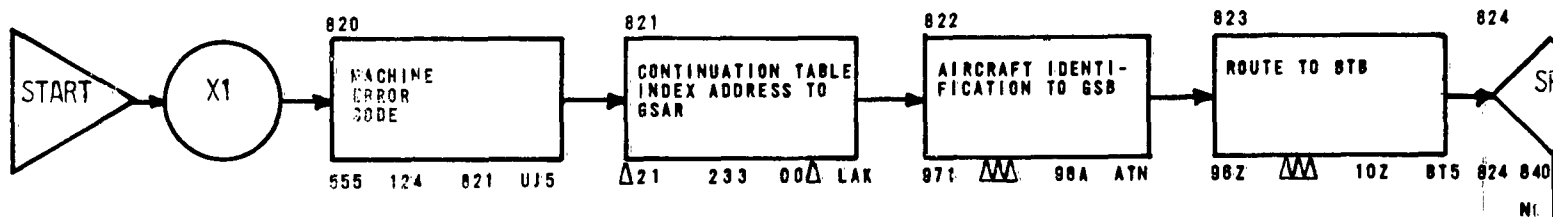
NOTE 2: LOCATIONS 176, 931, 933 and 958 PERTAIN TO THE UPDATING AND POSITION REPORT ROUTINES.

CONSTANTS AND FACTORS

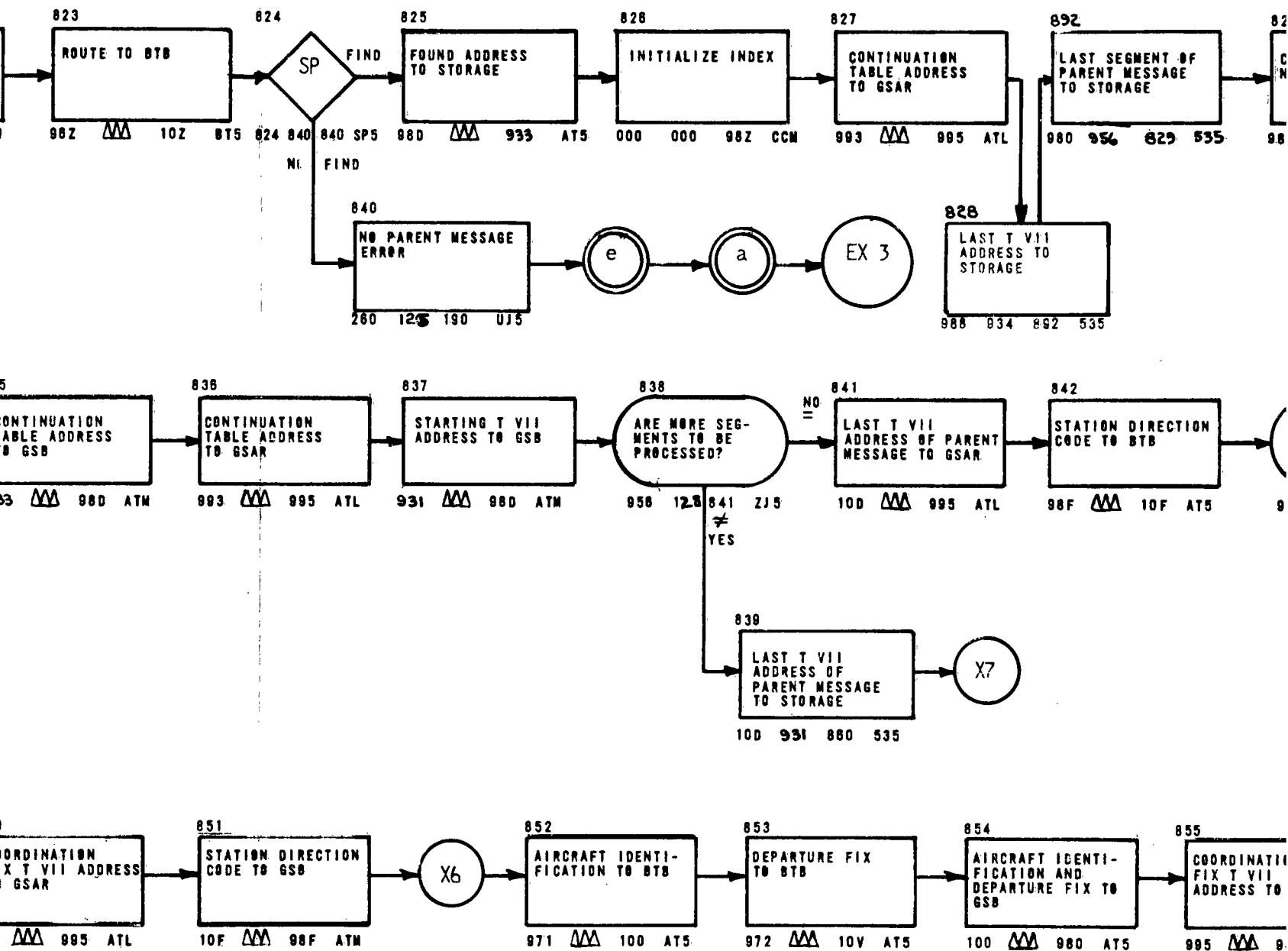
150	▲	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	SPACES COMPARATOR
151	i	i	i	i	i	i	P	i	i	i	i	i	PROPOSED COMPARATOR
152	∅	∅	∅	∅	∅	∅	∅	∅	∅	6	∅	Δ	TIME CONVERSION FACTOR
153	∅	∅	∅	∅	3	1	2	∅	∅	∅	∅	∅	REMARKS TABLE MODIFIER
154	Δ	Δ	Δ	Δ	Δ	Δ	Δ	2	4	∅	∅	Δ	TIME ADJUSTMENT CONSTANT
159	i	i	i	i	5	0	i	i	i	i	i	i	T VII ADDRESS COMPARATOR
161	∅	∅	∅	∅	1	∅	1	∅	∅	∅	∅	∅	T I ADDRESS MODIFIER
173	6	8	Z	Δ	∅	1	1	2	∅	∅	∅	Δ	AIRWAY ROUTINE LOAD FACTOR
176	8	9	Z	Δ	∅	1	1	8	∅	4	∅	Δ	POSITION REPORT ROUTINE LOAD FACTOR
177	8	5	Z	Δ	∅	1	1	6	6	∅	∅	Δ	CANCELLATION AND ERASE ROUTINE LOAD FACTOR
183	8	∅	Z	Δ	∅	1	1	4	6	∅	∅	Δ	DIRECT ROUTINE LOAD FACTOR
586	i	i	i	i	i	.	.	.	i	i	i	i	PAD COMPARATOR
587	i	i	i	i	i	i	i	i	.	.	.	i	PAD COMPARATOR
588	i	i	i	i	i	i	E	i	i	i	i	i	ENROUTE COMPARATOR
590	i	i	i	i	i	i	i	i	i	i	i	i	IGNORES FACTOR
591	i	i	i	i	i	i	i	i	i	i	P	i	PRINT COMPARATOR
592	i	i	i	i	D	i	i	i	i	i	i	i	DIRECT ROUTE TEST COMPARATOR
593	i	.	i	i	i	i	i	i	i	i	i	i	DIRECT ROUTE TEST COMPARATOR
594	i	i	i	i	.	i	i	i	i	i	i	i	DIRECT ROUTE TEST COMPARATOR
597	5	1	Z	Δ	∅	1	1	4	4	∅	∅	Δ	INTER-CENTER MESSAGE ROUTINE LOAD FACTOR
598	i	i	i	i	i	i	i	.	i	i	i	i	PAD COMPARATOR

APPENDIX XIII

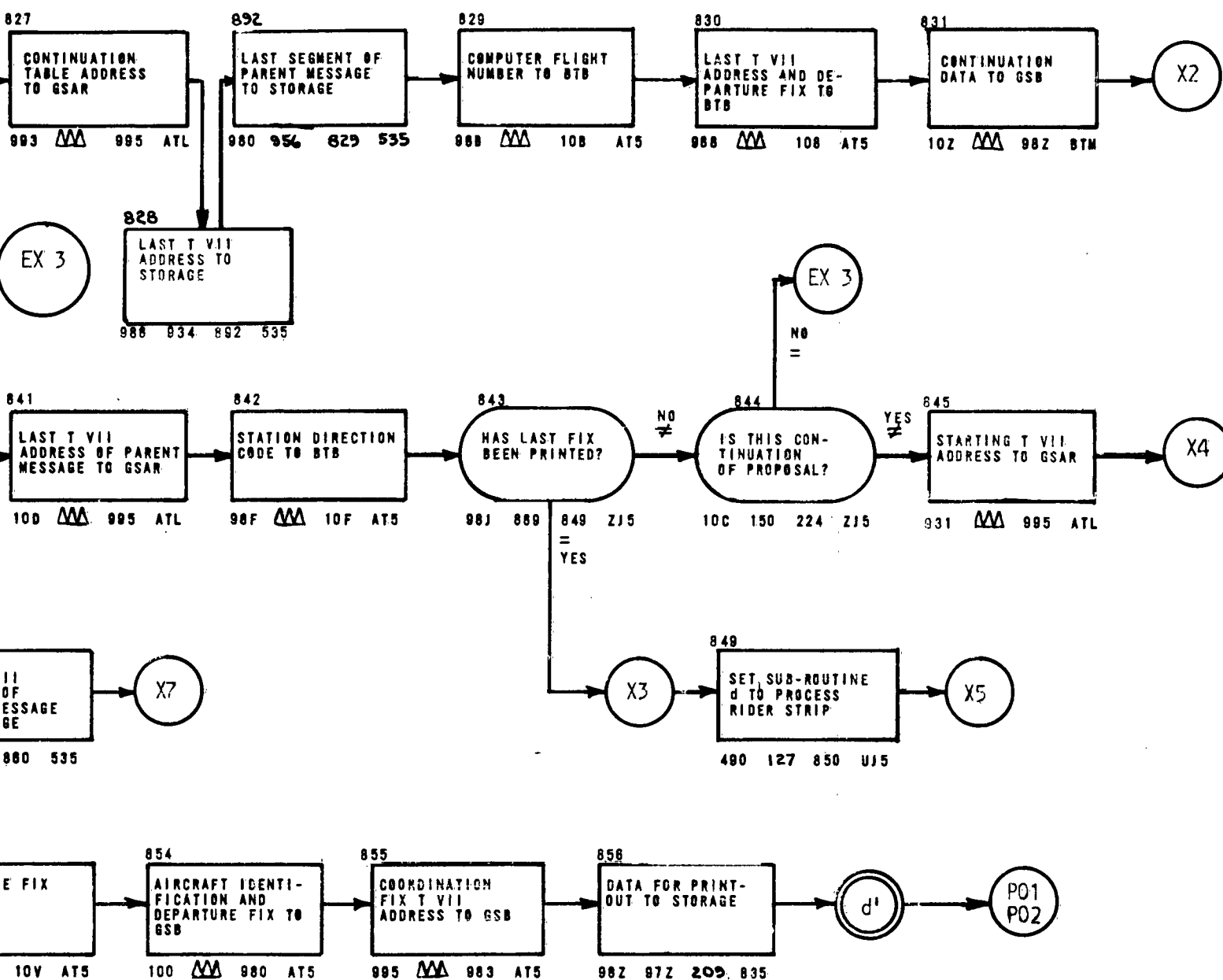
CONTINUATION ROUTINE



1

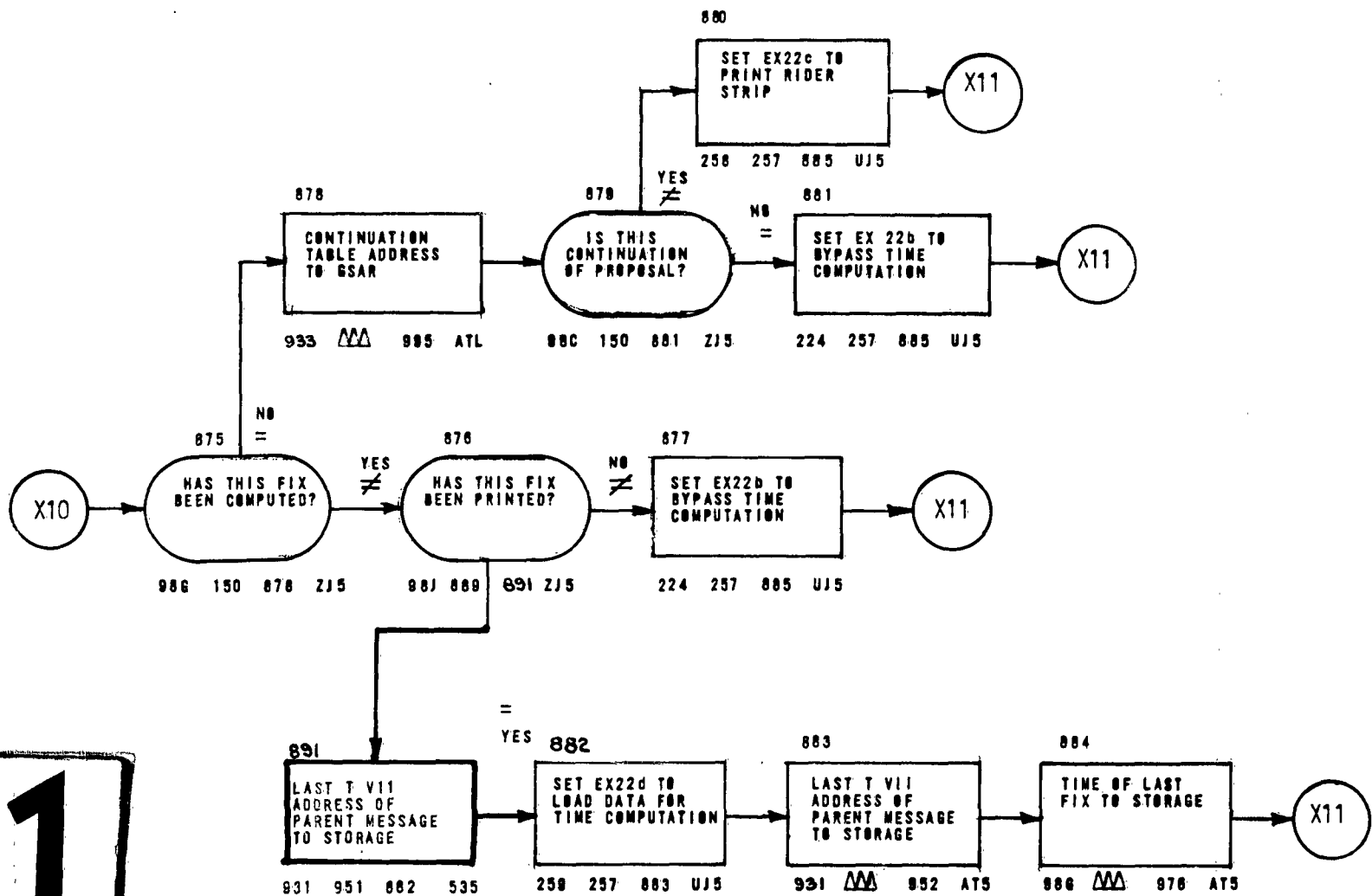
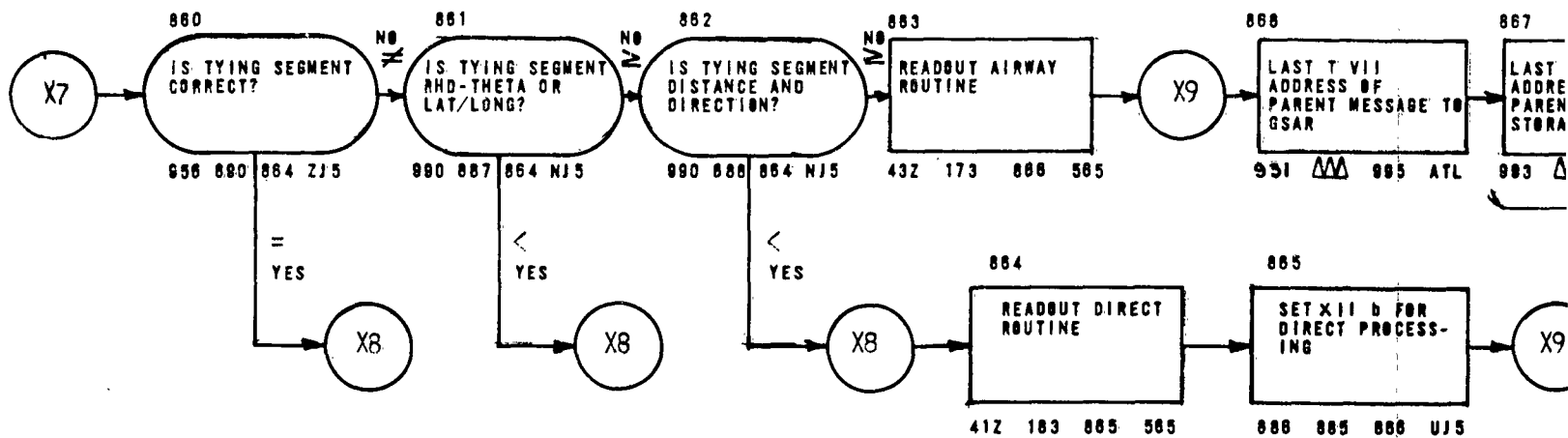


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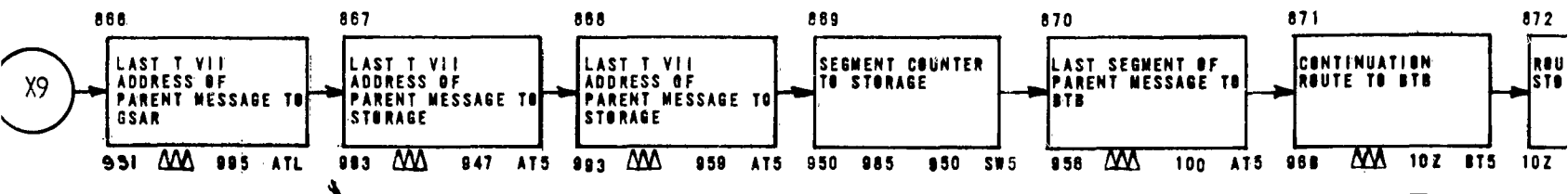


ASSEMBLY OF DATA FOR
PRINTING RIDER STRIP.

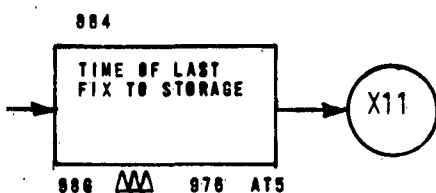
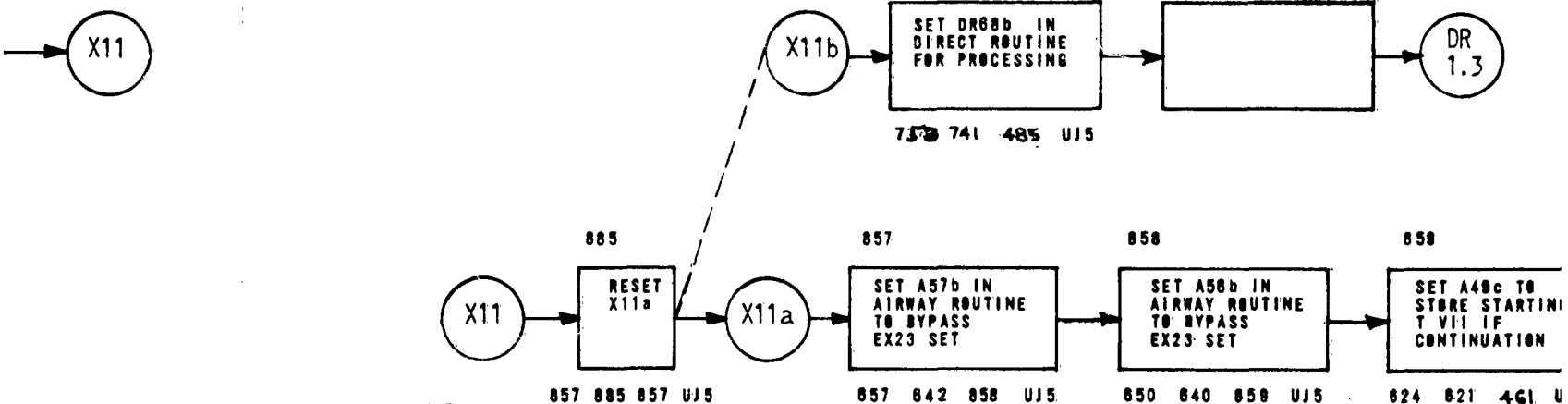
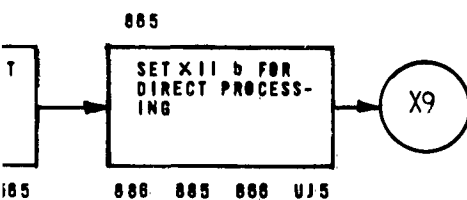
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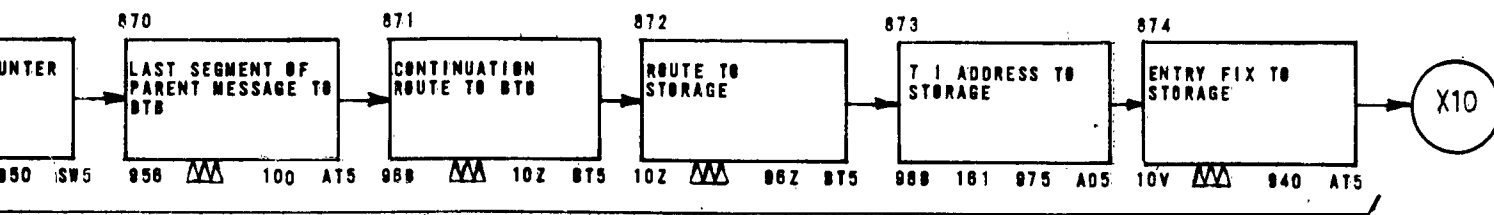


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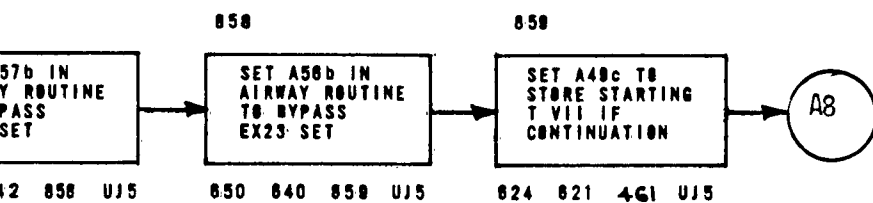
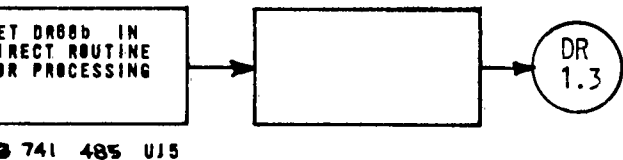


ASSEMBLY OF DATA FOR PROCESSING
ADDITIONAL SEGMENTS





ASSEMBLY OF DATA FOR PROCESSING
ADDITIONAL SEGMENTS



3

CONSTANTS AND FACTORS

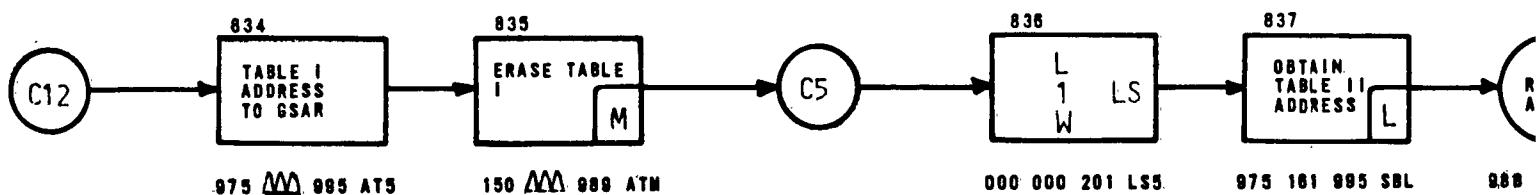
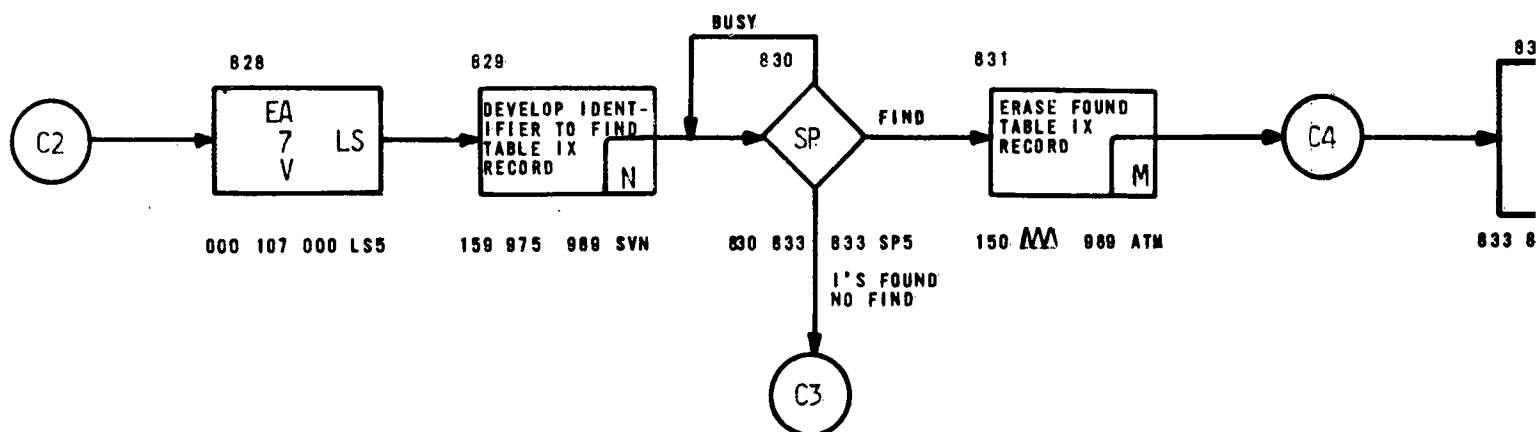
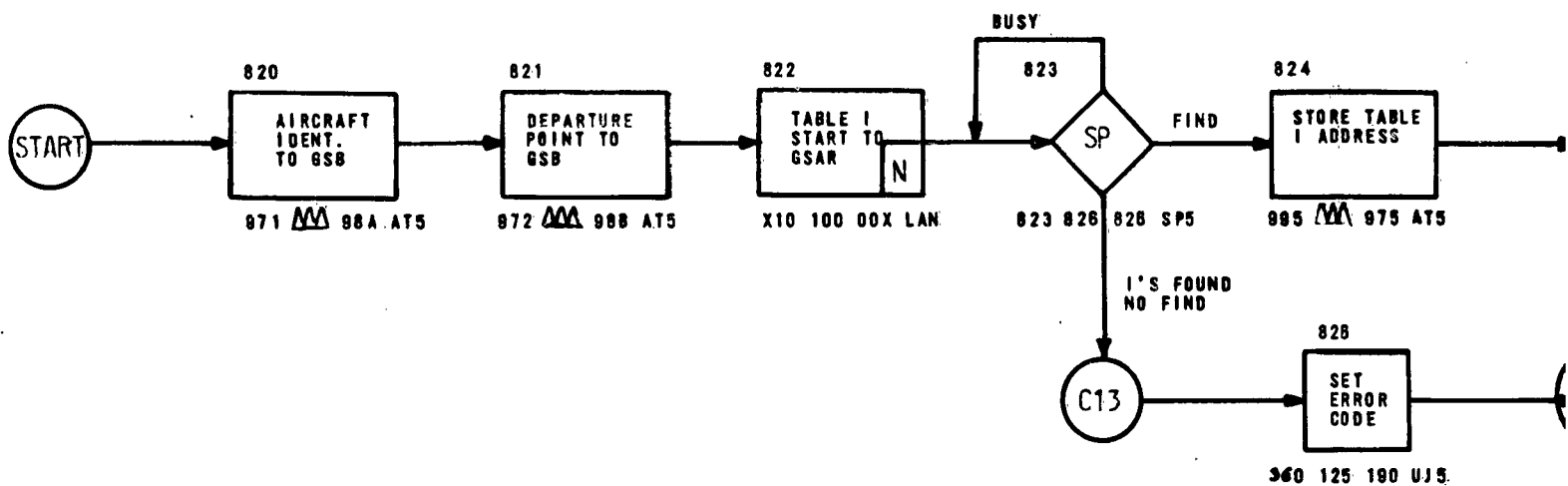
150	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	"SPACES" COMPARATOR
161	∅	∅	∅	∅	1	∅	1	∅	∅	∅	∅	Δ	TI MODIFIER
173	6	8	Z	Δ	∅	1	1	2	∅	∅	∅	Δ	AIRWAY ROUTINE LOAD FACTOR
183	8	∅	Z	Δ	∅	1	1	4	6	∅	∅	Δ	DIRECT ROUTINE LOAD FACTOR
887	i	i	i	i	.	i	i	i	i	i	i	i	DIRECT ROUTE TEST COMPARATOR
888	i	.	i	i	i	i	i	i	i	i	i	i	DIRECT ROUTE TEST COMPARATOR
889	i	i	i	i	i	i	i	i	i	i	P	i	PRINT COMPARATOR
890	i	i	i	i	D	i	i	i	i	i	i	i	DIRECT ROUTE TEST COMPARATOR

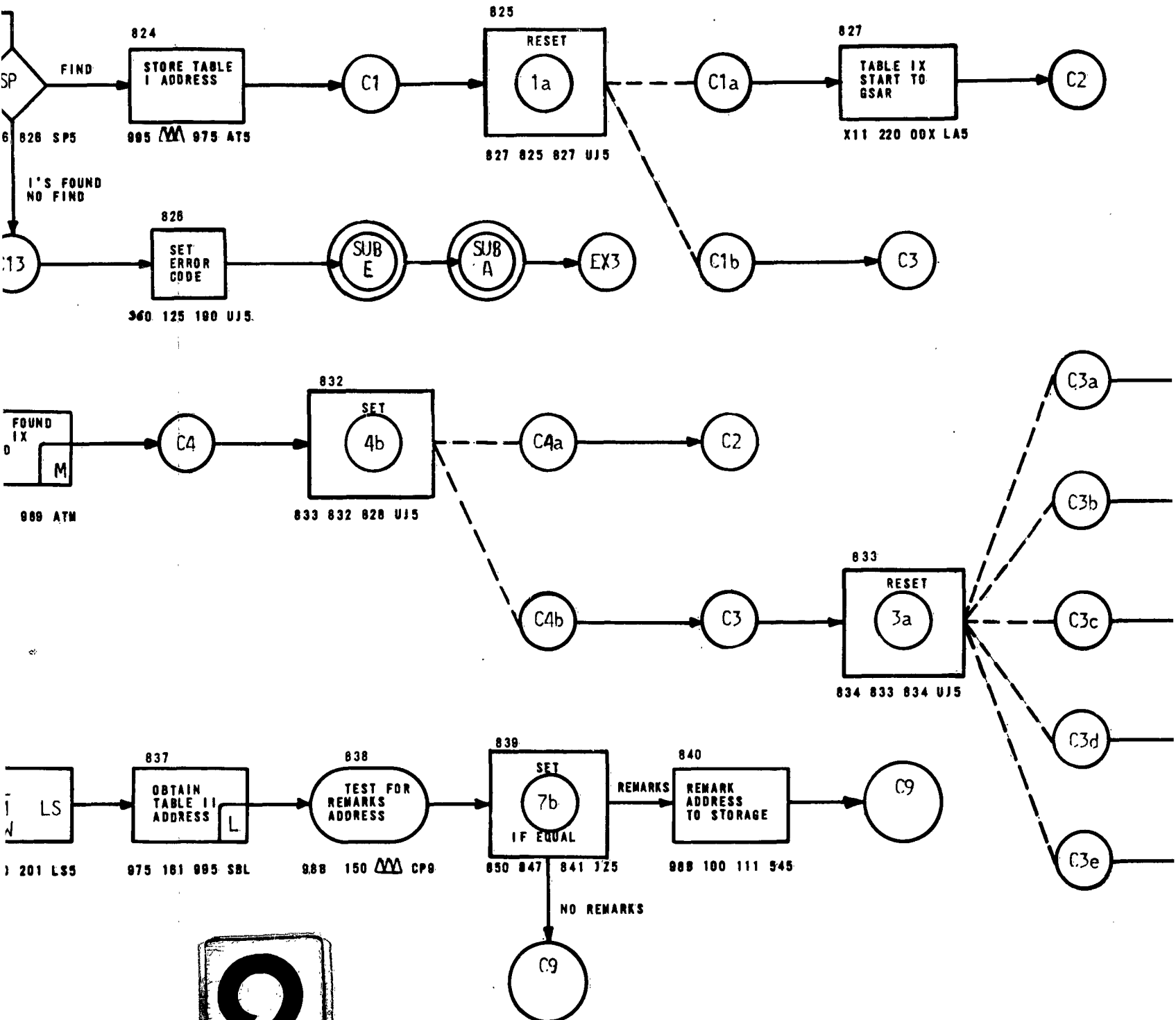
WORKING STORAGE LOCATIONS

920	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	START T VII ADDRESS OF PARENT MESSAGE
940	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	X	X	X	X	ENTRY FIX
946	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	CONTINUATION TABLE ADDRESS
947	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	LAST T VII ADDRESS OF PARENT MESSAGE
950	Δ	Δ	Δ	Δ	Δ	Δ	X	X	X	Δ	Δ	Δ	SEGMENT COUNTER
952	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	LAST T VII ADDRESS OF PARENT MESSAGE
956	X	X	X	X	X	X	X	X	X	X	X	X	LAST SEGMENT OF PARENT MESSAGE
959	Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	LAST T VII ADDRESS OF PARENT MESSAGE

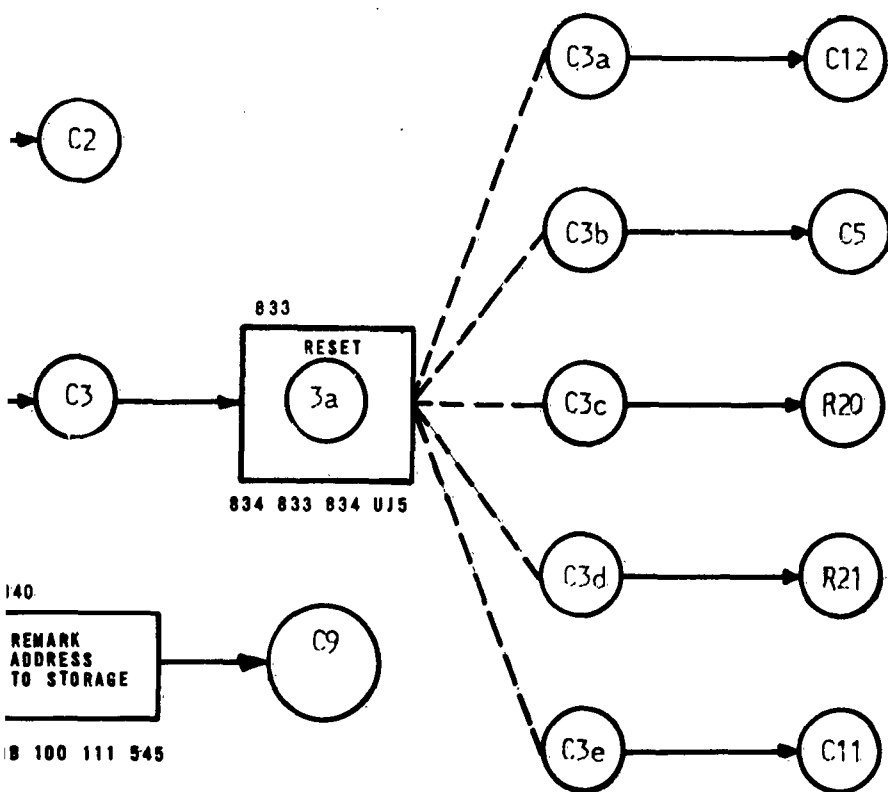
APPENDIX XIV

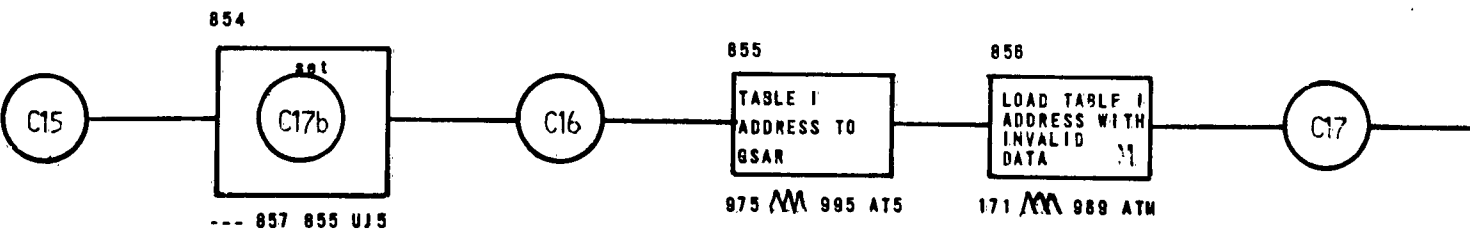
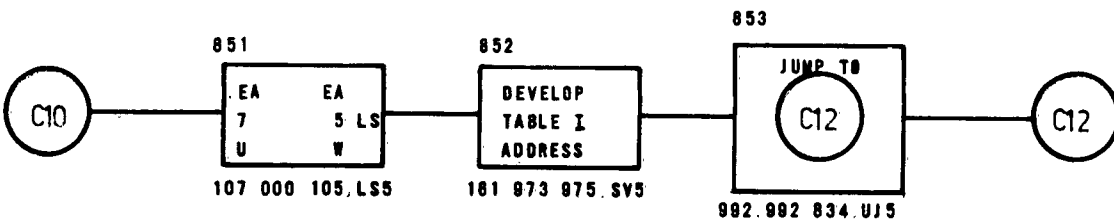
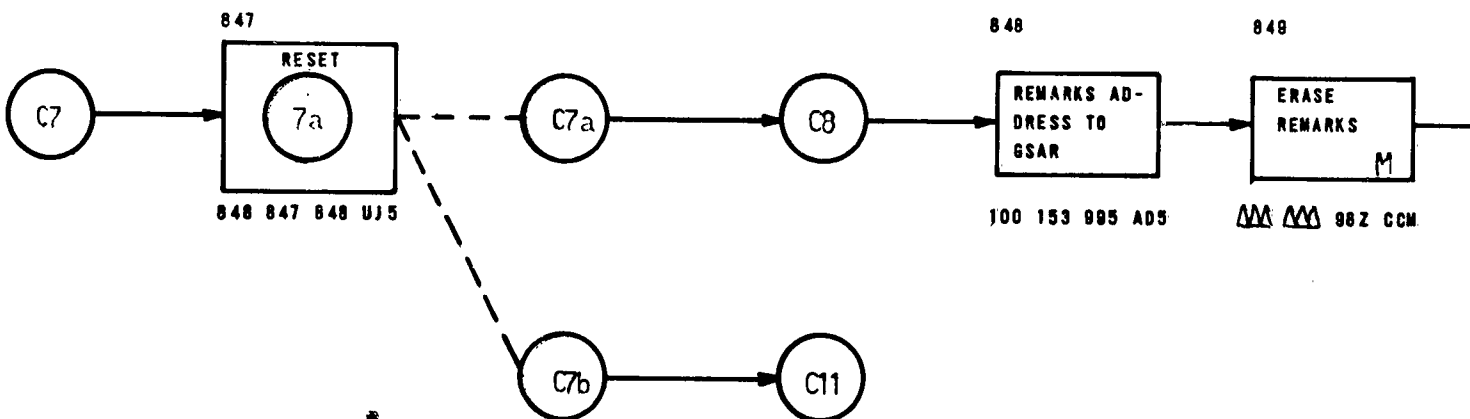
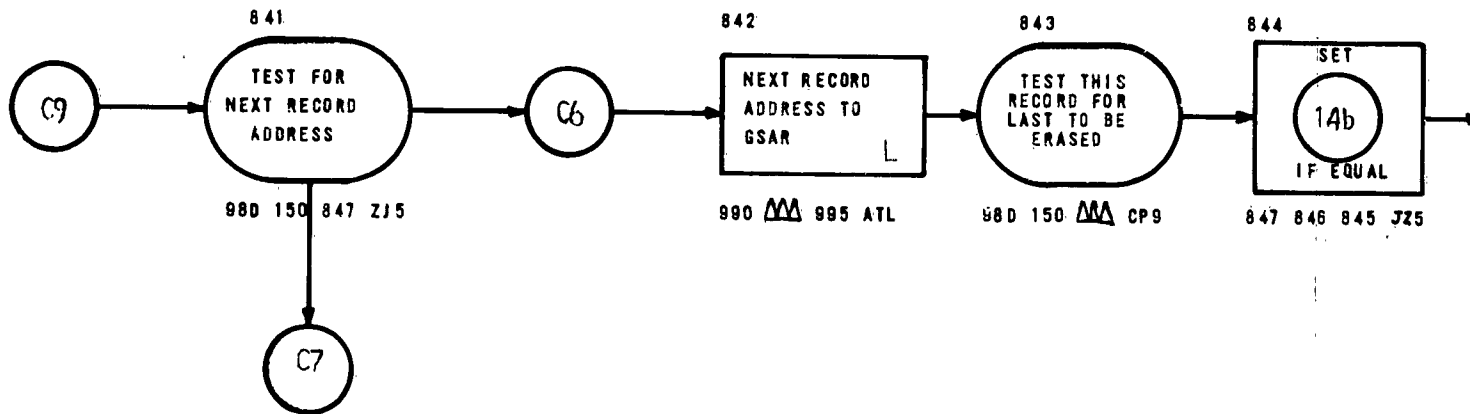
CANCELLATION ROUTINE



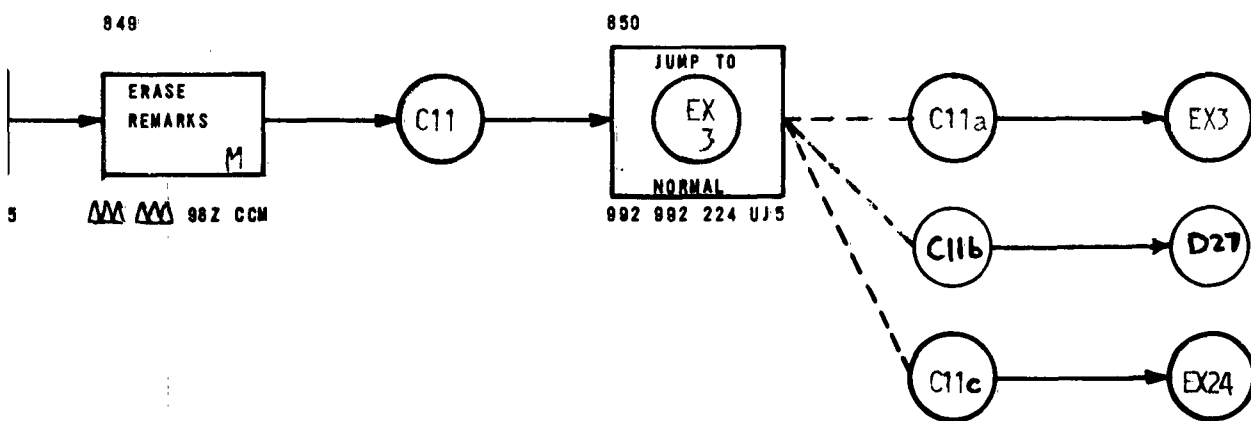
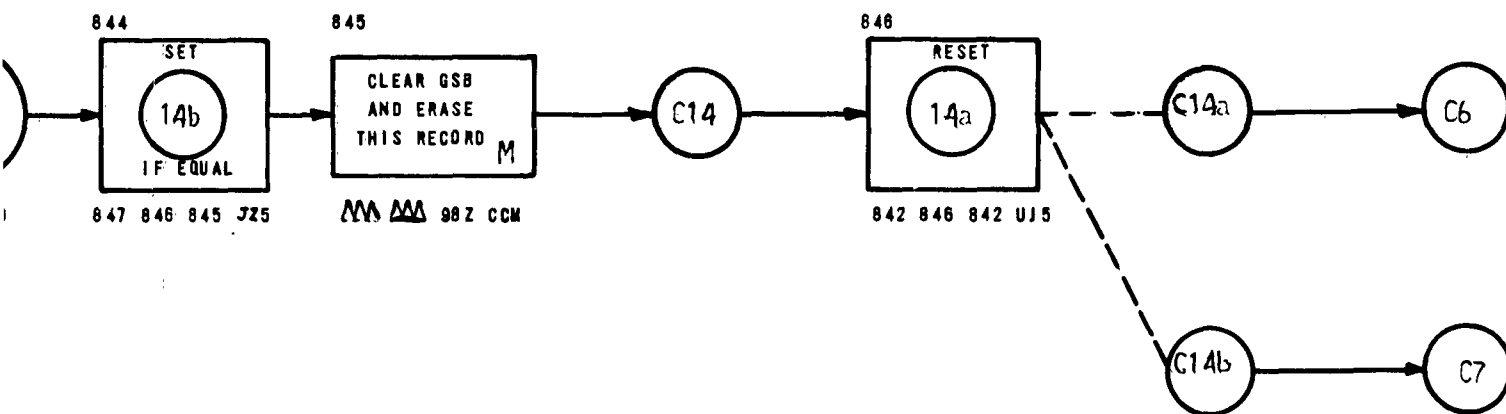


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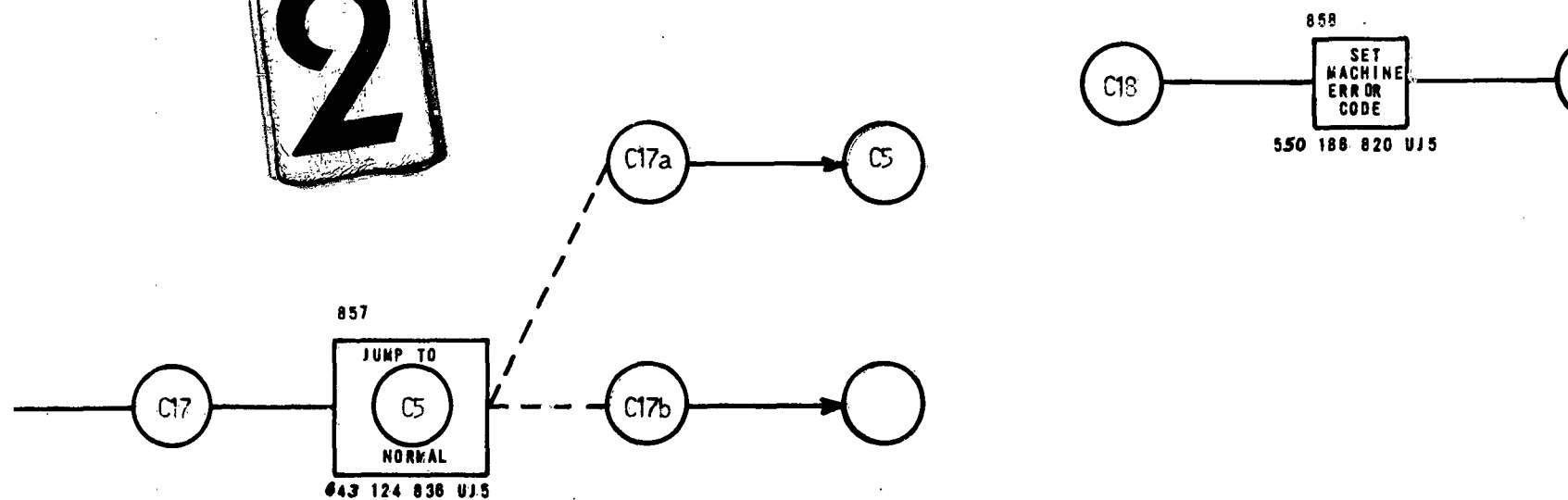


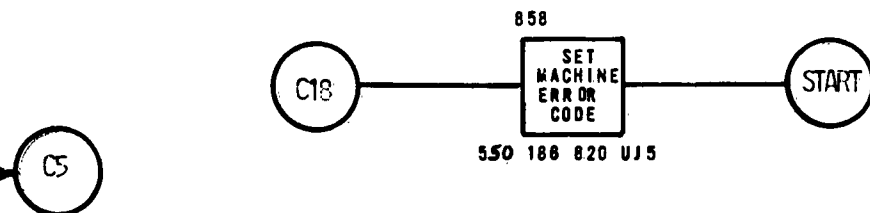
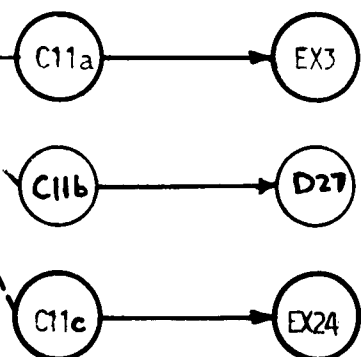
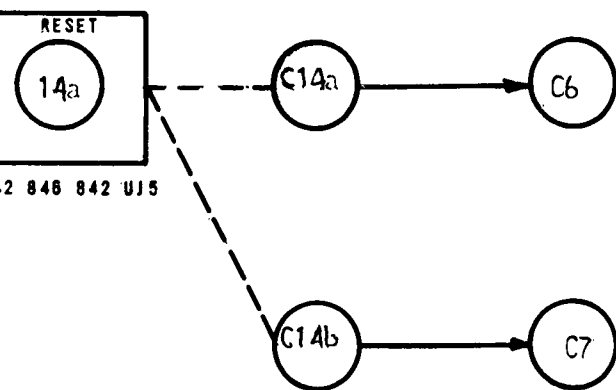


1



2





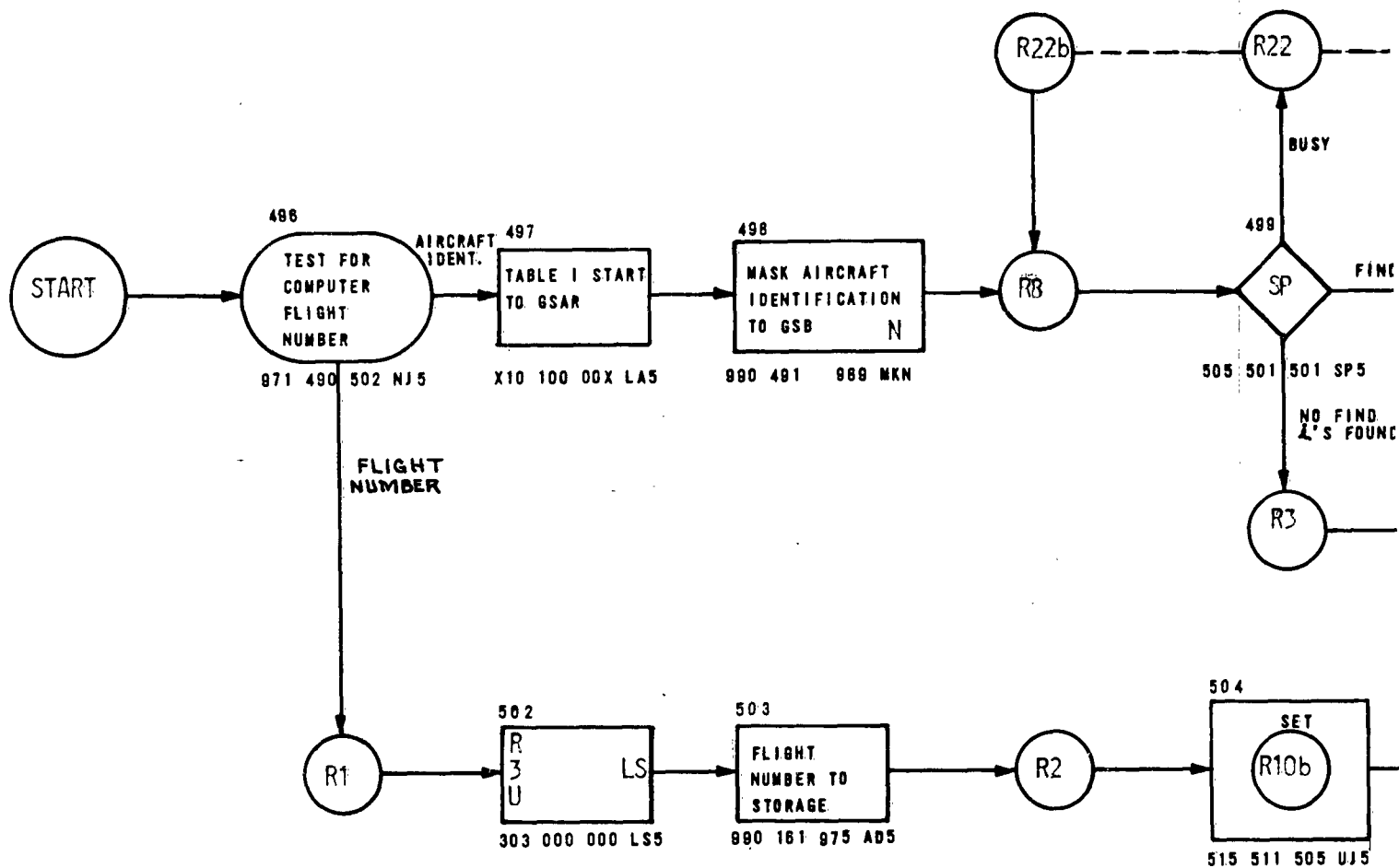
CONSTANTS, FACTORS AND WORKING STORAGE FOR CANCELLATION
AND ERASE ROUTINE.

1	5	0		Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	COMPARATOR
1	5	9		i	i	i	X	X	X	i	i	i	i	i	i	USED TO EXTRACT THE 3 LOW- ORDER DIGITS OF THE TABLE I ADDRESS, IN THE "V" POSITION, TO SEARCH FOR TABLE IX RECORDS.
1	6	1		0	0	0	0	1	0	1	0	0	0	0	0	TABLE II MODIFIER
9	7	5		Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	WORKING STORAGE FOR TABLE I ADDRESS.

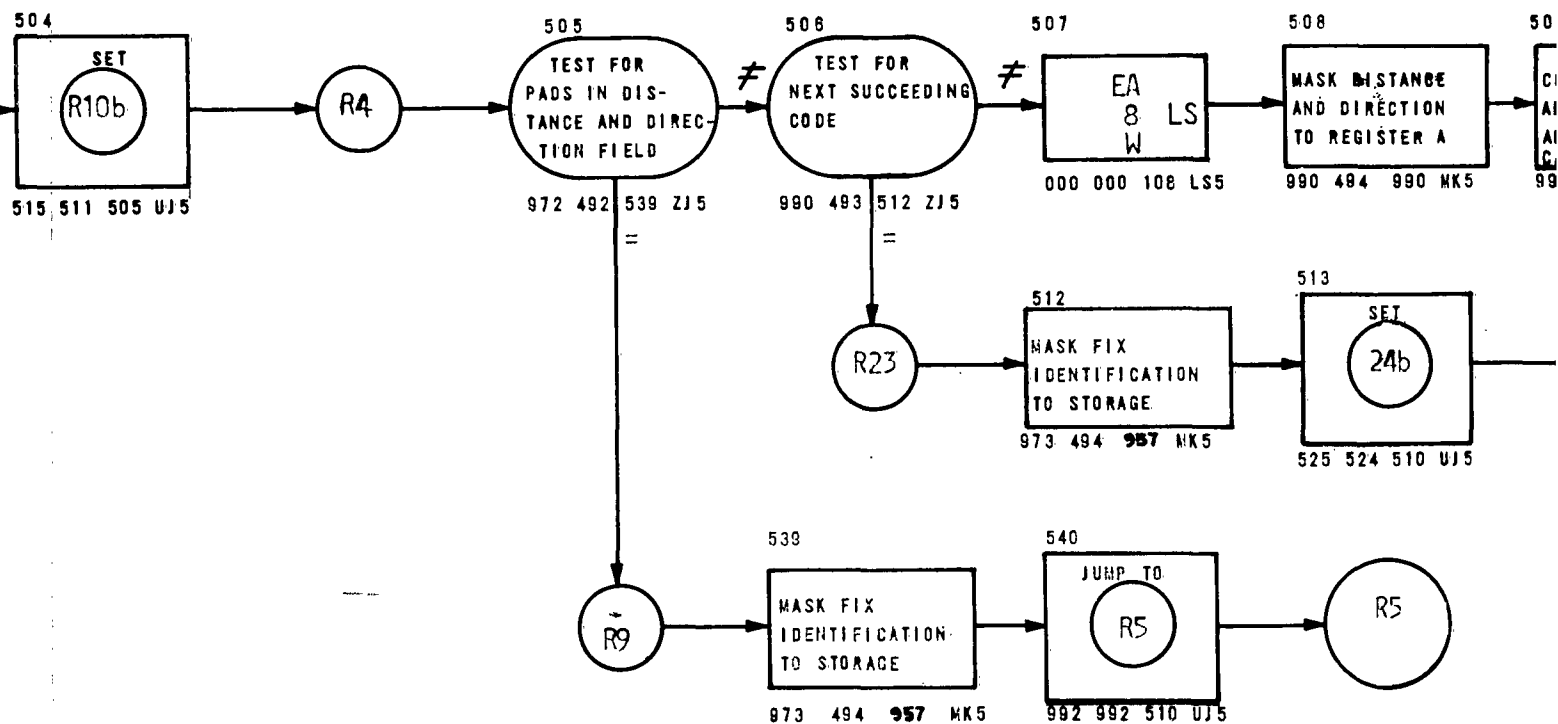
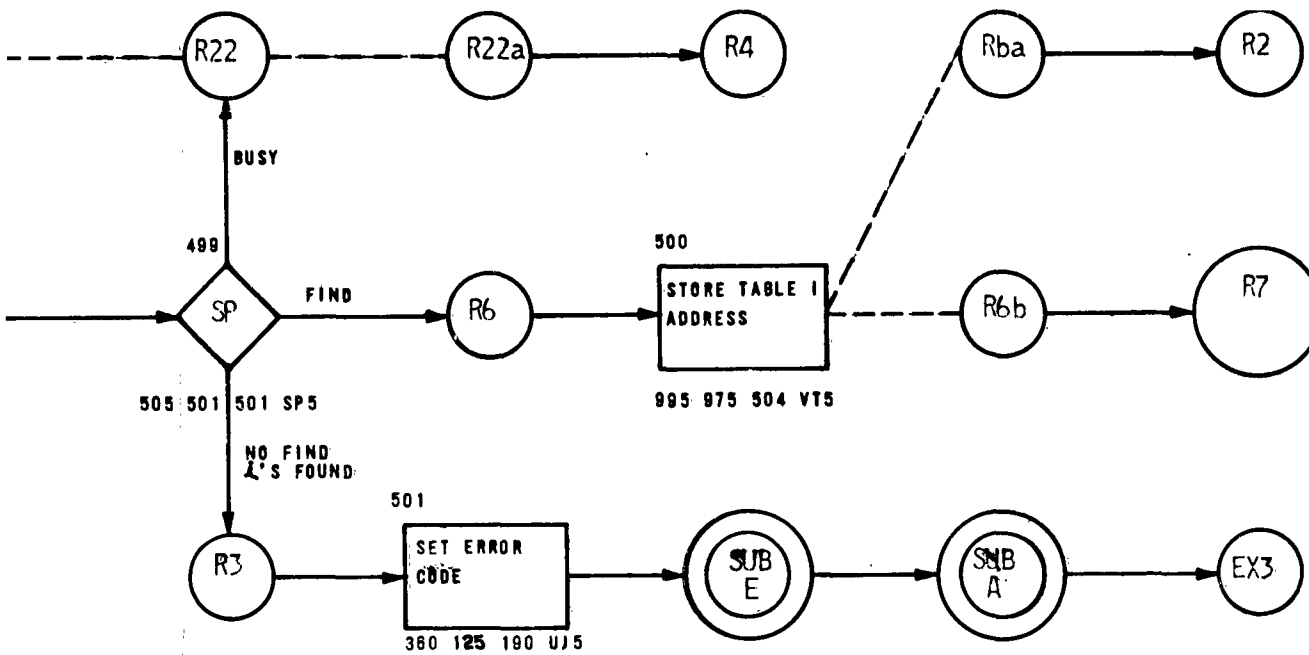
* NOTE: THE REMARKS BESIDE THE ABOVE FACTORS, CONSTANTS AND
WORKING STORAGES DEFINE THE PRIME PURPOSE AS USED IN THIS
ROUTINE. THE FLOW CHARTS WILL DEFINE ANY SECONDARY USAGE OF
THESE LOCATIONS.

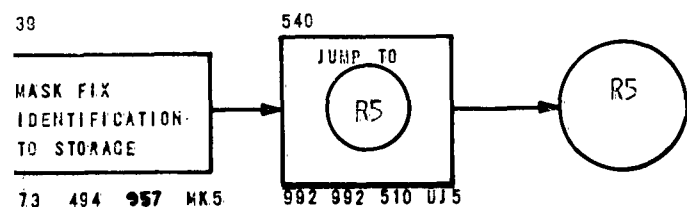
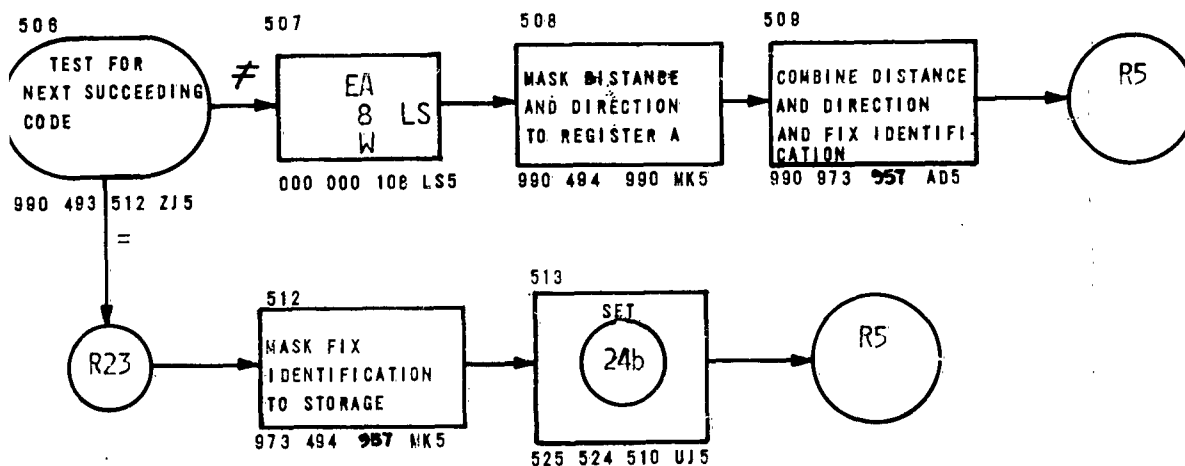
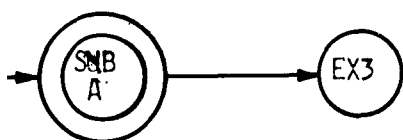
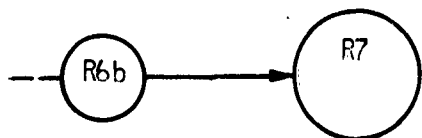
APPENDIX XV

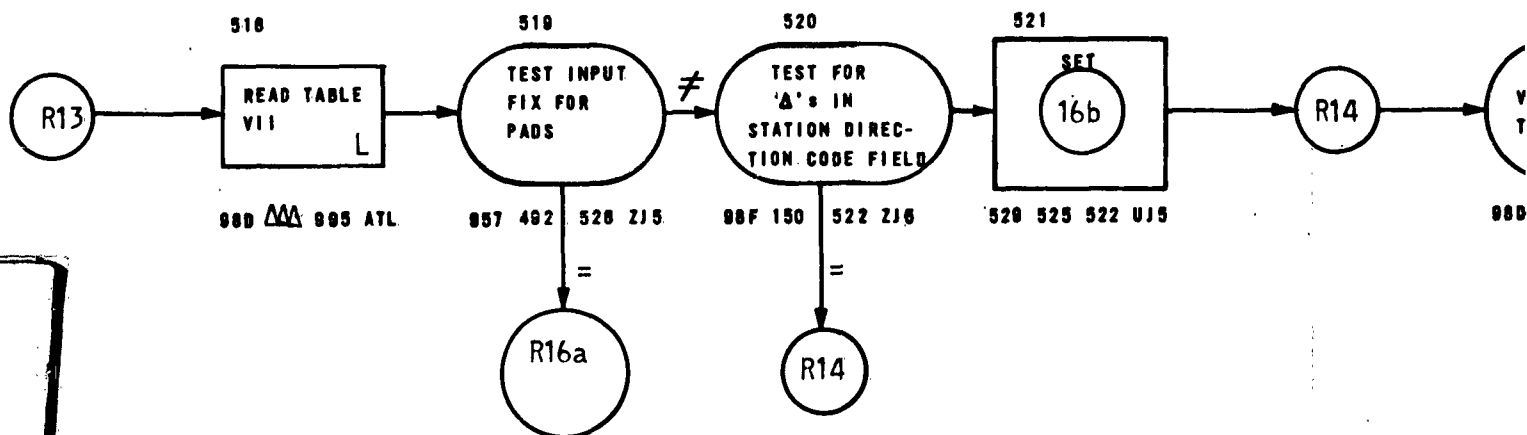
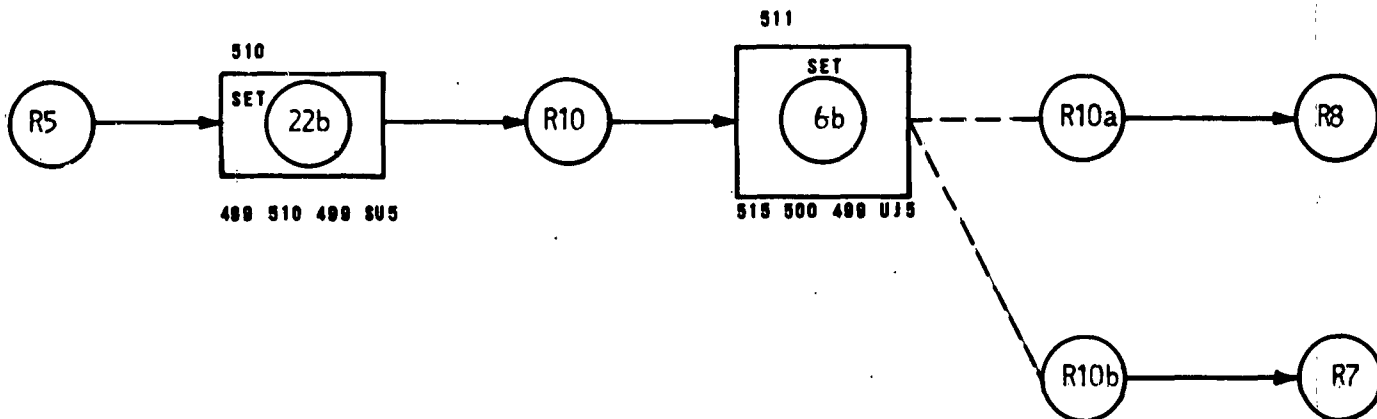
READOUT ROUTINE



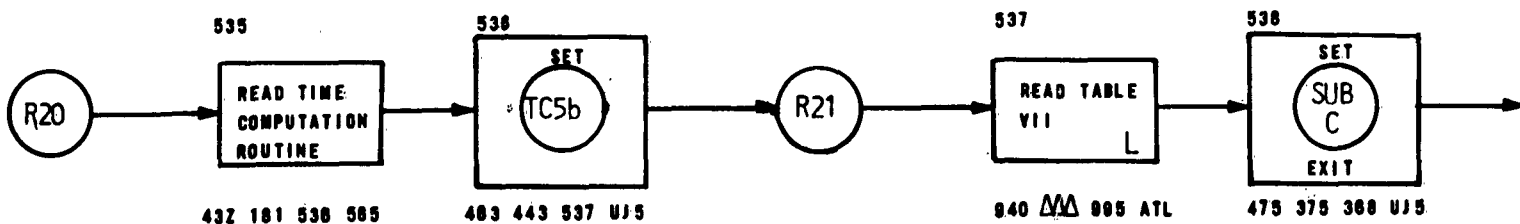
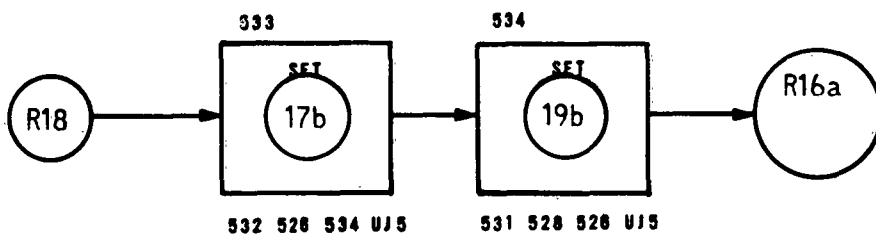
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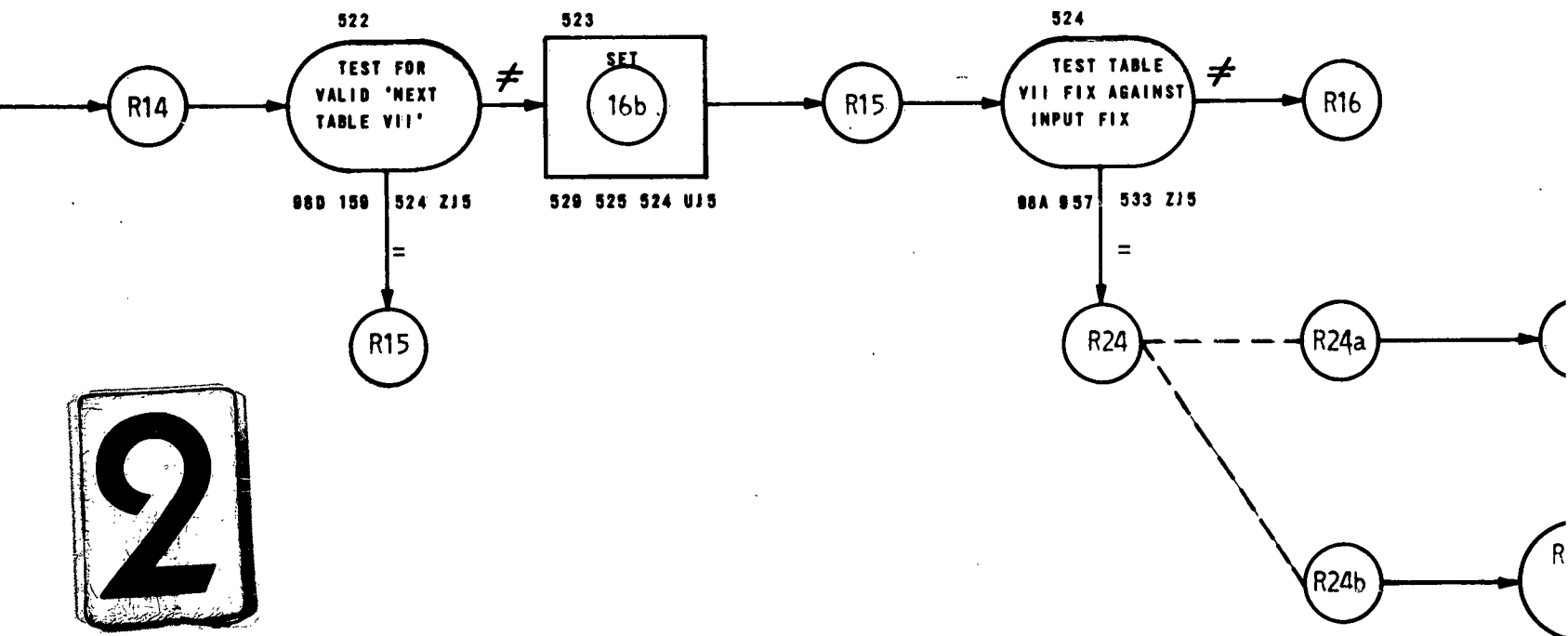
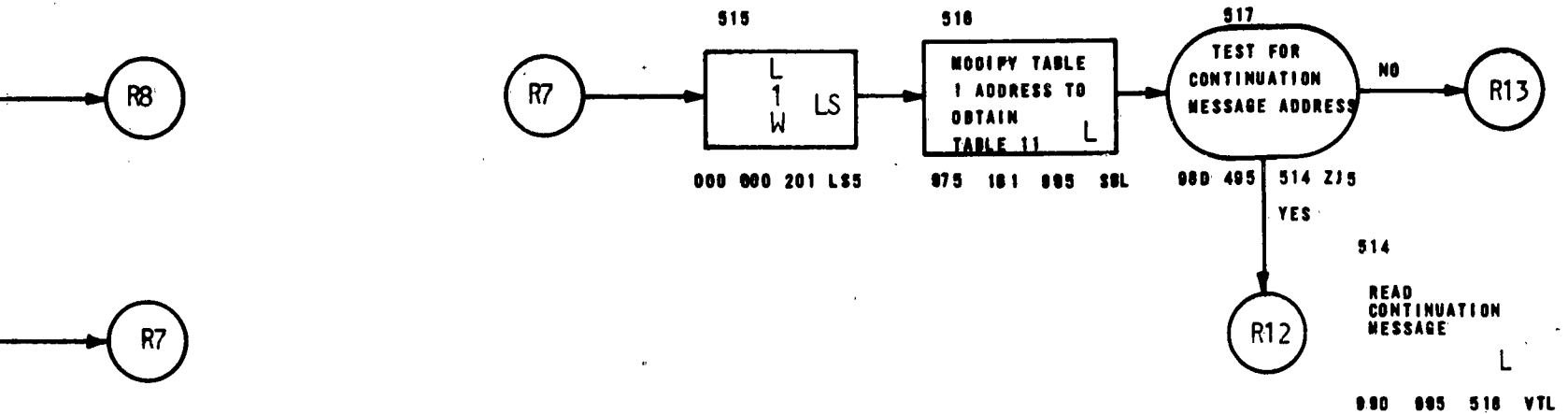




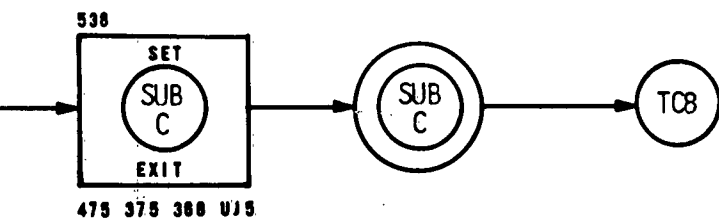


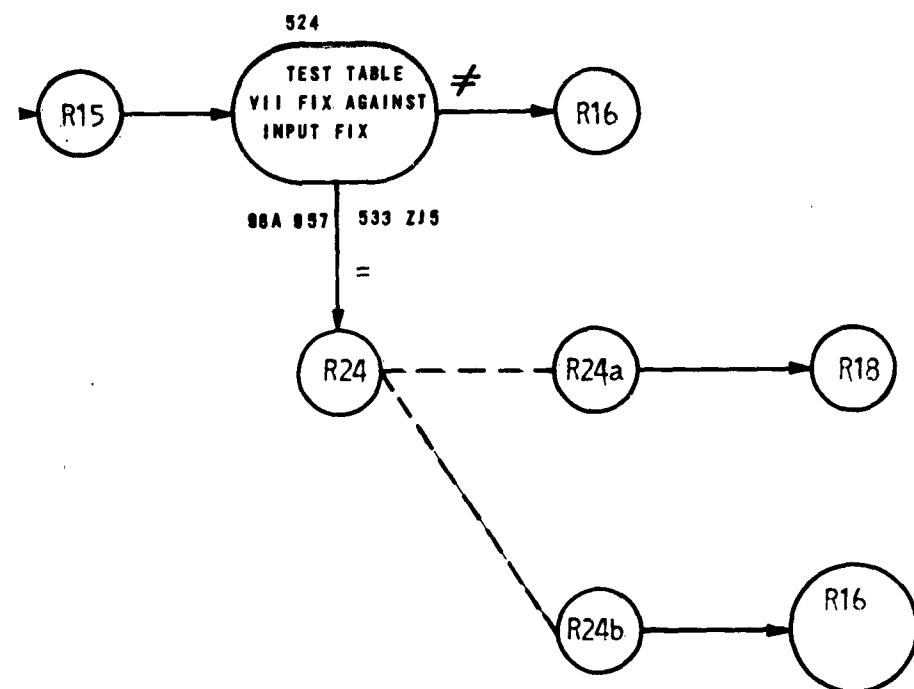
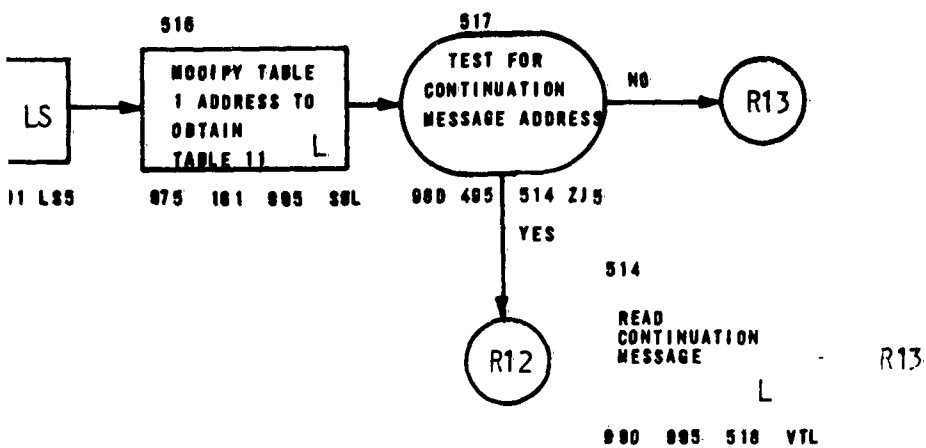
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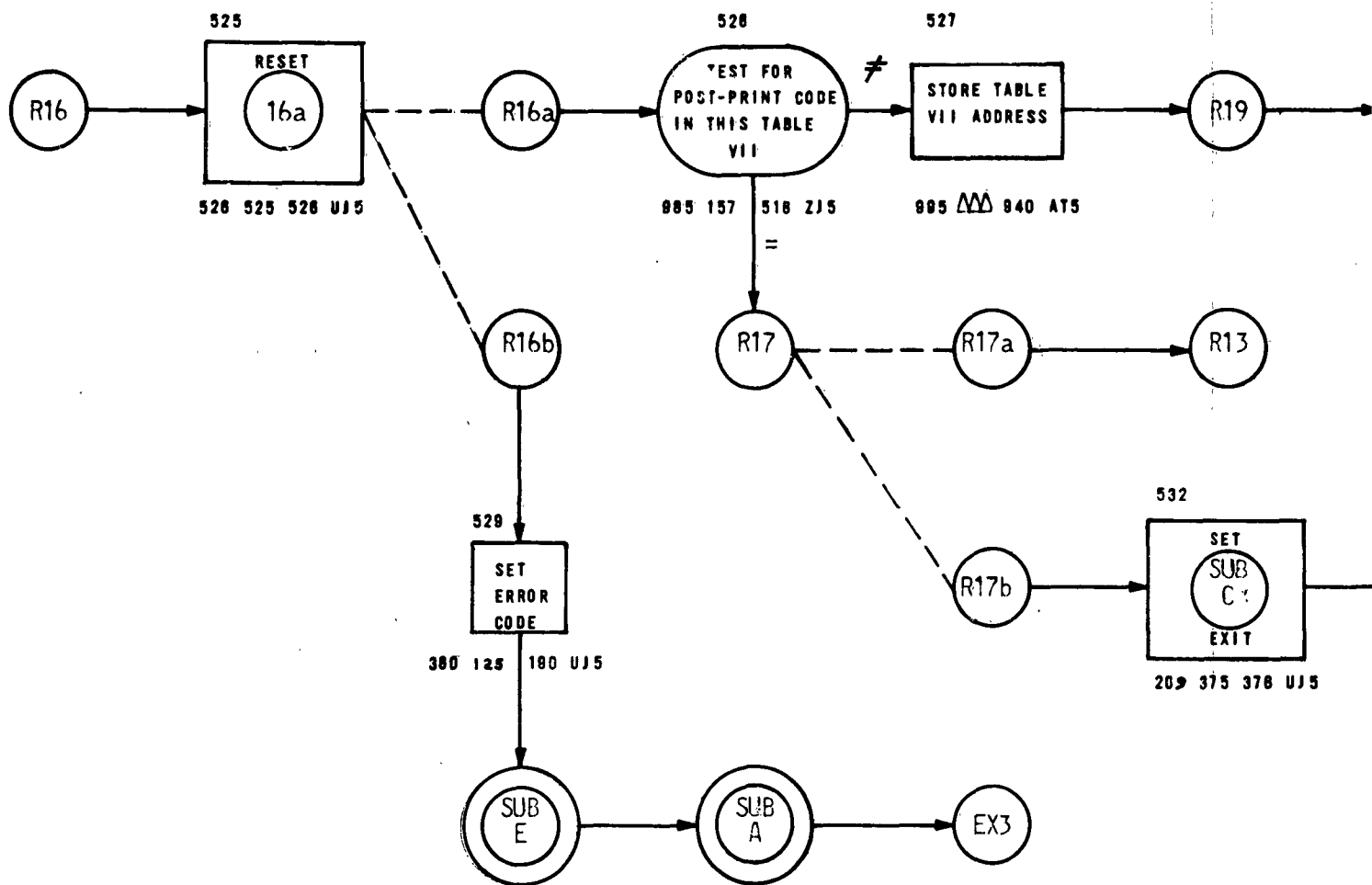


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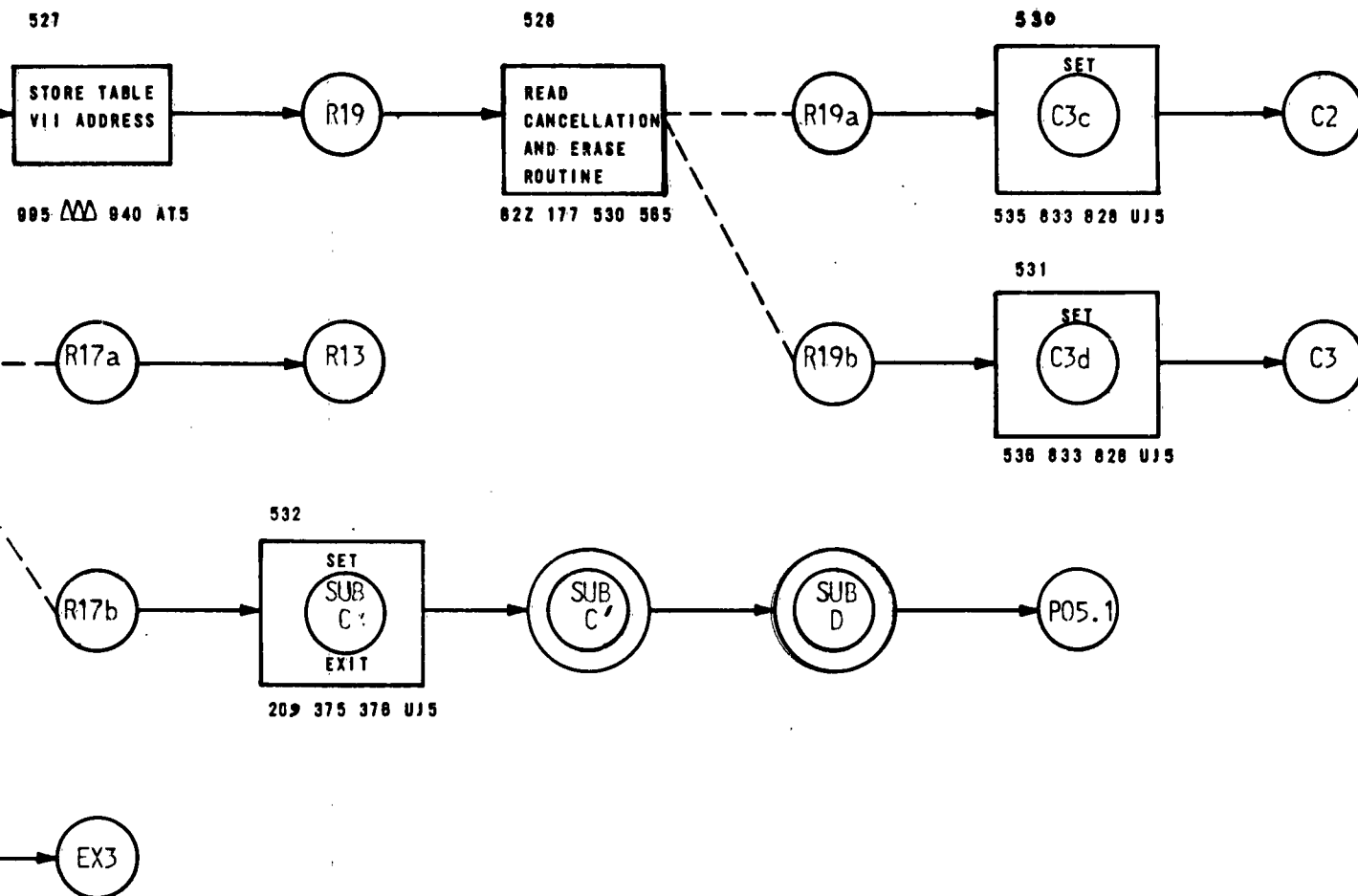




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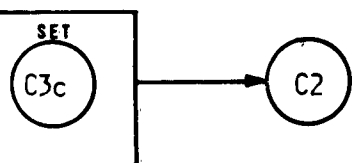


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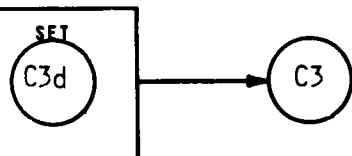
2

30



833 828 U15

31



8 833 828 U15



CONSTANTS, FACTORS AND WORKING STORAGE FOR READOUT ROUTINE

1	5	0		Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	SPACES COMPARATOR
1	5	7		i	i	i	i	i	i	i	i	i	i	P	i	"POST PRINT" TEST CONSTANT
1	5	9		i	i	i	i	5	0	i	i	i	i	i	i	TABLE VII ADDRESS TEST CONSTANT
1	6	1		0	0	0	0	1	0	1	0	0	0	0	0	TABLE II MODIFIER FACTOR
4	9	0		i	i	i	i	'	'	'	i	i	i	i	i	FLIGHT NUMBER TEST CONSTANT
4	9	1		A	A	A	A	A	0	0	0	0	0	0	0	MASKING CONSTANT
4	9	2		i	i	i	i	i	i	i	i	.	.	.	i	FIX IDENTIFIER TEST CONSTANT
4	9	3		i	i	i	i	i	i	i	i	N	S	i	i	"NEXT SUCCEEDING" TEST CONSTANT
4	9	4		A	A	A	A	A	A	A	A	0	0	0	0	MASKING CONSTANT
4	9	5		i	i	i	i	0	1	i	i	i	i	i	i	CONTINUATION MESSAGE ADDRESS TEST CONSTANT
9	4	0		Δ	Δ	Δ	Δ	X	X	X	X	X	X	X	Δ	WORKING STORAGE FOR TABLE VII ADDRESS

*NOTE: THE REMARKS BESIDE THE ABOVE CONSTANTS, FACTORS AND WORKING STORAGES DEFINE THE PRIME PURPOSE AS USED IN THIS ROUTINE. THE FLOW CHARTS WILL DEFINE ANY SECONDARY USAGE OF THESE LOCATIONS.

APPENDIX XVI

ENDING ROUTINE



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WORD	Ø												1												2												3					
CHARACTER			9	8	7	6	5	4	3	2	1	S			9	8	7	6	5	4	3	2	1	S			9	8	7	6	5	4	3	2	1	S			9	8	7	6
FORMAT CONST- ANTS	?	T	E	D	t	Δ	Δ	Δ	Δ	Δ	Δ	r	E	R	R	O	R	S	r	r	M	E	r	r	C	O	M	P	t	Δ	Δ	Δ	Δ	Δ	Δ	·	t	H	S	P	t	
DATA ADDED TO FORMAT						NUMBER OF STRIPS PRINTED					r																								r							
FIELD	v			u					T			S					R				Q					P																

WORD	Ø												1												2												3							
CHARACTER			9	8	7	6	5	4	3	2	1	S			9	8	7	6	5	4	3	2	1	S			9	8	7	6	5	4	3	2	1	S			9	8	7	6		
FORMAT CONST- ANTS	?	·	·	·	·	Δ	Δ	Δ	Δ	Δ	Δ	·	t	I	N	t	Δ	Δ	Δ	Δ	Δ	Δ	Δ	·	T	O	T	A	L	t	Δ	Δ	Δ	Δ	Δ	Δ	r	·	P	E	t	t		
DATA ADDED TO FORMAT						NUMBER OF CUE OPERATOR ERRORS					r						NUMBER OF INPUT OPERATOR ERRORS					r											TOTAL NUMBER OF OPERATOR ERRORS					r						
FIELD	V				U				T				S				R				Q				P																			

	Ø												1												2												3					
			9	8	7	6	5	4	3	2	1	S			9	8	7	6	5	4	3	2	1	S			9	8	7	6	5	4	3	2	1	S			9	8	7	6

CODE TYPEWRITER FUNCTION
 ? ——— RED COLOR PRINT
 / ——— UPPER CASE
 β ——— LOWER CASE
 r ——— CARRIAGE RETURN

CODE TYPEWRITER FUNCTION
 Δ ——— SPACE
 † ——— TABULATE
 ! ——— TERMINATE PRINT AND CLEAR
 · (DOT) ——— NON-PRINT PAD CHARACTER



BLOCK TRANSFER BUFFER

[illegible]

BLOCK TRANSFER BUFFER

3													4													5													6																									
4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9																					
Δ	Δ	Δ	Δ	Δ	r		P	E	t	t	Δ	Δ	Δ	Δ	Δ	r	D	E	t	t	Δ	Δ	Δ	Δ	Δ	Δ	r	r	M	T	Δ	D	U	M	P	r	t	M	O	U	N	T	Δ	T	A	P	E	r	t	L	A	B	L	E										
TOTAL NUMBER OF OPERATOR ERRORS					r						NUMBER OF PROGRAM ERRORS					r							NUMBER OF DATA ERRORS					r																																				
Q					P					N					M					L					K					J					H					G																								

[illegible]

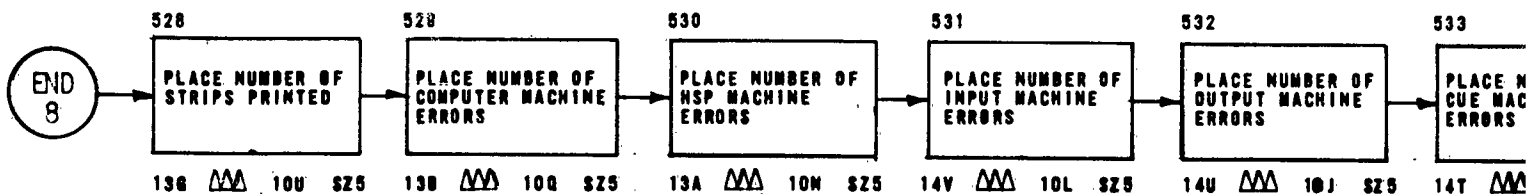
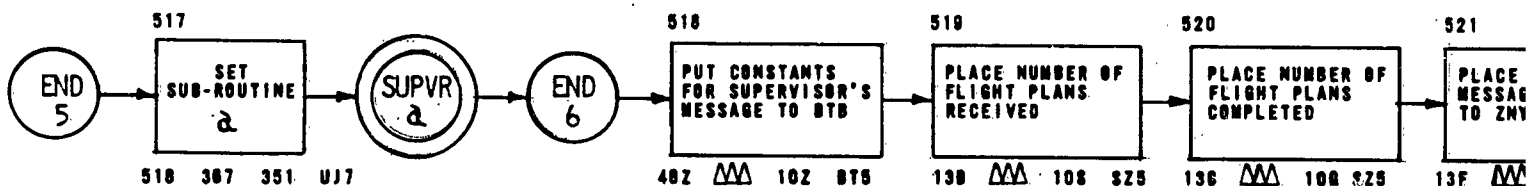
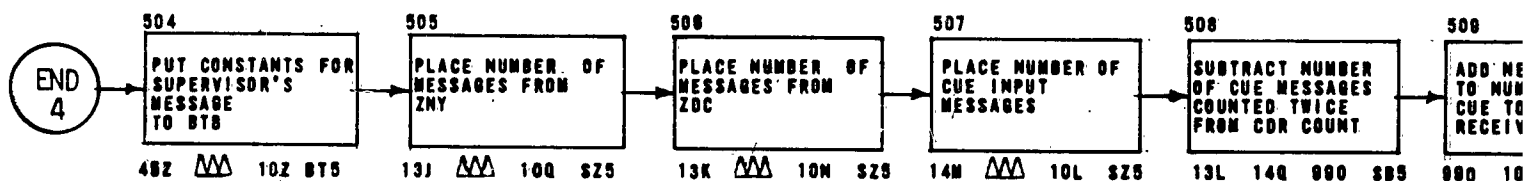
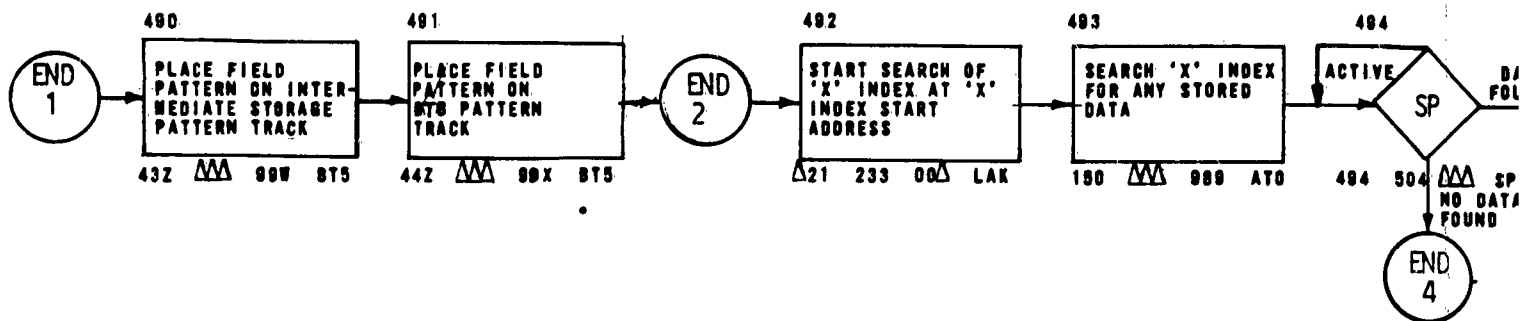
ER FUNCTION

E PRINT AND CLEAR
T PAD CHARACTER

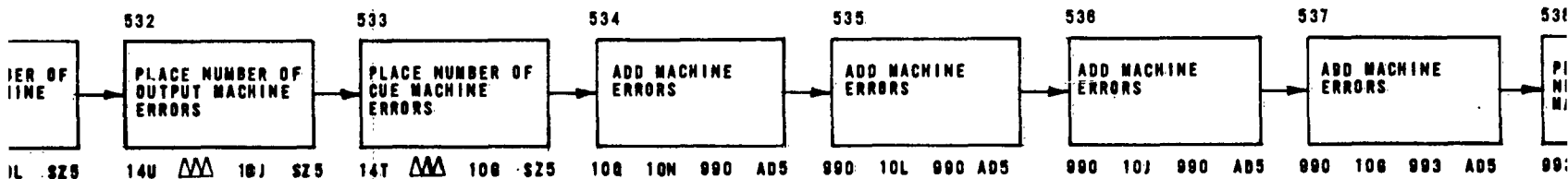
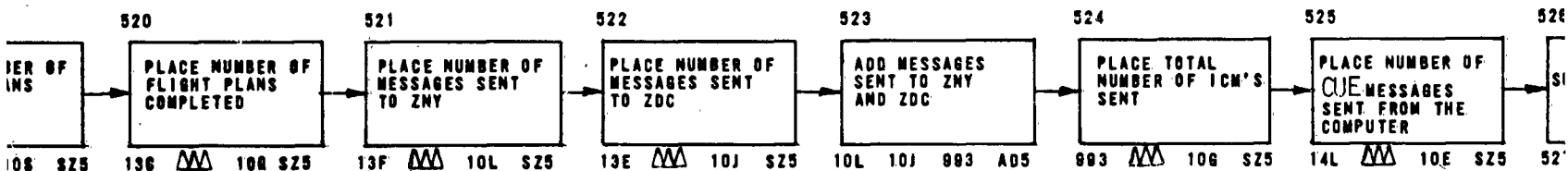
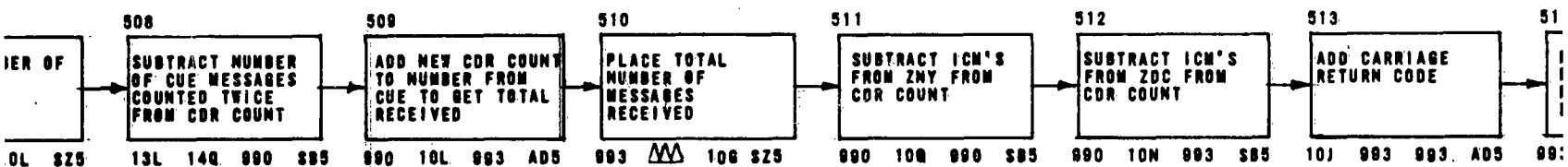
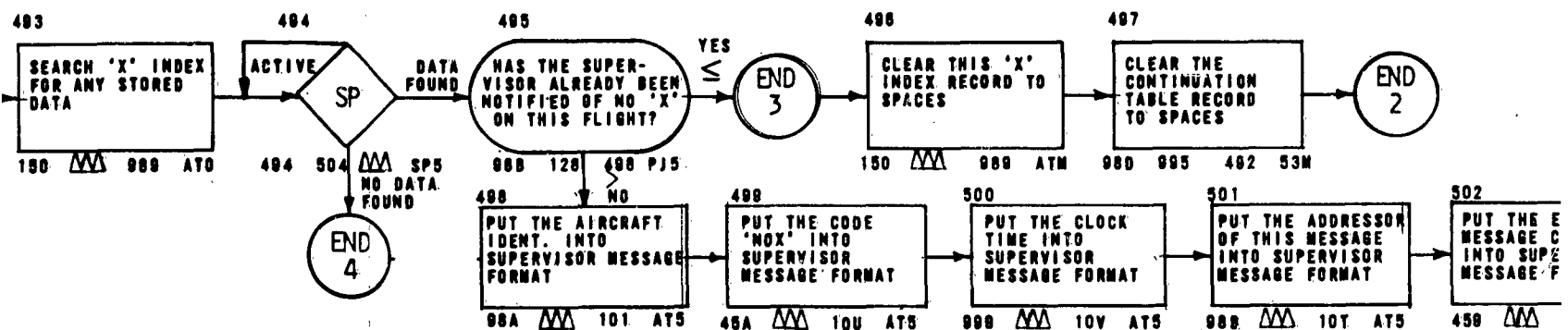


[illegible][illegible][illegible]

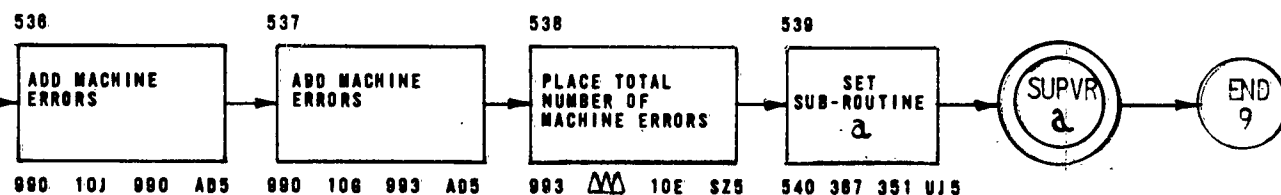
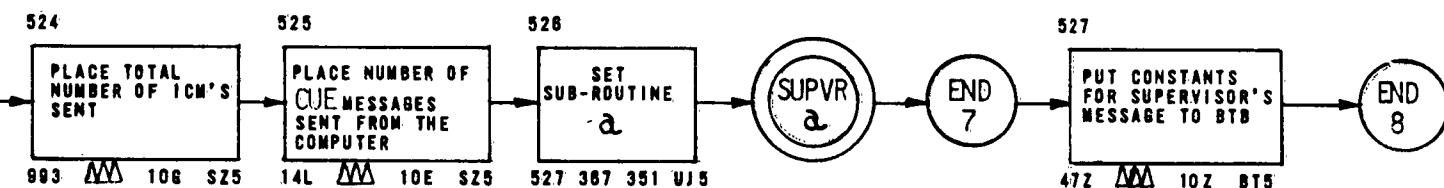
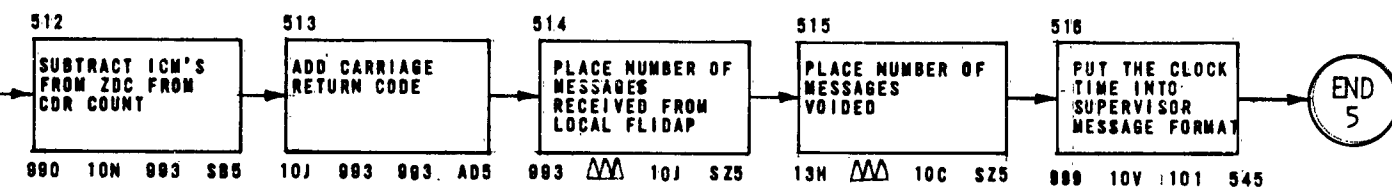
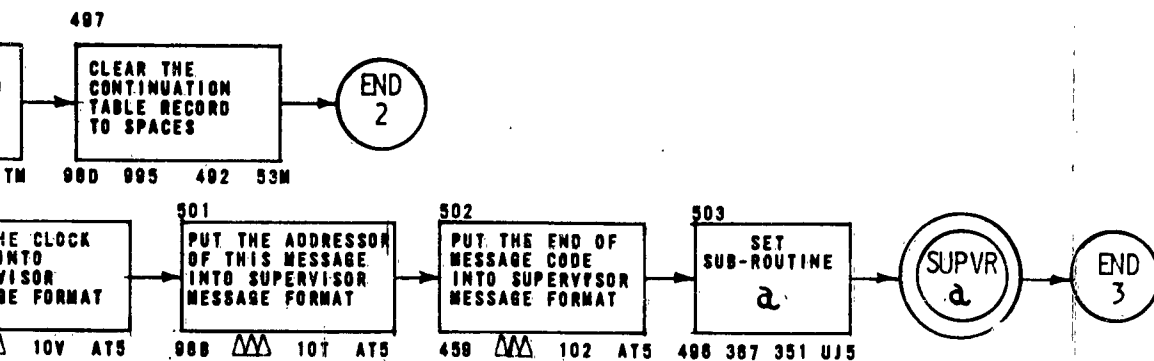
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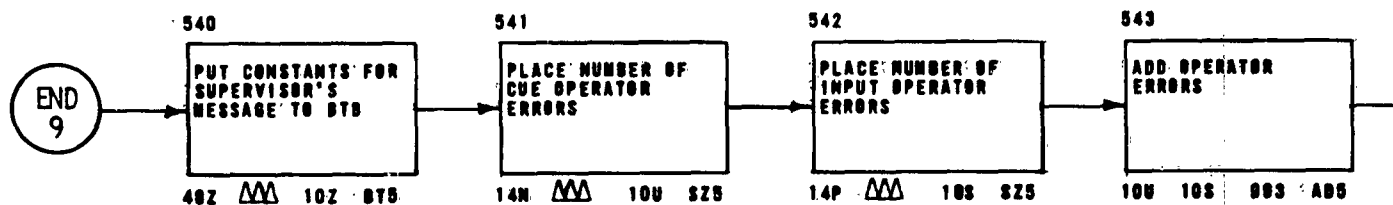


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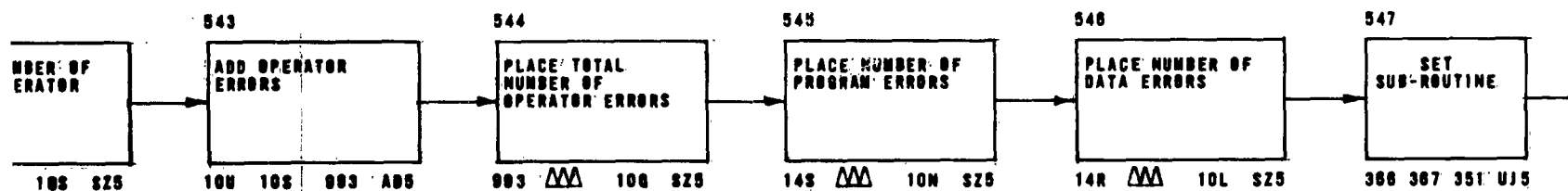


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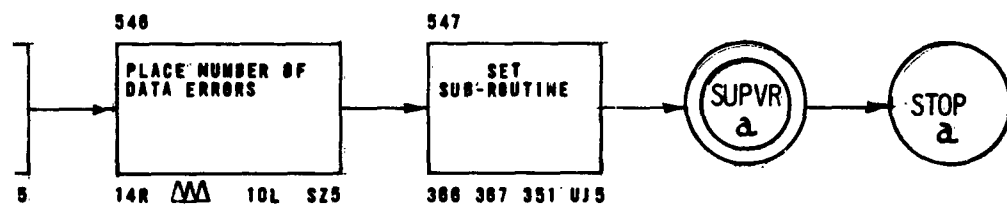




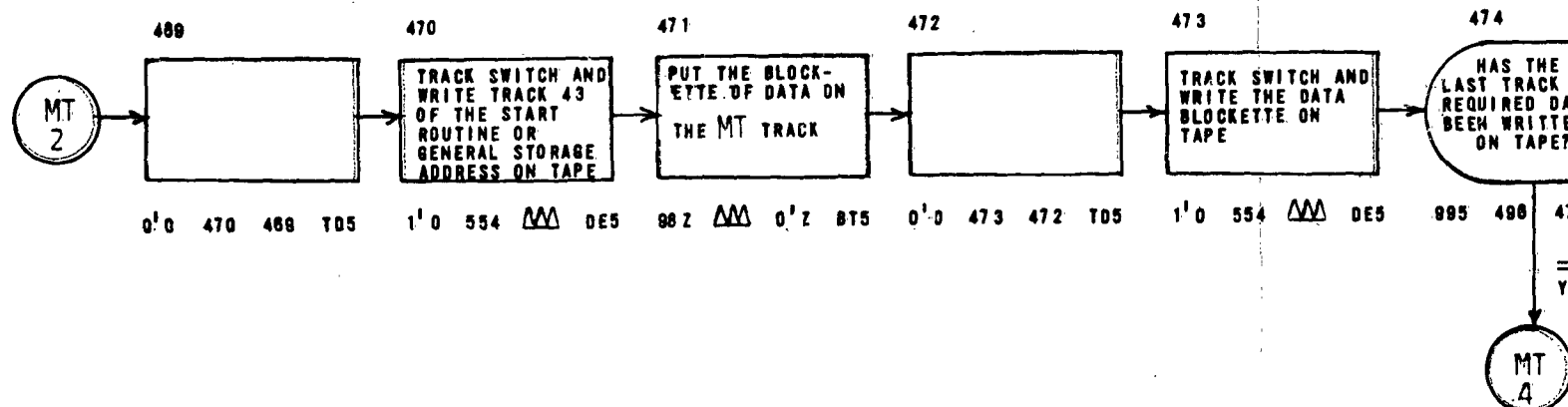
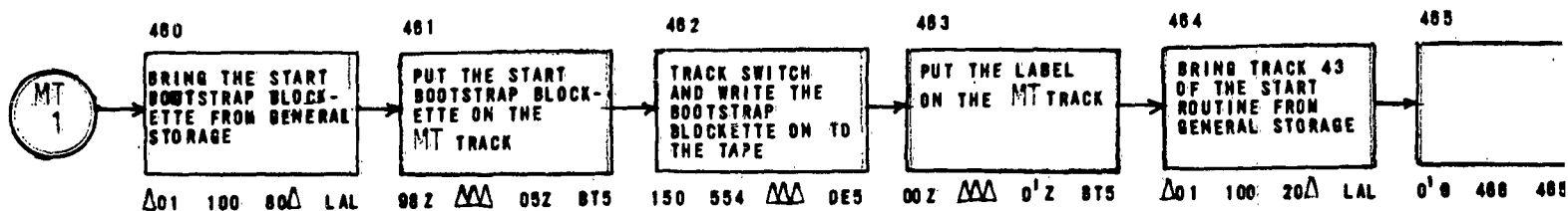
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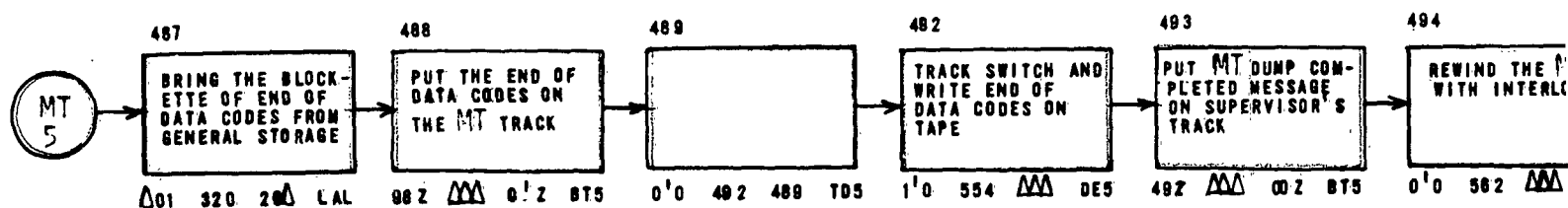
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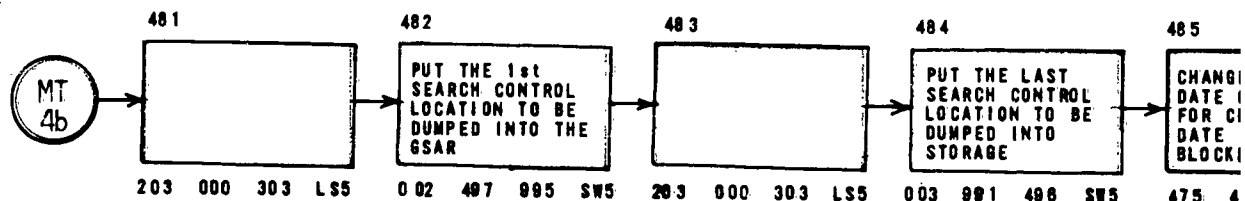
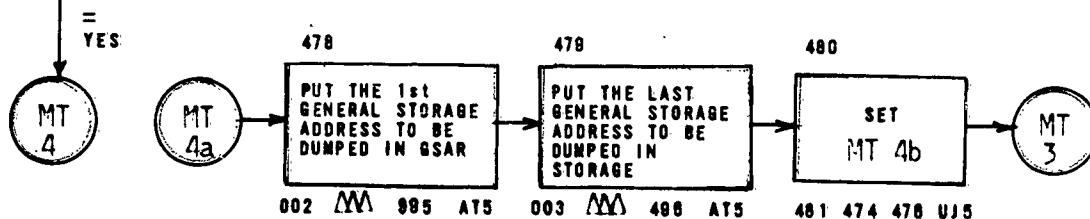
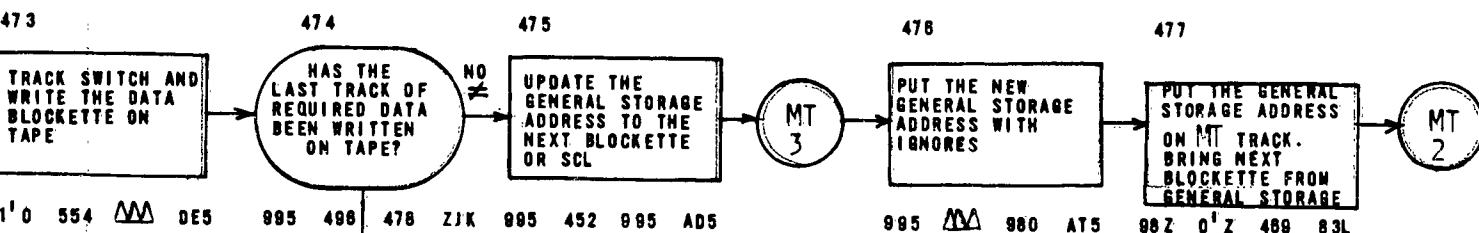
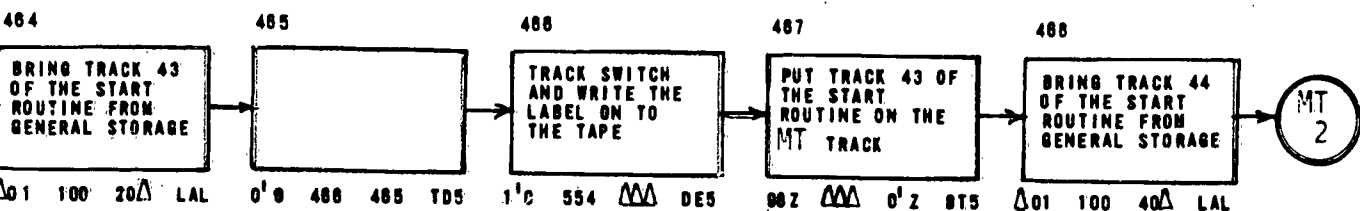


3

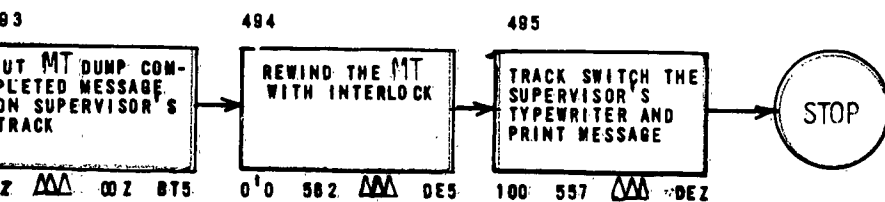
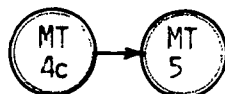


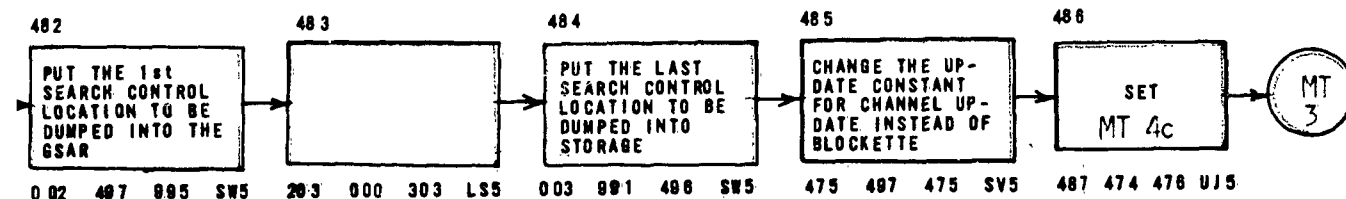
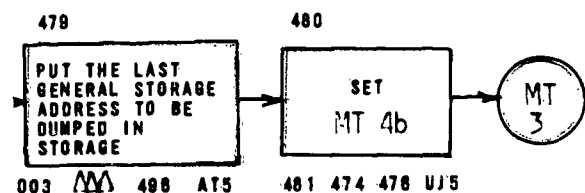
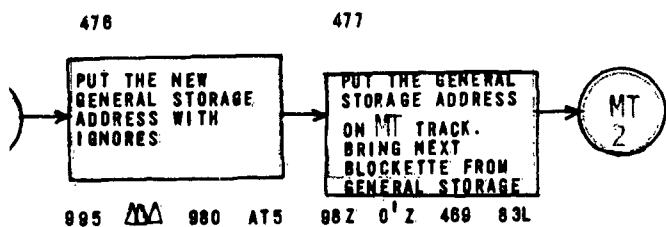
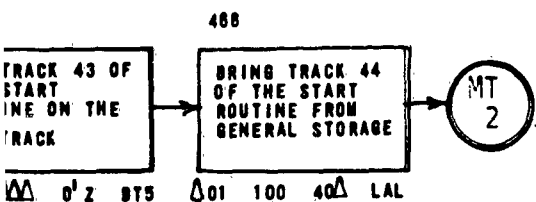
1





2





3

128 Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ 1 Δ 1 constant
 150 Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Space constants

Each of the following, except as noted in 134, contain two counters, one in the high order 6 character positions and one in the low order 6 positions. At the end of a run they may appear as follows:

0 0 0 0 9 r 0 3 4 3 2 r

	High order 6	Low order 6
134	r r ; r t	Total number of messages received. A pad is in the low order position.
135	Messages received from ZDC.	Messages received from ZNY.
136	Messages voided on input.	Number of strips printed.
137	Messages sent to ZNY.	Messages sent to ZDC.
138	Number of flight plans received.	Number of flight plans completed.
139	Number of computer machine errors.	Number of HSP machine errors.
140	Number of input machine errors.	Number of output machine errors.
141	Number of CUE machine errors.	Number of program errors.
142	Number of data errors.	Number of messages added to the total count twice, once in CUE and once in Executive routine.
143	Number of input operator errors.	Number of CUE operator errors.
144	Number of CUE messages received.	Number of CUE messages transmitted.

Constants for the Supervisor Messages

450	Δ	Δ	Δ	Δ	Δ	?	/	E	Δ	N	Δ	D
451	β	r	r	M	S	G	S	Δ	R	C	V	D
452	r	t	z	N	Y	t	Δ	Δ	Δ	Δ	Δ	Δ
453	.	t	z	D	C	t	Δ	Δ	Δ	Δ	Δ	Δ
454	t	C	U	E	t	Δ	Δ	Δ	Δ	Δ	Δ	Δ
455	L	O	C	A	L	t	Δ	Δ	Δ	Δ	Δ	r
456	T	O	T	A	L	t	Δ	Δ	Δ	Δ	Δ	Δ
457	r	r	M	S	G	S	Δ	V	O	I	D	E
458	D	t	Δ	Δ	Δ	Δ	Δ	Δ
459	r	l	Δ	Δ	Δ	Δ	Δ	?	N	O	X	Δ

NOX Costant for Supervisor's
message.

460	?	F	L	T	Δ	P	L	N	S	r	t	R
461	C	V	D	t	Δ	Δ	Δ	Δ	Δ	Δ	t	c
462	M	P	L	T	D	t	Δ	Δ	Δ	Δ	Δ	Δ
463	r	M	S	G	S	Δ	X	M	T	D	Δ	r
464	t	z	N	Y	t	Δ	Δ	Δ	Δ	Δ	r	.
465	.	t	z	D	C	t	Δ	Δ	Δ	Δ	Δ	Δ
466	.	I	C	M	t	t	Δ	Δ	Δ	Δ	Δ	Δ
467	r	.	C	U	E	t	Δ	Δ	Δ	Δ	Δ	Δ
468	r	S	T	R	I	P	S	Δ	P	R	I	N
469	l	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ

470	?	T	E	D	t	Δ	Δ	Δ	Δ	Δ	Δ	r
471	E	R	R	O	R	S	r	r	M	E	r	t
472	C	O	M	P	t	.	Δ	Δ	Δ	Δ	Δ	Δ
473	.	t	H	S	P	t	Δ	Δ	Δ	Δ	Δ	Δ
474	.	t	I	N	t	Δ	Δ	Δ	Δ	Δ	Δ	Δ
475	.	t	O	U	T	t	Δ	Δ	Δ	Δ	Δ	Δ
476	.	t	C	U	E	t	Δ	Δ	Δ	Δ	Δ	Δ
477	T	O	T	A	L	t	Δ	Δ	Δ	Δ	Δ	Δ
478	r	O	E	r	t	C	U	E	t		Δ	Δ
479	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ

480	?	Δ	Δ	Δ	Δ	Δ	Δ	.
481	t	I	N	t	Δ	Δ	Δ	Δ	Δ	Δ	Δ	.
482	T	O	T	A	L	t	Δ	Δ	Δ	Δ	Δ	Δ
483	r	.	P	E	t	t	Δ	Δ	Δ	Δ	Δ	Δ
484	r	D	E	t	t	Δ	Δ	Δ	Δ	Δ	Δ	r
485	r	M	T	Δ	D	U	M	P	r	t	M	O
486	U	N	T	Δ	T	A	P	E	r	t	L	A
487	B	L	E	Δ	O	N	Δ	Ø	r	r		Δ

APPENDIX XVII

MACHINE ERROR ROUTINE

SUPERVISOR'S MACHINE ERROR MESSAGE
BLOCK TRANSFER BUFFER

[illegible]

PROCESSING IDENTIFICATION (WORD 6 ABOVE)

10

Dptr. point Δ Aircraft Ident.

A	A	A	A	A	AIRCRAFT IDENT.
I	N	P	U	T	r r i A A A
S	E	A	R	C	H r r i A A A
E	R	A	S	E	r r i A A A
E	R	R	O	R	r r i A A A
C	U	E	r	i	A A A A A A
E	N	D	I	N	G- r r i A A A

AGE

				6												7												8												9											
3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S
i	i	i	i	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	r	r	l	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	
M	E	Δ	Δ	Processing, identification (see below)												Δ	P	S	N	Δ	R	P	R	T	r	r	l																								
J		H				G				F		E				D				C		B		A																											

	6										7										8										9									
3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	

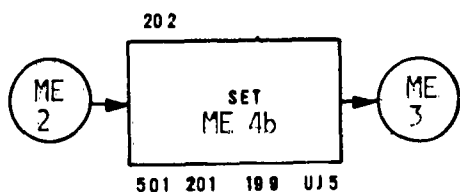
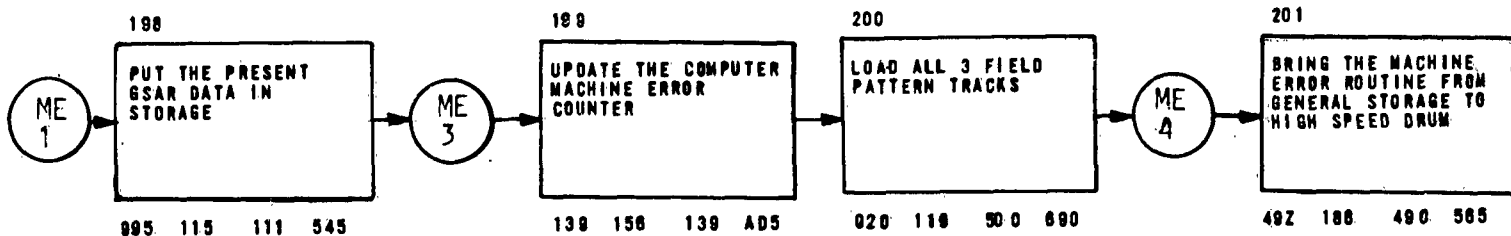
				6										7										8										9									
3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S	11	10	9	8	7	6	5	4	3	2	1	S				

FICATION (WORD 6 ABOVE)

DENT. or Dptr. point Δ Aircraft Ident.

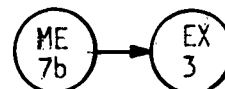
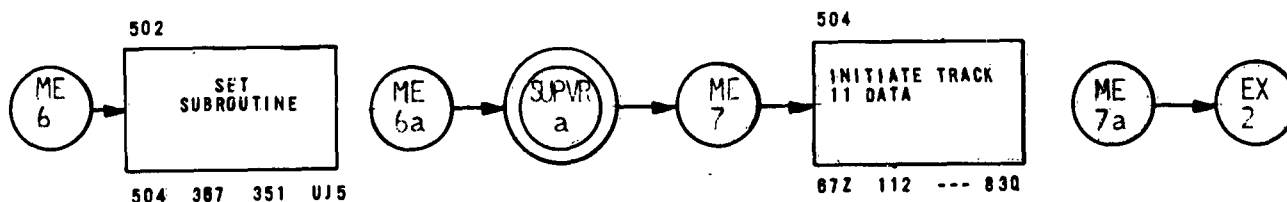
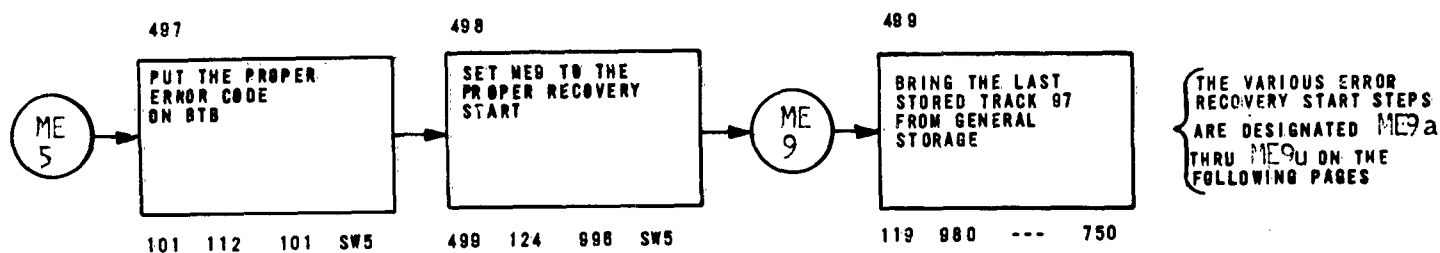
Δ	Δ	Δ
Δ	Δ	Δ
Δ	Δ	Δ
Δ	Δ	Δ
Δ	Δ	Δ
Δ	Δ	Δ



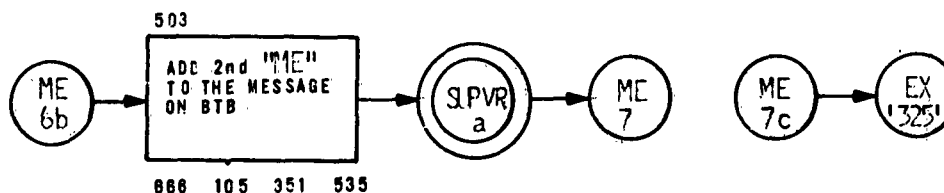


ME
4b

2nd MACHINE ERROR OCCURED BEFORE RECOVERY
COMPLETED FROM ORIGINAL MACHINE ERROR

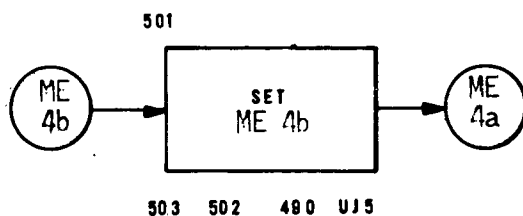
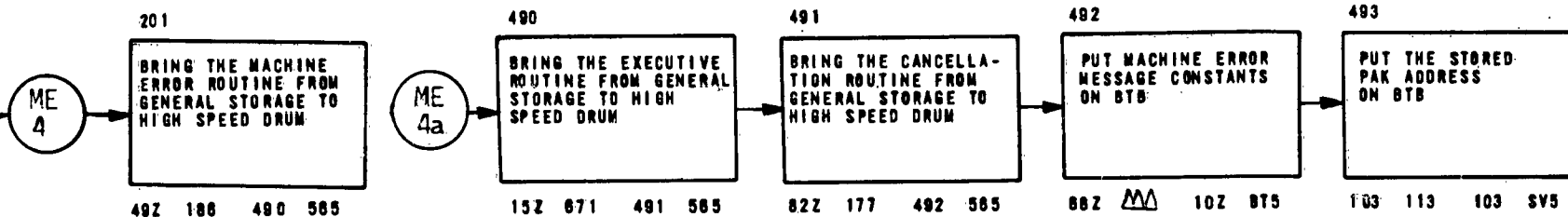


1

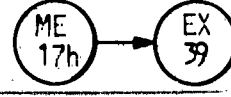
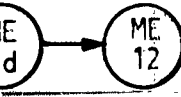
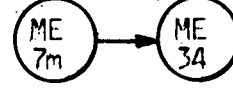
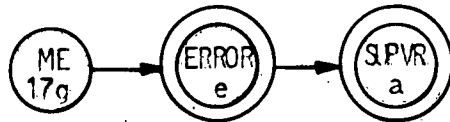
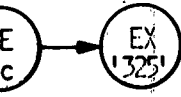
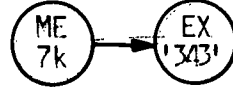
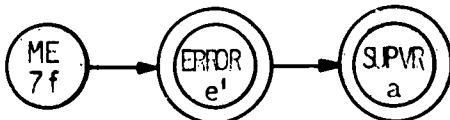
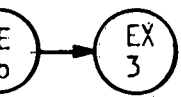
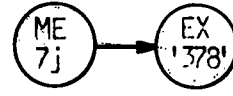
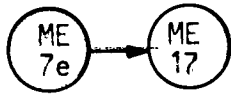
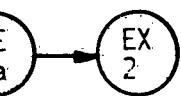


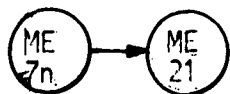
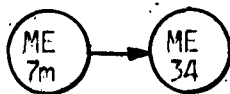
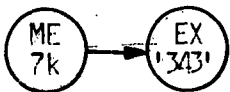
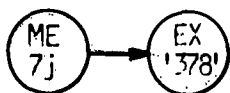
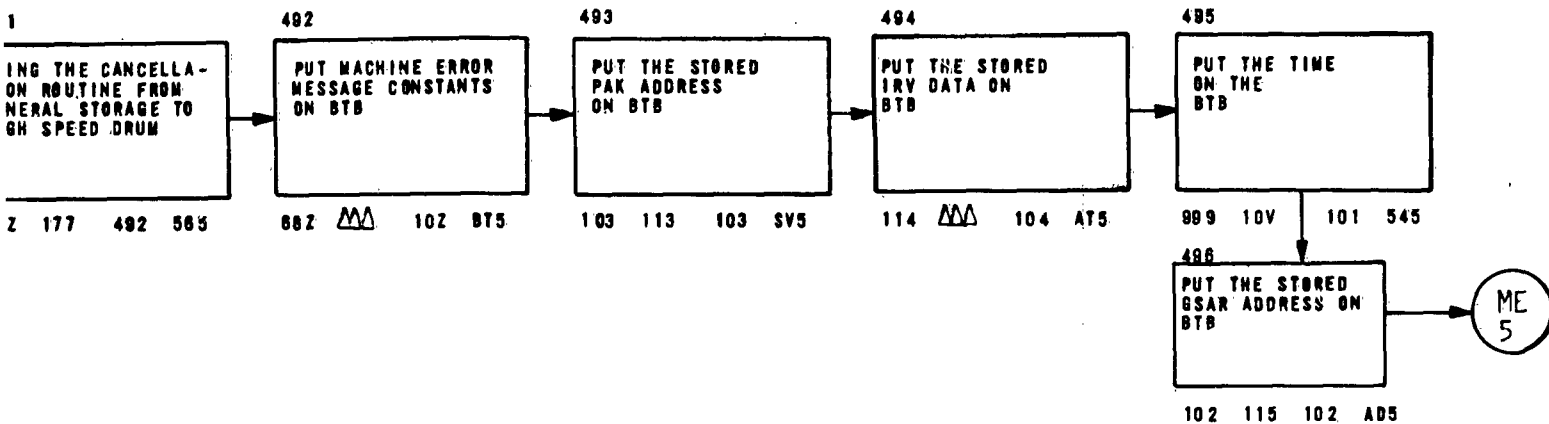
2nd MACHINE ERROR

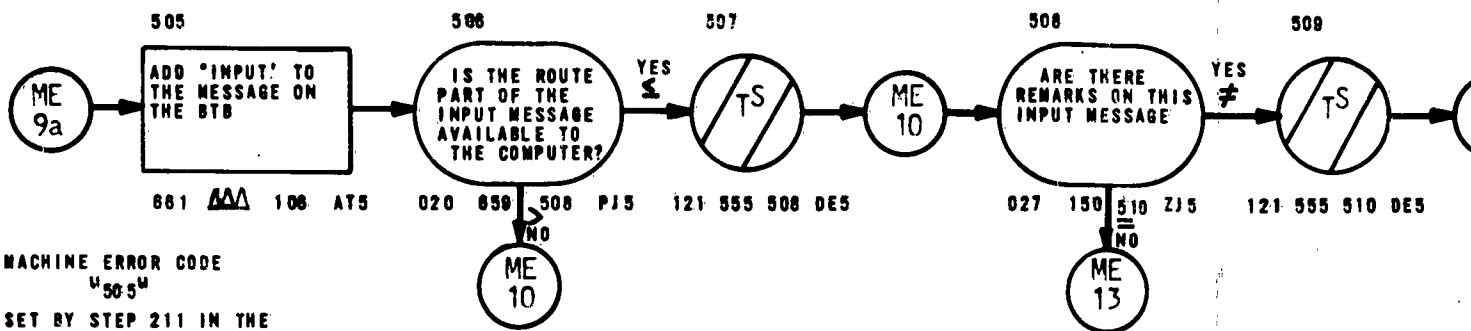




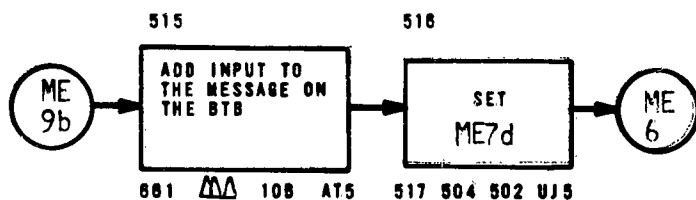
THE VARIOUS ERROR RECOVERY START STEPS ARE DESIGNATED ME9a THRU ME9u ON THE FOLLOWING PAGES



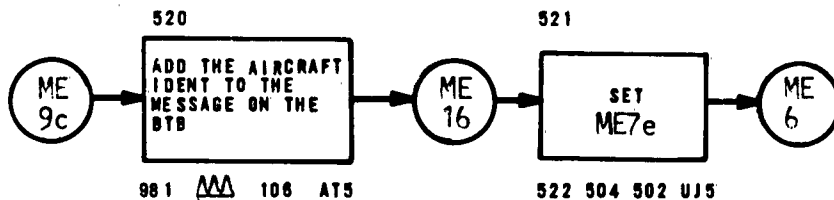
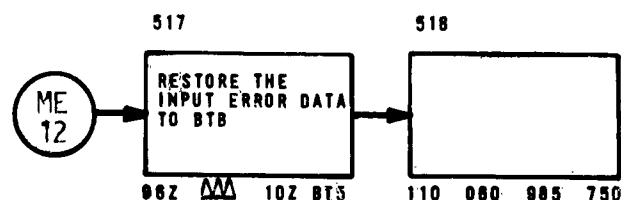




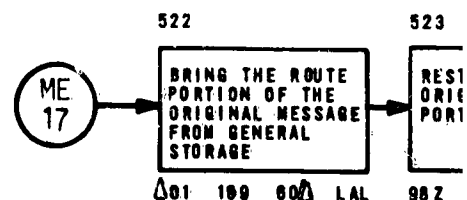
MACHINE ERROR CODE
"505"
SET BY STEP 211 IN THE
INPUT PORTION OF THE
EXECUTIVE ROUTINE

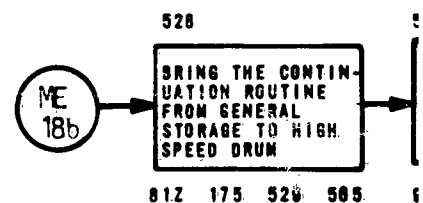
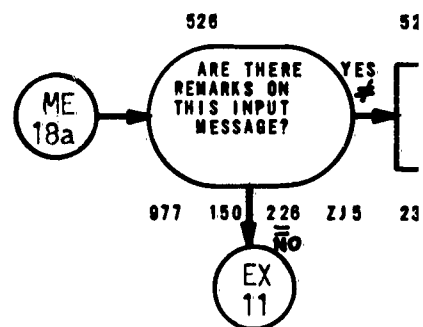
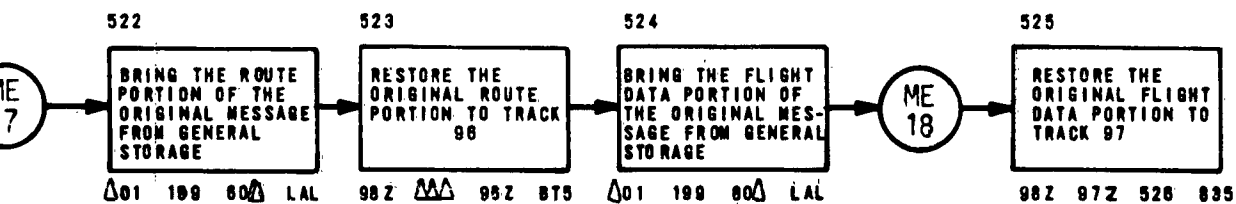
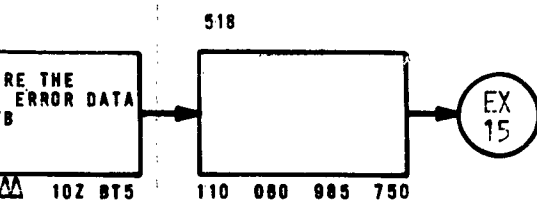
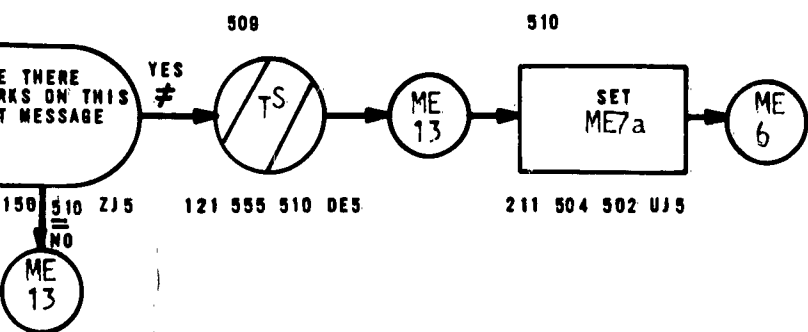


MACHINE ERROR CODE
"515"
SET BY THE INPUT
ERROR PORTION OF THE
EXECUTIVE ROUTINE

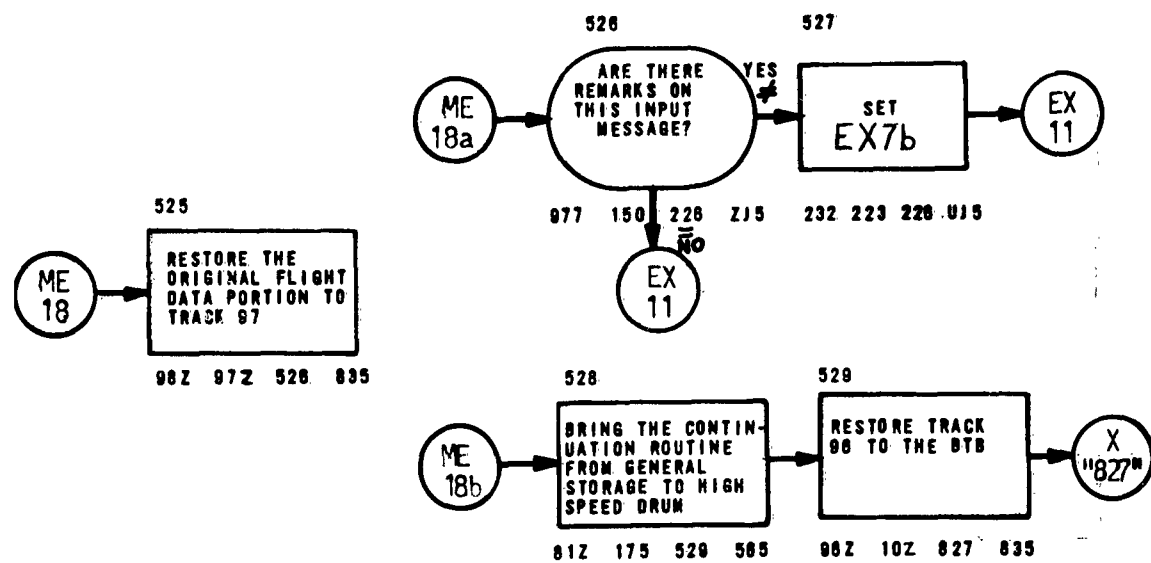


MACHINE ERROR CODE
"520"
SET BY STEPS 221 AND
240 IN THE INITIAL
PROCESSING PORTION OF
THE EXECUTIVE ROUTINE

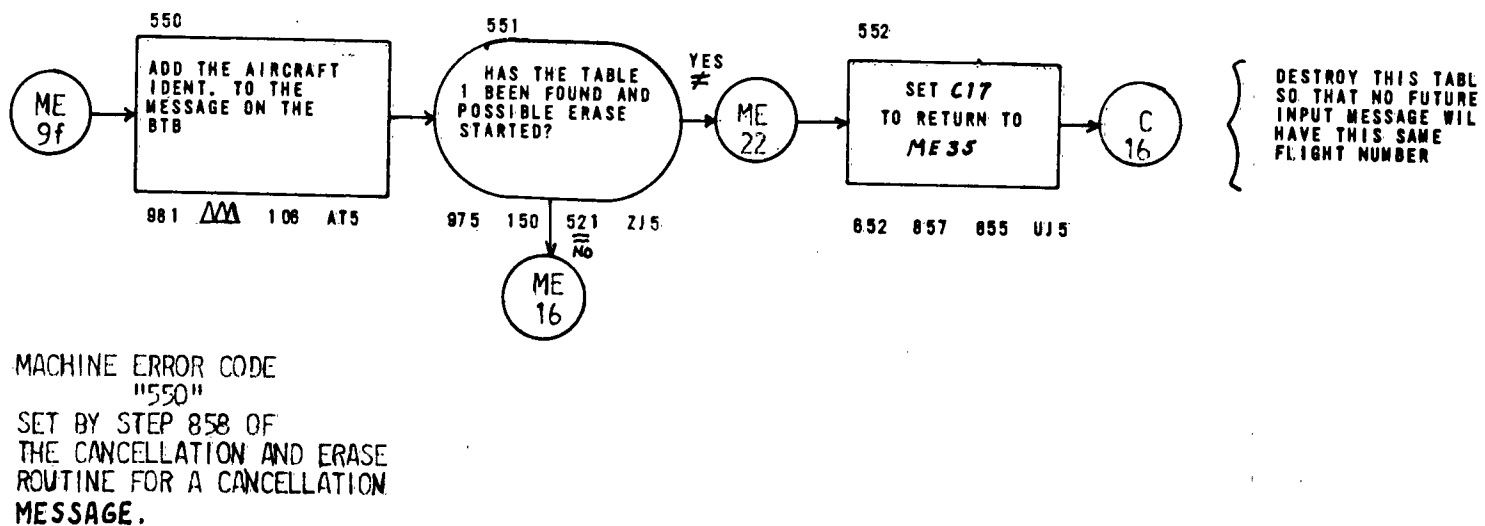
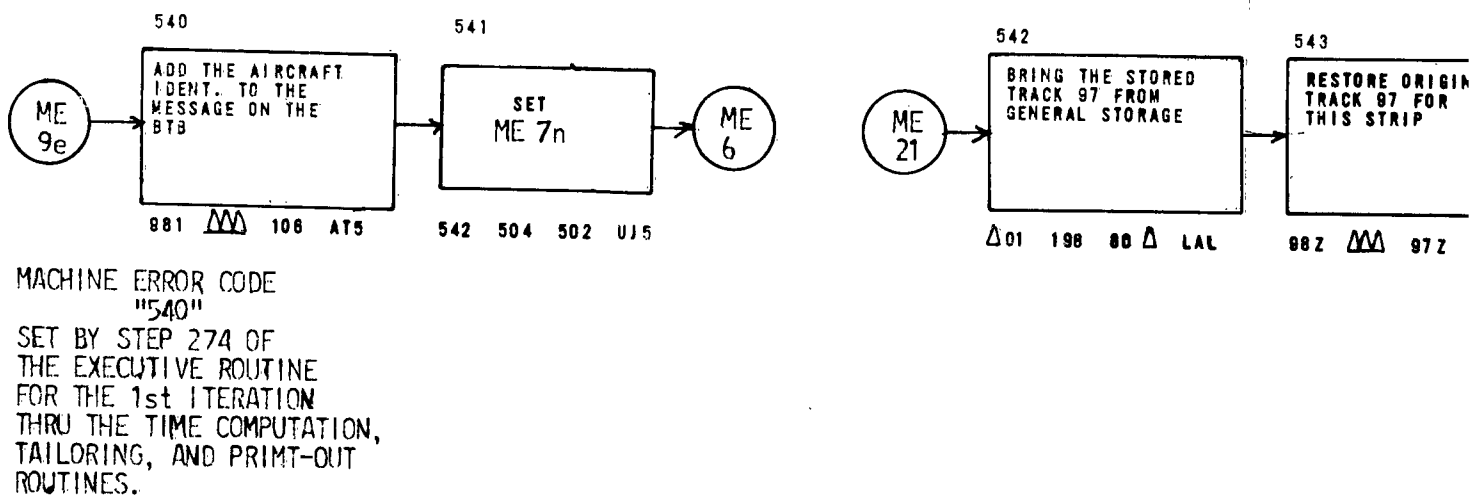
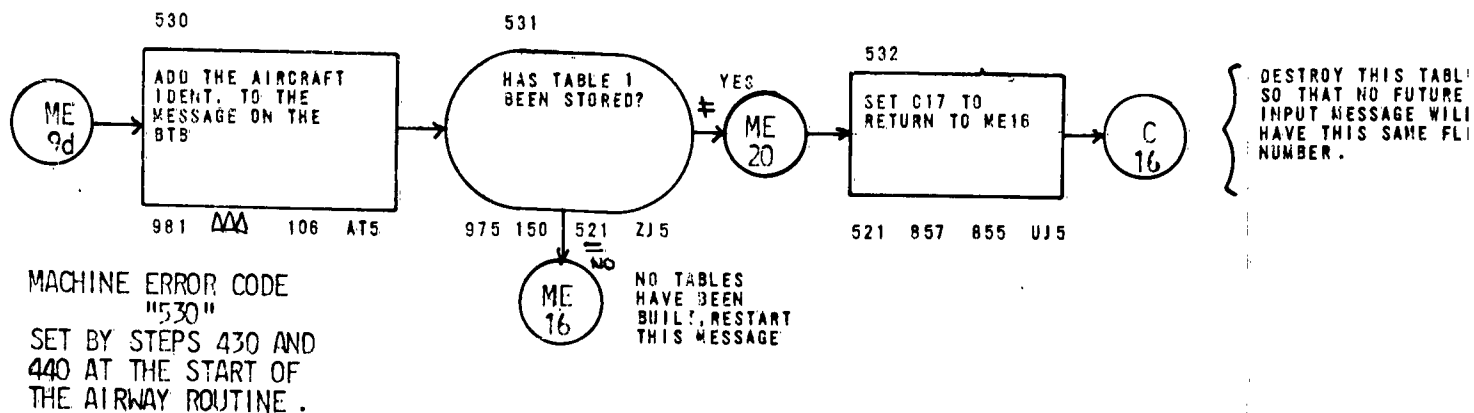


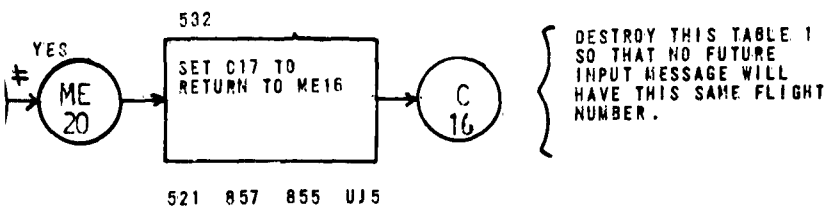


2

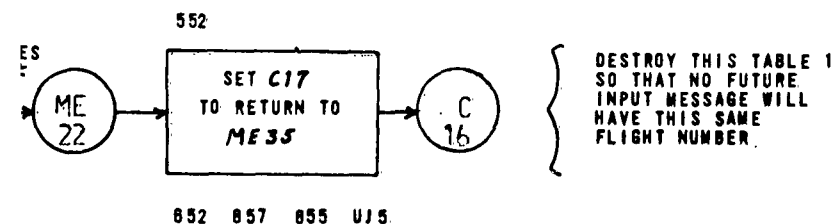
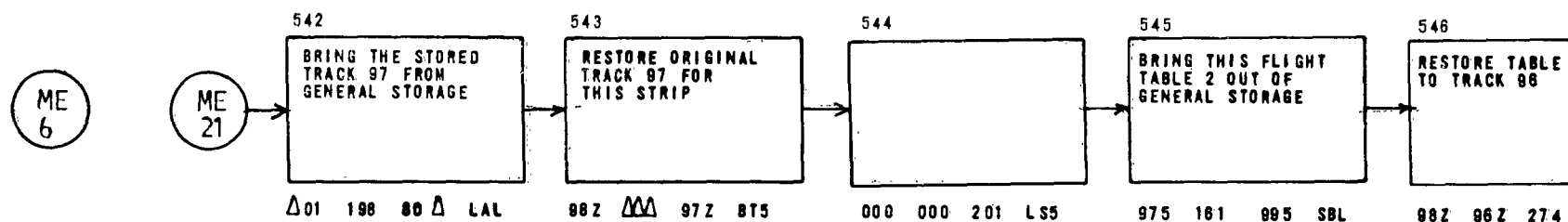


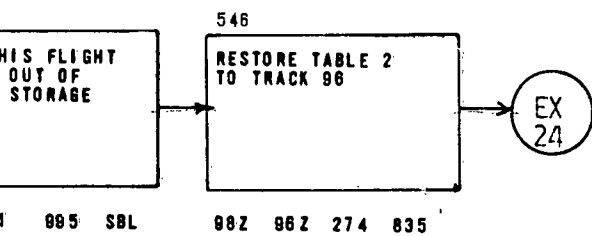
3

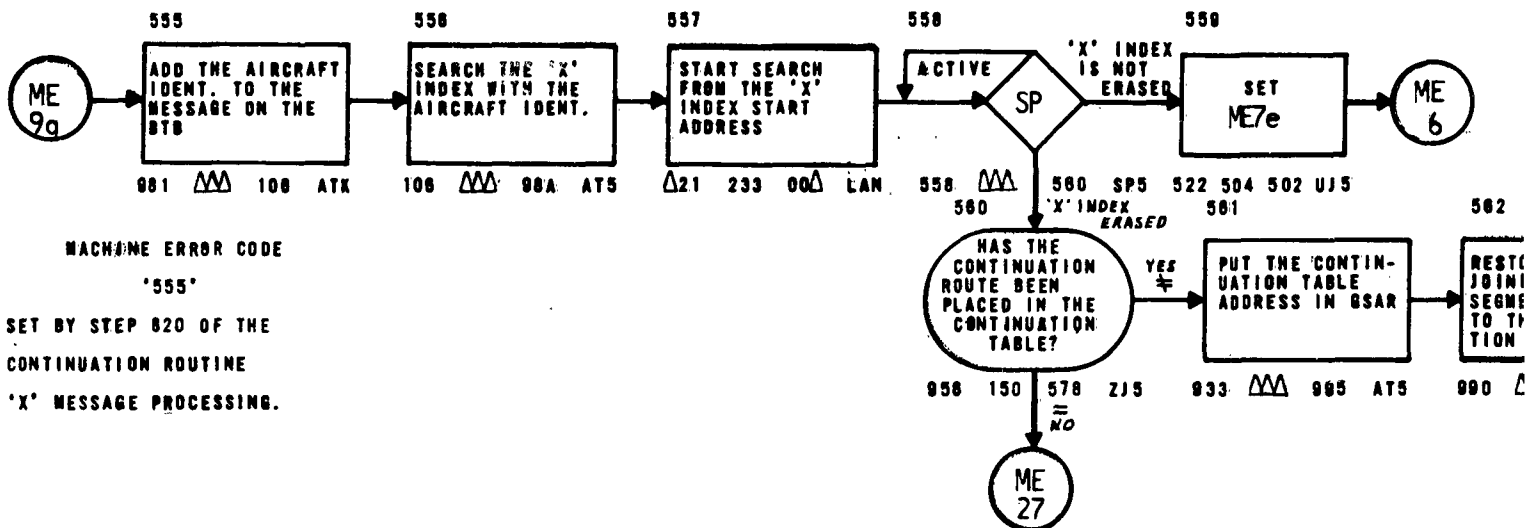




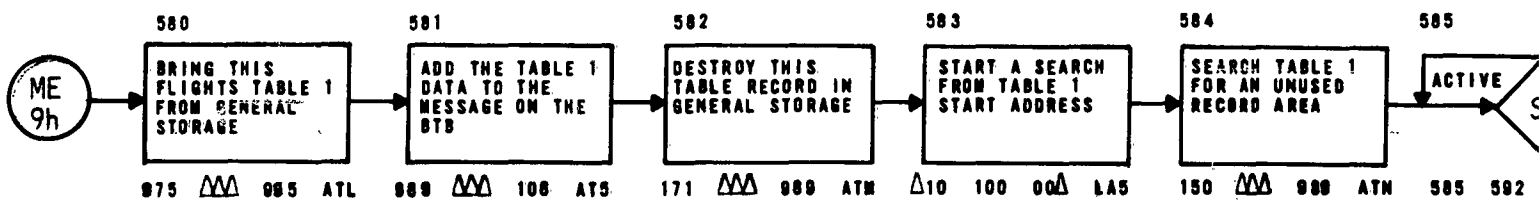
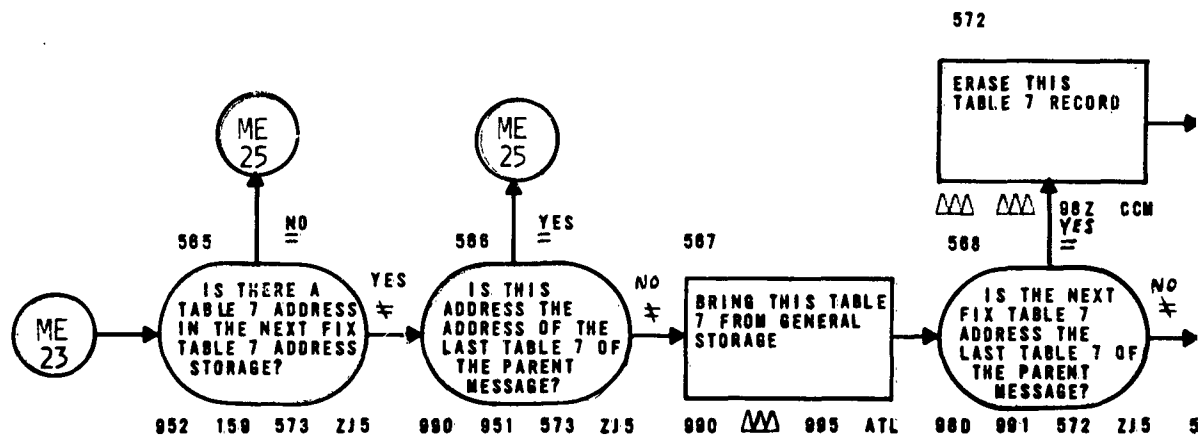
ABLES
BEEN
RESTART
MESSAGE







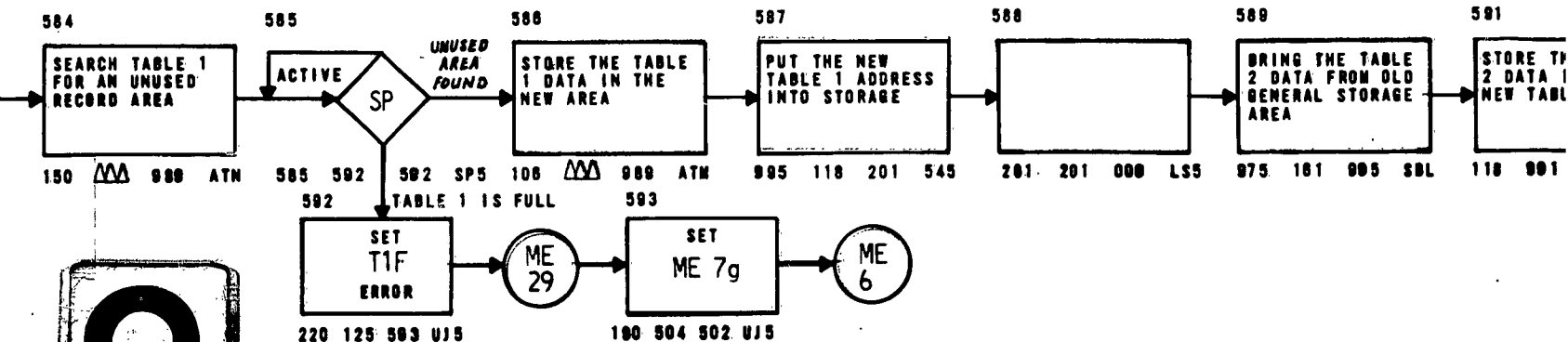
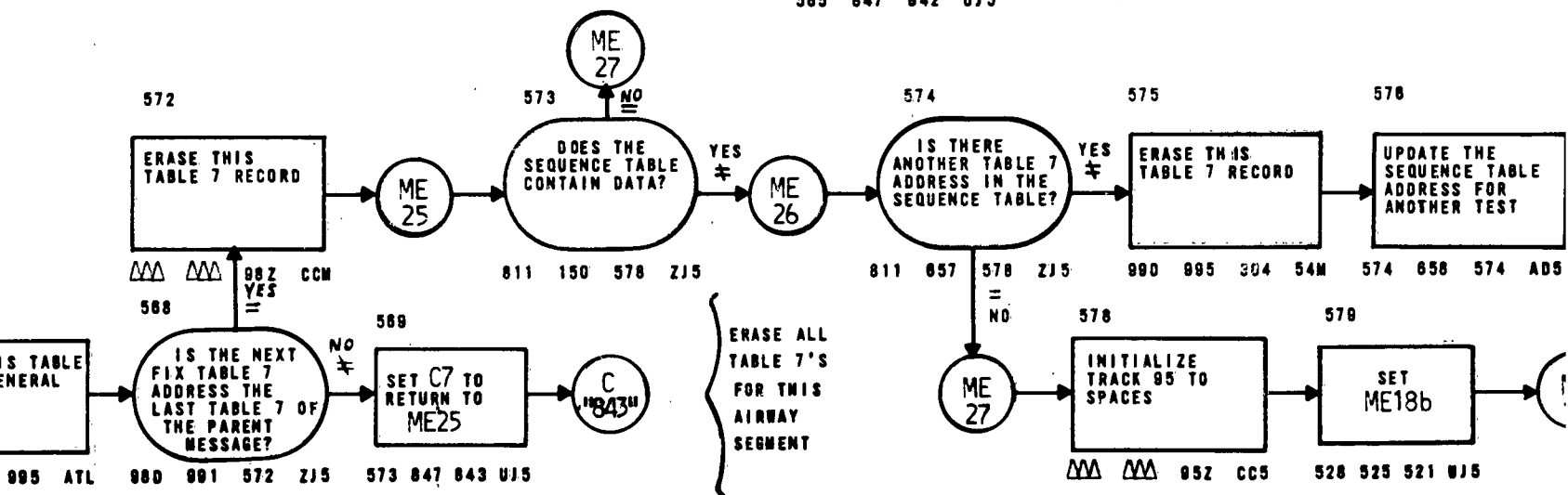
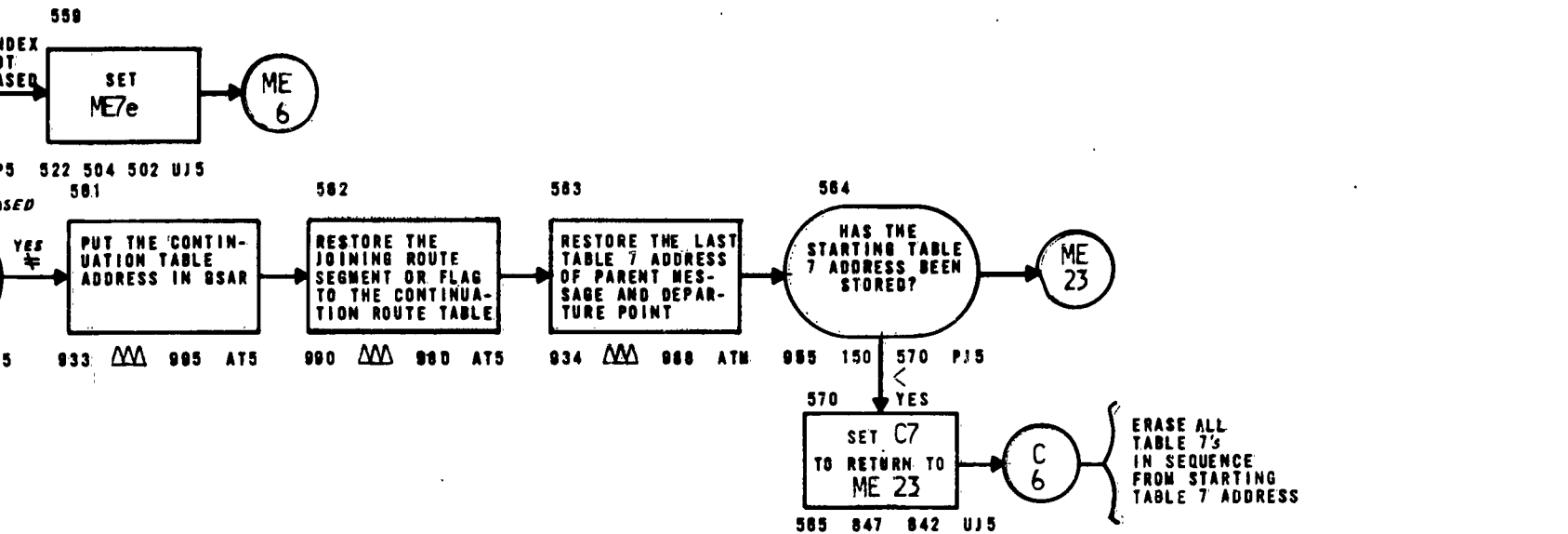
1



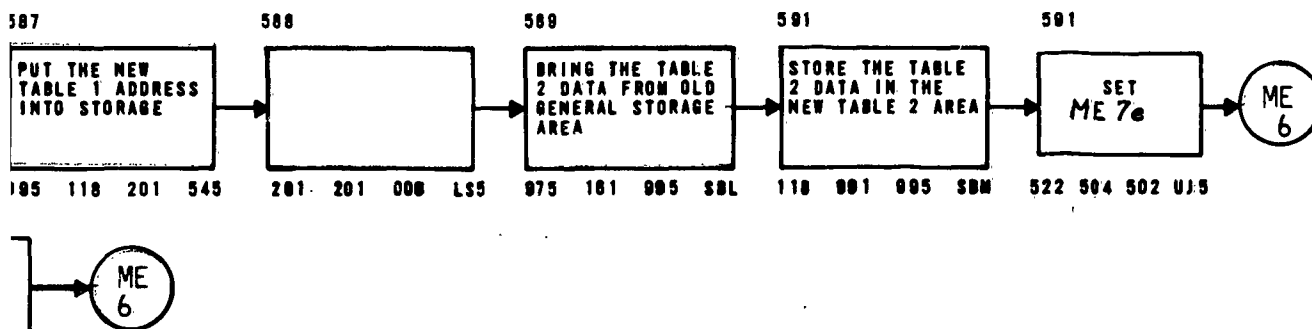
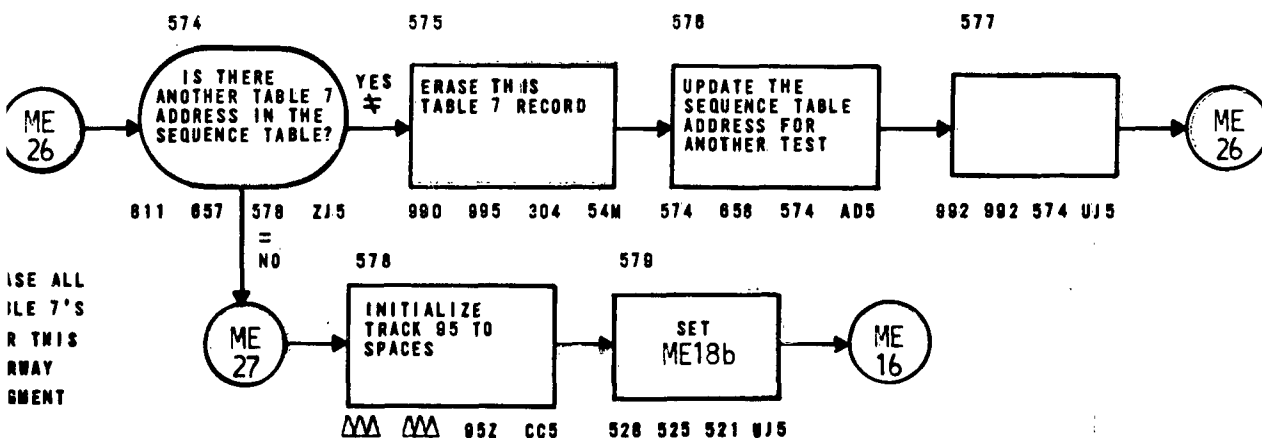
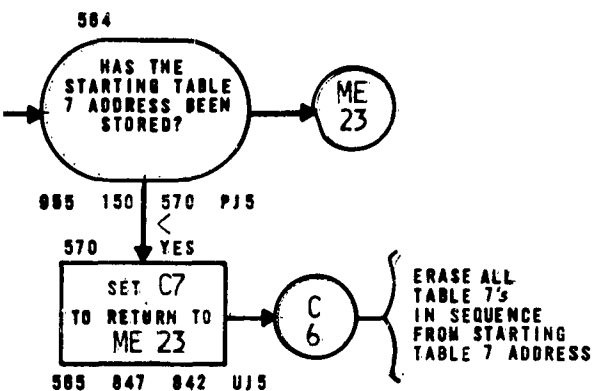
MACHINE ERROR CODE
'580'
SET BY STEP 509 AND 510 OF THE DEPARTURE MODIFICATION ROUTINE . ROUTE CHANGE.

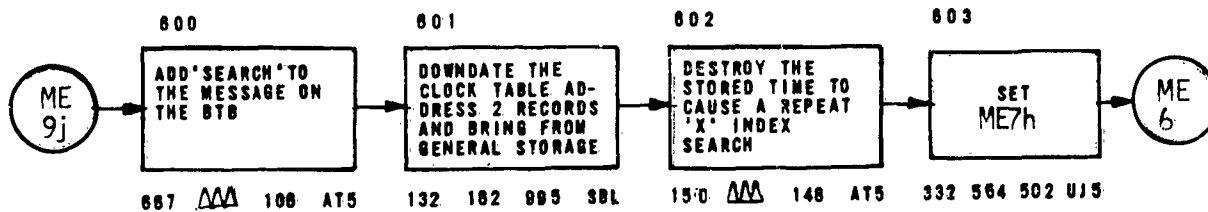
ALL TABLE 7's MAY NOT HAVE BEEN ERASED. MOVE TABLES 1 and 2 AND DESTROY THE OLD TABLE 1 AREA SO THAT NO FUTURE MESSAGE COULD HAVE THIS SAME FLIGHT NUMBER.

582
S
T
EN
220 125



2





MACHINE ERROR CODE

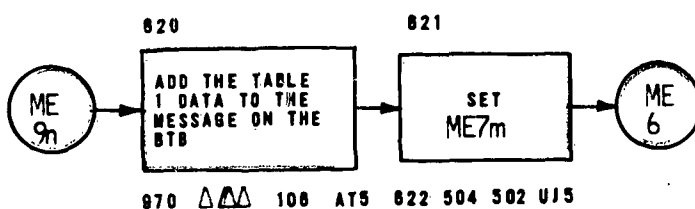
'800'

SET BY STEPS 382 AND 318 IN
THE 'X' INDEX AND TABLE 9
SEARCH PORTION OF THE EXECUTIVE
ROUTINE.

ME
9k

MA

SET BY
EXECUTIVE
RECORD

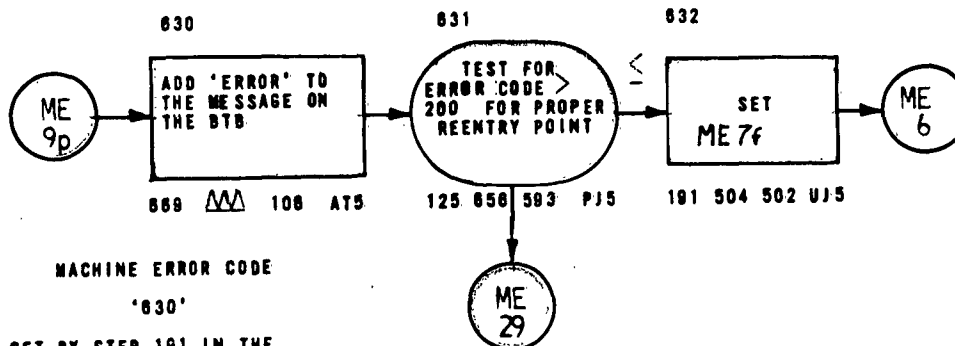
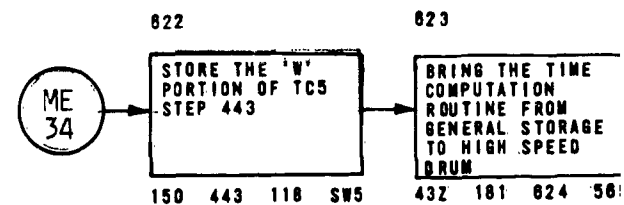


MACHINE ERROR CODE

'820'

SET BY STEPS 885 OF THE HSP
PRINTOUT AND 890 OF THE
TYPEWRITER PRINTOUT ROUTINES.

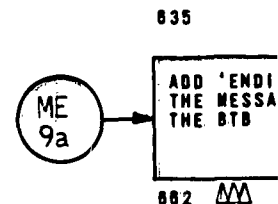
TIME COMPUTATION, TAILORING AND
PRINTOUT ROUTINES AFTER THE 1ST
STRIP.



MACHINE ERROR CODE

'830'

SET BY STEP 191 IN THE
ERROR PORTION OF THE
EXECUTIVE ROUTINE.

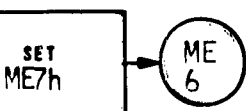


MACHINE ERROR CODE

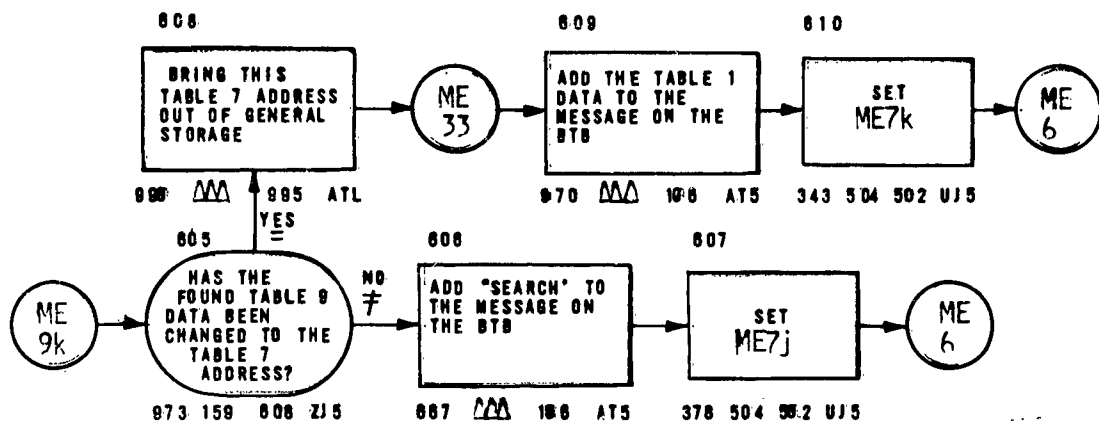
'835'

SET BY STEP 324 OF THE
EXECUTIVE ROUTINE. END
ESSING.

1



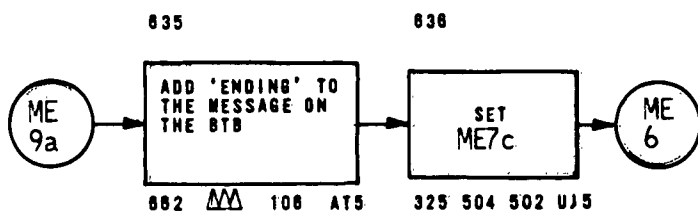
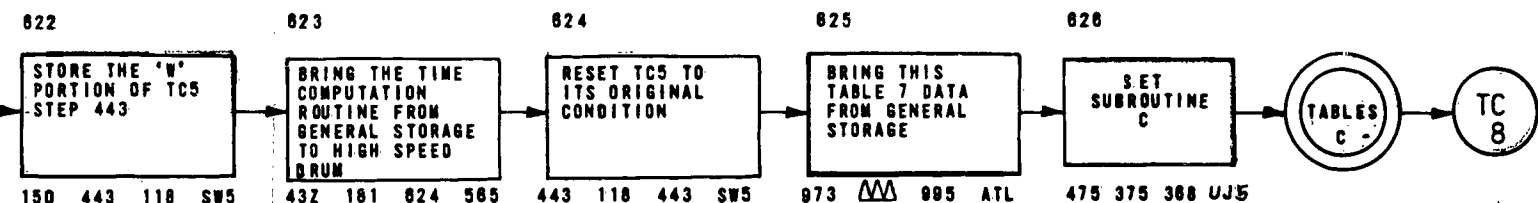
564 502 UJ5



MACHINE ERROR CODE

"005"

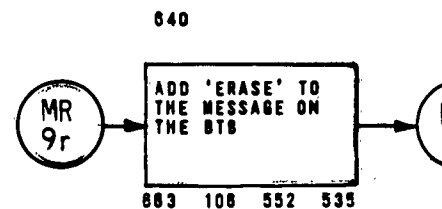
SET BY STEP 378 OF THE
EXECUTIVE ROUTINE. TABLE 9
RECORD FOUND.



MACHINE ERROR CODE

"835"

SET BY STEP 324 OF THE
EXECUTIVE ROUTINE. ENDING PROC-
ESSING.

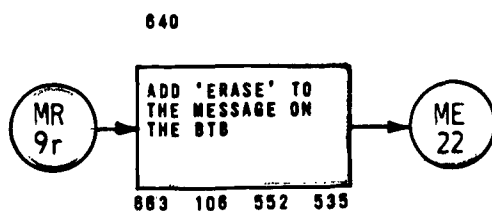
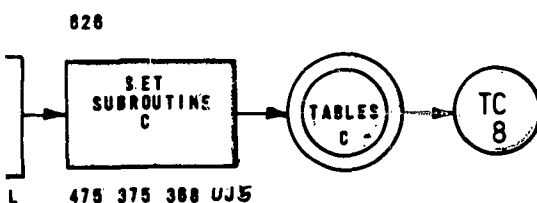
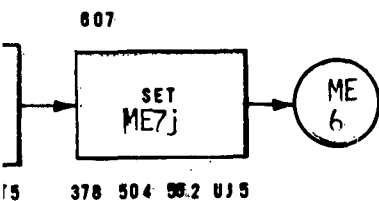
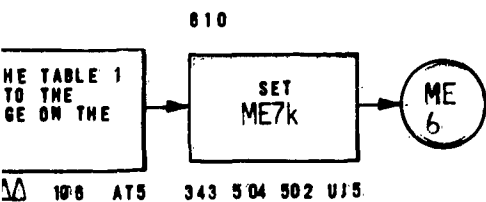


MACHINE ERROR CODE

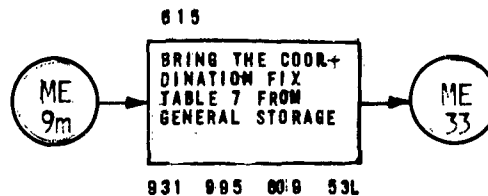
"840"

SET BY STEP 328 OF THE
EXECUTIVE ROUTINE. NORMAL DATA
ERASE.



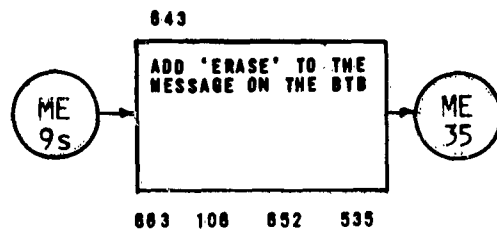


MACHINE ERROR CODE
'840'
SET BY STEP 328 OF THE
EXECUTIVE ROUTINE. NORMAL DATA
ERASE.



MACHINE ERROR CODE
'815'
SET BY STEP 833 OF THE TAILORING
ROUTINE ICM PROCESSING.

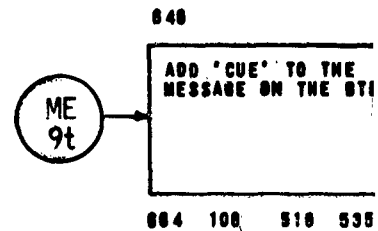




MACHINE ERROR CODE

'843'

SET BY STEP 857 OF THE CANCELLATION
AND ERASE ROUTINE. ERASE OF DATA
FOLLOWING AN ERROR.

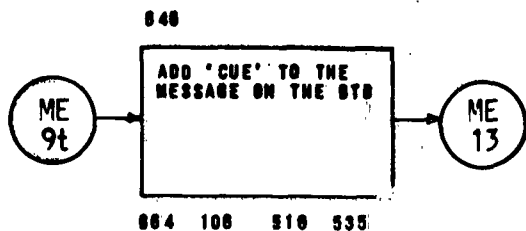


MACHINE ERROR CODE

'848'

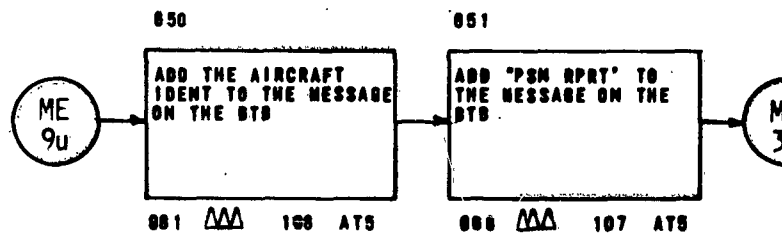
SET BY STEPS 224 OF THE
482 OF THE TIME COMPUTAT
728 OF THE HSP PRINTOUT,
THE TYPEWRITER PRINTOUT
CUE MESSAGE PROCESSING.

1



MACHINE ERROR CODE
"848"

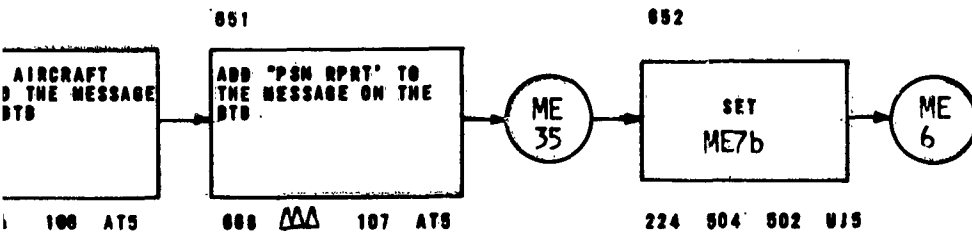
SET BY STEPS 224 OF THE EXECUTIVE,
482 OF THE TIME COMPUTATION, 700 AND
728 OF THE MSP PRINTOUT, AND 723 OF
THE TYPEWRITER PRINTOUT ROUTINES,
CUE MESSAGE PROCESSING.



MACHINE ERROR CODE
"850"

SET BY STEP IN THE
POSITION REPORT ROUTINE

2



ERROR CODE
'050'
STEP IN THE
'EPORT ROUTINE



Working Storage

812	Direct Route Sequence Table Track													
931	Δ	Δ	Δ	Δ	5	0	X	X	X	X	X	Δ	Coordination fix Table 7 address.	
933	Δ	Δ	Δ	Δ	0	0	X	X	X	X	X	Δ	Continuation route address	
934	5	0	X	X	X	X	X	Δ	X	X	X	X	Word 8 of the Continuation Route Table before route is stored.	
921	Δ	Δ	Δ	Δ	5	0	X	X	X	X	X	Δ		Last Table 7 address of a parent message.
952	Δ	Δ	Δ	Δ	5	0	X	X	X	X	X	Δ		
955	Δ	Δ	Δ	Δ	5	0	X	X	X	X	X	Δ	Starting Table 7 address.	
956	X	X	X	X	X	X	X	X	X	X	X	X		Joining route segment or flag for continuation process.
970	X	X	X	X	Δ	X	X	X	X	X	X	X	Table 1 data.	
973	Δ	Δ	Δ	Δ	5	0	X	X	X	X	X	Δ		This Table 7 address.
975	Δ	Δ	Δ	Δ	1	0	1	0	X	X	X	Δ	This Table 1 address.	
977	X	X	X	X	X	X	X	X	X	X	X	X		1st word of remarks.

The remarks beside the above constants, factors and working storages define the prime purpose as used in this routine. The flow charts will define any secondary usage of these locations.

112	P	A	R	G	S	P	T	M	O	A	T	H	Error designator codes for the supervisor's error message.
113	Δ	Δ	Δ	-	-	-	Δ	Δ	Δ	Δ	Δ	Δ	Storage for error PAK data.
114	-	-	-	-	-	-	-	-	-	-	-	-	Storage for error instruction revolver data.
115	Δ	Δ	Δ	Δ	-	-	-	-	-	-	-	Δ	Storage for error GSAR data.
117	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	2	0	2	0	Starting address for 2nd machine error in sequence.
124	Δ	Δ	Δ	Δ	Δ	Δ	6	0	0	Δ	Δ	Δ	Machine error code storage.
125	Δ	1	1	1	1	4	-	-	-	Δ	Δ	Δ	Error designator address.
132	Δ	Δ	Δ	Δ	1	1	2	8	6	0	0	Δ	Current Clock Table address.
139	X	X	X	X	X	r							Computer machine error counter.
148	Δ	Δ	Δ	Δ	Δ	Δ	Δ	X	X	X	X	Δ	Current Clock Time
150	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Spaces constant.
156	Δ	Δ	Δ	Δ	Δ	1	Δ	Δ	Δ	Δ	Δ	Δ	One constant.
159	i	i	i	i	5	0	i	i	i	i	i	i	Table 7 address comparator.
161	0	0	0	0	1	0	1	0	0	0	0	0	Table 1 address modifier.
162	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	4	+	Clock Table downdate constant.
171	r	/	1	Δ	Δ	0	0	U	T	r	r		Constant to destroy Table 1.
175	8	9	z	Δ	0	1	1	5	5	8	0	Δ	Continuation routine load factor.
177	8	5	z	Δ	0	1	1	6	6	0	0	Δ	Cancellation and erase routine load factor.
181	4	8	z	Δ	0	1	1	2	6	0	0	Δ	Time Computation routine load factor.
186	6	8	z	Δ	0	1	1	1	5	0	0	Δ	Machine Error routine load factor.
655	i	i	i	i	i	i	5	0	0	i	i	i	Error code comparator.
656	i	i	i	i	i	i	2	0	0	i	i	i	Error code comparator.
657	i	i	i	i	9	9	9	z	i	i	i	i	End of direct sequence table.
658	0	0	1	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Sequence Table update constant.
659	i	i	i	i	i	i	i	i	i	i	Δ	i	Input track comparator.

661	I	N	P	U	T	r	r		Δ	Δ	Δ	Δ
662	E	N	D	I	N	G	r	r		Δ	Δ	Δ
663	E	R	A	S	E	r	r		Δ	Δ	Δ	Δ
664	C	U	E	r	r		Δ	Δ	Δ	Δ	Δ	Δ
666	Δ	r	t	2	N	D	Δ	Δ	M	E	Δ	Δ
667	Δ	P	S	N	Δ	R	P	R	T	r	r	
669	E	R	R	O	R	r	r		Δ	Δ	Δ	Δ

Supervisor's machine error message constants.

6 7 -Z - Original Track 11 data to initialize Track 11

680	Δ	Δ	Δ	Δ	Δ	Δ	?	/	M	Δ	E	β
681	Δ	Δ	U	F	C	Δ	-	-	-	Δ	G	S
682	A	R	-	Δ	Δ	Δ	Δ	Δ	Δ	Δ	-	P
683	A	K	Δ	-	-	-	Δ	I	R	V	Δ	Δ
684	-	-	-	-	-	-	-	-	-	-	-	-
685	Δ	r	t	i	i	i	i	i	i	i	i	i
686	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ
687	r	r		Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ	Δ

Supervisor's machine error message constants.

APPENDIX XVIII

MAIN FRAME PLUGBOARD WIRING CHARTS

MODEL 1 UNIVAC FILE-COMPUTER EXTERNAL PROGRAM CHART

CUSTOMER: NAFEC			Phase I Lab.			APPLICATION: Operational Plugboard			PROGRAMMED BY: DATE October 30, 1961		PROGRAM NO.	
STEP NO.	V ₁	V ₁ SHIFT	PROCESS	V ₂	V ₂ SHIFT	R	R SHIFT	NEXT STEP	REMARKS	IN		
51	U adr	Ustrv	CP	Vadr	Vstrv			BR 1	Jump Negative			
52	IRV		AT			PAK	R2	Uni2-2	W of IRV → PAK and NI	UNI 3 out		
53	V1 51	V1 51 SH	PR52		V2 51 SH	Vadr	V4 55 SH	Uni6-2	AT and JUMP			
54	V1 52		PR53			SRV		IN 55	AT and SHIFT			
55	V1 53	Wstrv	PR54			R53		UNI2-1			OUT 54	
56	V adr		PR55			GSAR		RUR-1				
57	V1 54		+CK	FS2-9		IRV		NI			RUR1	
58	GSBZ		BT			Uadr		IN59	Load Sub-Program to HSD			
59	GSAR		PR57	V2 57	L1	R56		RUR-2			OUT 58	
60	V1 57	R9	PR51	V2 51	R9			BR 4			RUR2	
61	V1 60		PR59	FS2-8	L9	R57		NI			BUS 817	
62	V1 55	CT2	PR56			CDR		NI	Determine Flight Direction			
63	CT18A		± SQ	CT18B		CT18C		64			OUT 68	
64	R66		PR61	V2 63		R63		65			OUT 63	
65	V1 64		ML	W adr		R64		66			OUT 64	
66	V1 65		PR64	V2 65		R65	R1	67	Square Root		OUT 65	
67	V1 66		PR60	V2 64				BR 5			OUT 66	
68	V1 67		PR62			V2 67		IN63			BUS d17	
69	CT24A	CT12A	PR66	R 61	CT12B	R59		RUR-3	Load Patterns		NST41A	
70	GSBZ		BT			CT12C		BUSc28			RUR3	
71	NI 62	V1 53 SH	PR67	V2 60	V2 53 SH			BR 2	Jump Positive			
72	RA	CT24B	PR68		CT24C	CT42D	CT24D	BUSb28	Shift A/C Identification		ST41A	
73	V1 71		PR 69	V2 71		R62		CDR pulse in				
74	CT32		PR 72			R52		BUS V11	Determine message kind		BUS R27	
75	V2 69	EA 5	PR 74			R69		RUR-4	Ten instruction subroutine			
76	FS2-2	EA 3	PR 75			R74		NI	Start Address End or Day Rt.			
77	R77		PR 76			FS1-2	EA3	BUS V16		step cl. 1 out		
78	V177		PR 77		CT7	V1 78		BUS U16	Machine Error		BUS U13	
79	R82	L5	PR 78			FS1-3		IN80	Recovery		DET OUT PS 13	
80	V1 75		PR 79			FS1-4		IN81			OUT 79	
81	V1 82	EA6	PR 80			R76		BUS J2			OUT 80	
82	V1 76	EA9	PR 81			R81		BUS W12	Plugboard Start		NS ATT SW 1	
83	V1 91		BT			R55		UNI 6-1	BT and Jump			
84	RD		AT			FS2-3		UNI 7-1			BUS N13	
85	PAK		PR84			I/O-0	EA4	BUS N17			BUS Q16	
86	FS1-6	CT17A	PR85		CT48A	I/O-1	CT48B	CT16B			BUS M17	
87	R84		PR86			CT39B		OE1 in	Tracker		NST16B	
88	V1 80		PR87			R85		CT48C			BUS N16	
89	CT33A	CT34A	PR88		CT34B	CT33B	CT35A	IN90			BUS I16	
90	CT35B	V1 89 SH	PR89		V2 89 SH	CT36A	SH 89	IN92			OUT 89	
91	V1 73	V1 71 SH	PR71	V2 73	V2 71 SH			BR3	Jump Equal			
92	CT36B	V1 90 SH	PR90			CT36C		CT36D	Tracker		OUT 90	
93												
94	FS1-7	R7	AT			PAK		BUS I2	Machine Error Recovery		BUS E13	
95			CC			I/OE		CT22A	Tracker		BUX C16	
96	FS1-8		PR92			FS1-8		BUS C18	Timing to Clear Selectors		Clear	
97	U adr	CDR in grp 1	PR94			CDR		BUS A18	Determine msg. kind for CUE			
98												

U 1386.3

MODEL 1 UNIVAC® FILE COMPUTER CONTROL CHART #1

CUSTOMER: NAFEC Phase 1 LAB			APPLICATION: OPERATIONAL PLUGBOARD			PROGRAMMED BY: DATE Oct. 30, 1961		PROGRAM NO.															
DEMAND UNITS (DMD)																							
UTS	TEST IN	NOT READY	READY	DEMAND IN	DEMAND OUT	SPECIAL OUT	TRACK SWITCH																
0																							
1	NS T11A	TEST IN	DEMAND IN	READY	Bus v17	Bus w17	NS T9B																
2																							
3																							
4	ST 11B	TEST IN	DEMAND IN	READY	ST10B	ST10A	ST9B																
5																							
6																							
7																							
8																							
9																							
UNIBUSES (U/B)																							
NO	IN	IN	IN	IN	OUT	START TO: C ALT SW 1 ERROR SIGNALS <table border="1" style="width:100%; border-collapse: collapse;"> <tr><td>TYPE</td><td>TO</td></tr> <tr><td>PARITY</td><td>CT47A</td></tr> <tr><td>NO'FLOW</td><td>+ - o' Flow</td></tr> <tr><td>NO'FLOW</td><td>N o' Flow</td></tr> <tr><td>NO'FLOW</td><td>ARITH.</td></tr> <tr><td>ARITH.</td><td>CT46B</td></tr> <tr><td>GS PROG</td><td>CT46A</td></tr> </table>				TYPE	TO	PARITY	CT47A	NO'FLOW	+ - o' Flow	NO'FLOW	N o' Flow	NO'FLOW	ARITH.	ARITH.	CT46B	GS PROG	CT46A
TYPE	TO																						
PARITY	CT47A																						
NO'FLOW	+ - o' Flow																						
NO'FLOW	N o' Flow																						
NO'FLOW	ARITH.																						
ARITH.	CT46B																						
GS PROG	CT46A																						
1	BR1 +	BR1 0	BR2 -	BR2 0	NI																		
2	BR3 +	BR3 -	OUT 52	OUT 55	NI																		
3	UNI 4 OUT	RUR 4	BR1 -	BR3 0	IN 52																		
4	BR2 +	BR4 0	UNI 6 OUT	Clear PSH25	UNI 3 IN 4																		
5	DEL OUT PS6	DEL OUT PS7	DEL OUT PS8		CT41A																		
6			OUT 53	OUT 83	UNI 4 IN 2																		
7			Bus 128	OUT 84	CT11A																		
8																							
HI-SPEED CONTROL LINES (HCL)						STEP CLEAR (SC)		STEP REPEAT (SR)															
NO	IN	YES	NO	NO	IN	OUT	NO	FROM															
W					1	ST46A	IN 77																
X					2	ST 46B	Bus T23																
Y					3	ST 47A	PU PS 12																
Z					4																		
FUNCTION SEQUENCE (FS)				OUT EXPANDERS (OE)																			
NO	SET	PROBE	OUT	NO	IN	out 1	OUT 2																
1				1	OUT 87	Bus G8	Bus b25																
2				2	ST16B	Bus e25	Bus m28																
3				3																			
4				4																			
				5																			
				6																			
				7																			
				8																			
FUNCTION DELAY (FD)				OUTPUT CONTROL LINES (OCL)																			
NO	IN ₁	IN ₂	OUT	NO	FROM	NO	FROM																
A	Bus G17	Bus F17	TIMER ON	A	ST9A	F																	
B				B	NST9A	G	NST17B																
C				C		H																	
D				D		I																	
				E		J																	
REMARKS:																							

U. 1356.4

MODEL 1 UNIVAC^R FILE-COMPUTER CONTROL CHART #2

CUSTOMER:		APPLICATION:		PROGRAMMED BY: DATE		PROGRAM	
NAFEC Phase I LAB		OPERATIONAL PLUGBOARD		Oct. 30, 1961			

READ URA (R)		
NO	IN	OUT
1	OUT 56	IN 57
2	OUT 59	IN 60
3	OUT 69	IN 70
4	OUT 75	UNI 3 IN 3

WRITE URA (W)		
NO	IN	OUT
1		
2		
3		
4		

WRITE & CHECK URA (W/C)		
NO	IN	OUT
1		
2		
3		
4		

CHANNEL SEARCH EQUAL (ECS)		
NO	IN	OUT
1		
2		
3		
4		

CHANNEL SEARCH UNEQUAL (UCS)		
NO	IN	OUT
1		
2		
3		
4		

CONDITION COMPARE (C/C)		
NO	IN	OUT
1		
2		
3		
4		

CLEAR BLOCK TRANSFER BUFFER (CLBTB)		
NO	IN	OUT
1		
2		
3		
4		

CLEAR GEN'L STORAGE BUFFER (CLGSB)		
NO	IN	OUT
1		
2		
3		
4		

BRANCHING (BR)				
NO	IN FROM	+	-	O
1	OUT 51	UNI 1 IN 4	UNI 3 IN 2	UNI 1 IN 3
2	OUT 71	UNI 4 IN 4	UNI 1 IN 2	UNI 1 IN 1
3	OUT 91	UNI 2 IN 4	UNI 2 IN 3	UNI 3 IN 1
4	OUT 60	Bus i 17	Bus h 17	UNI 4 IN 3
5	OUT 67	Bus c 17	Bus b 17	Clear PS h 25
6				
7				
8				
9				
10				
11				
12				

CHANNEL SEARCH PROBE & WAIT (CS P/W)				
NO	IN FROM	+	-	O
1				
2				
3				
4				

CHANNEL SEARCH PROBE (CS P)					
NO	IN FROM	ACTIVE	+	-	O
1					
2					
3					
4					

CODE DISTRIBUTOR (CD)			
ALPHA/NUMERIC IN:			
V1 SH 74			
GROUP 1 IN	GROUP 2 IN	GROUP 3 IN	GROUP 4 IN
V1 SH 97			
NO	OUT	NO	OUT
0	R1	;	+
1	Bus a 35	A	/
2	R3	B	S
3	R4	C Bus W 57	T
4	R5	D Bus V 63	U
5	R6	E Bus W 58	V
6	R 7	F	W
7	R8	G Bus V 62	X Bus X 62
8	R9	H	Y
9	R10	I Bus W 62	Z

CDR PULSE IN:			
OUT 73			
NO	OUT	NO	OUT
0	Bus S11	3	Bus M11
1	Bus R11	4	Bus F9
2	Bus Q11	5	Bus G9
		6	Bus P12
		7	Bus L11
		8	Bus O12
		9	Bus N11

U 1286.8

CUSTOMER NAFEC Phase I LAB		APPLICATION OPERATIONAL PLUGBOARD		PROGRAMMED BY: DATE Oct. 30, 1961		PROGRAM NO.	
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INPUT CONTROL LINES		INDICATORS		PROGRAM SELECTS				
no	to	no	from	no	in	delay out	drop out	
a		1		1	ST25	Bus M13	Bus F8	
b		2		2	SCO R		Bus x27	
c		3		3	SCO W		PU T2	
d		4		4	SCO U		PU T3	
e		5		5	SCO V		PU T6	
f		switch in		6	NST12D	UNI5 IN4	Bus p27	
g		switch out		7	NST30B	UNI5 IN3	Bus g27	
h		BREAKPOINTS		8	ST41B	UNI5 IN2	Bus i28	
i				9				
j				10	Bus E8	NI	Bus i28	Bus V27
k				11	ST22B	Bus L13	Bus k28	PU T48
l		1	CT25	12	STEP CLEAR	Bus V13	PU T7	
		2	CT26	13	Bus W17	IN 79	PU T8	
		3	CT37	14	Bus E9	Bus O17	Bus T11	
TIMER				15	Bus K11	Bus P17	Bus U11	
on	OUT FUNCTION DELAY A			16	NS T36 D	Bus G16	Bus K17	
off	Bus I 17			17	h25-BR5-0	g24-IN96	H24-SCO Q	
TIME - OUT PULSES				18	h25-SCO-S	g24 NOT WIRED	H25-ST43	
no	to			19	h25-SCO-S	g24 NOT WIRED	H25-ST43	
A	Bus U63			20	h25-SCO-S	g24 NOT WIRED	H25-ST43	
B	Bus F 13			21	h25-SCO-S	g24 NOT WIRED	H25-ST43	
C	Bus U62			22	h25-SCO-S	g24 NOT WIRED	H25-ST43	
D				23	h25-SCO-S	g24 NOT WIRED	H25-ST43	

SPECIAL CHARACTER OUTS					
no	to	no	to	no	to
Q	Clear PS H24	T	Bus H17	W	PU PS 3
R	PU PS 2	U	PU PS 4	X	
S	Clear PS h24	V	PU PS 5	Y	

ALTERNATE SWITCHES		
no	select	common
1	NI	START
2	Bus D3	SEL HOLD B+
3	Bus I11	SEL HOLD B+
4		
5	Bus v28	CT10A
6	Bus D1	SEL HOLD B+

MODEL 1 UNIVAC FILE-COMPUTER SELECTOR CHART

CUSTOMER:			APPLICATION:		PROGRAMMED BY: DATE	PROGRAM NO.
NAFEC Phase I LAB			OPERATIONAL PLUGBOARD		Oct. 30, 1961	
T #	PICK-UP FROM	GROUND	SELECT	COMMON	NON-SELECT	
1						
2	B+PS3	Comp.	CT3A	V1 SH 62	CT 13	
3a	B+ PS4	Comp.	R 7	ST 2	CT 6A	
b			R 1	NST6B	R 2	
4						
5						
6a	B+PS5	Comp.	R 5	NST3A	R 6	
b			R 3	ST13	CT 3B	
c			R 4	NST13	R8	
7	B+PS12	Comp.	EA6	V2 SH 78	V1 SH 82	
8	B+PS13	Comp.	Bus G13	Bus W16	Bus T17	
9a	Bus C1	Comp.	C-I/O A	Bus d25	C-I/O B	
b			T/S DS4	Bus c25	T/S DS1	
10a	Bus B1	Comp.	Spec Out DS4	C Alt SW5	Bus x17	
b			Demand Out DS 4	Bus w28	Not Wired	
11a	Bus B 2	Comp.	Test in DS4	UNI 7 Out	Test in DS 1	
b		Comp.	Bus D16	NST16A	Bus R16	
12a	Bus 127		Not Wired	V1 SH 69	Bus d35	
b			R8	V2 SH 69	R11	
c			CT30A	R70	ISP	
d			CT30B	Bus a28	PU PS 6	
13	Bus w27	Comp.	CT6B	NST2	CT6C	
14						
15						
16a	Bus X27	Comp.	CT21A	Bus x28	CT11B	
b			OE 2 IN	Out 86	IN 87	
17a	Bus W27	Comp.	EA1	V1 SH 86	Not Wired	
b			Not Wired	Bus f25	C-1/0 G	
18a	Bus v27	Comp.	BTB5	V1 63	GSB3	
b			BTB1	V2 63	GSB6	
c			BTB0	R 63	GSB1	
d						
19						
20						
21a	Bus C2	Comp.	Bus B16	ST16A	Bus M16	
b						
22a	Bus U27	Comp.	Bus L16	Out 95	Bus P16	
b			IN PS 11	ST37	Bus n28	
23a						
b						
24a	Bus m27	Comp.	V1 59	V1 69	V1 61	
b			CT30C	V1 SH 72	EA 9	
c			CT30D	V2 SH 72	R6	
d			CT31	R SH 72	EA5	

U 1386.7

MODEL 1 UNIVAC FILE-COMPUTER SELECTOR CHART

CUSTOMER:			APPLICATION:		PROGRAMMED BY: DATE	PROGRAM NO.
NAFEC Phase I LAB			OPERATIONAL PLUGBOARD		Oct. 30, 1961	
T #	PICK-UP FROM	GROUND	SELECT	COMMON	NON-SELECT	
25	Bus C3	Comp.	IN PS1	BKPT 1	STOP	
26	Bus B3	Comp.	Bus K 13	BKPT 2	STOP	
27						
28						
29						
30A			BTP	ST 12 C	GSP	
B			CT 43	ST 12D	PU PS 7	
C	Bus g27	Comp.	CT 42 A	ST 24 B	R5	
D			CT 42 B	ST 24 C	EA 6	
31	Bus h 27	Comp.	CT 42 C	ST 24 D	R 4	
32	B+ PS 14	Comp.	FS 2 - 0	V1 74	CT 44	
33A	Bus I 27	Comp.	V1 86	V1 89	V1 72	
B			I/O 3	R 89	I/O - 2	
34A	Bus H 27	Comp.	EA 1	V1 SH 89	NOT WIRED	
B			L 2	V2 SH 89	NOT WIRED	
35A	Bus G 27	Comp.	Bus c 35	R SH 89	NOT WIRED	
B			V1 89	V1 90	RB	
36A			I/O 5	R 90	I/O 4	
B			FS 1 - 6	V1 92	FS2 - 3	
C			I/O 7	R 92	I/O 6	
D	Bus F 27	Comp.	Bus J 17	OUT 92	IN PS 16	
37	Bus A3	Comp.	CT 22 B	BKPT 3	STOP	
38						
39A						
B	B+ PS 1	Comp.	SRV	R 87	V1 84	
40						
41A			IN 72	UNI 5 OUT	IN 69	
B	Bus u27	Comp.	PU PS 8	NS T 43	Bus o27	
42A			R 2	ST 30 C	EA 7	
B			EA 9	ST 30 D	R 3	
C	Bus h 28	Comp.	R SH 66	ST 31	EA 8	
D			GSBF	R 72	RA	
43	Bus g 28	Comp.	CLEAR PS H25	ST 30 B	CT 41 B	
44	B+ PS 15	Comp.	FS 2 - 1	NST 32	FS 2-2	
45A						
B						
46A			STEP CLEAR 1	GS Prog. Error	NOT WIRED	
B	Bus H 11	Comp.	STEP CLEAR 2	ARITH. ERROR	NOT WIRED	
47A			STEP CLEAR 3	PARITY ERROR	NOT WIRED	
B	Bus G 11	Comp.				
48A			L 2	V2 SH 86	NOT WIRED	
B			Bus b 35	R SH 86	NOT WIRED	
C	B+ PS 11	Comp.	Bus H 16	OUT 88	Bus L 17	
D						

U 1356.7

I
II
III
IV
V
VI
VII
VIII
IX
X
XI
XII
XIII
XIV
XV
XVI
XVII
XVIII
XIX
XX

BUSSES OPERATIONAL PLUGBOARD Oct. 30, 1961

PROGRAM CONTROL CHART # 4

NAFEC Phase I LAB		BUSSES		OPERATIONAL	PLUGBOARD	Oct. 30, 1961
I 11	SALT SW 3	N 12			S 13	
H 11	PU T46	M 12			R 13	
G 11	PU T47	L 12			Q 13	
F 11		K 12			P 13	
E 11		J 12			O 13	
D 11		I 12			N 13	IN 84
C 11		H 12			M 13	DEL OUT PS 1
B 11		G 12			L 13	DEL OUT PS 11
A 11		F 12			K 13	ST 26
		E 12			J 13	
x 12						
w 12		D 12			I 13	
v 12		C 12			H 13	
u 12		B 12			G 13	ST 8
t 12		A 12			F 13	TIME OUT PULSE B
					E 13	IN 94
s 12		W 13				
r 12		v 13			D 13	
q 12		u 13			C 13	
p 12		t 13			B 13	
o 12					A 13	
		s 13				
n 12		r 13			x 16	
m 12		q 13			w 16	
l 12		p 13			v 16	
k 12		o 13			u 16	
j 12					t 16	
		n 13				
i 12		m 13			a 16	
h 12		l 13			r 16	
g 12		k 13			q 16	
f 12		j 13			p 16	
e 12		i 13			o 16	
		h 13				
d 12		g 13			n 16	
c 12		f 13			m 16	
b 12		e 13			l 16	
a 12					k 16	
		d 13			j 16	
X 12	NI	c 13			i 16	
W 12	OUT 82	b 13			h 16	
V 12	BUS r27	a 13			g 16	
U 12					f 16	
T 12		X 13			e 16	
		W 13				
S 12		V 13	DEL OUT PS 12		d 16	
R 12	BUS Q17	U 13	IN 78		c 16	
Q 12	CDR PULSE 8	T 13	OUT STEP CLEAR 2		b 16	
P 12	CDR PULSE 6				a 16	
O 12	BUS O 11					

PROGRAM CONTROL CHART #4

NAFEC Phase I LAB		BUSSES		OPERATIONAL PLUGBOARD Oct. 30, 1961	
L 1		G 5		D 10	
K 1		F 5		C 10	
J 1		E 5		B 10	
I 1				A 10	
		D 5			
H 1		C 5		x 11	
G 1		B 5		w 11	
F 1		A 5		v 11	
E 1				u 11	
		G 6		t 11	
D 1	SALT SW 6	F 6			
C 1	PU T9	E 6		s 11	
B 1	PU T10			r 11	
A 1	BUS A2			q 11	
		D 6		p 11	
L 2		C 6		o 11	
K 2	NI	B 6			
J 2	OUT 81	A 6		n 11	
I 2	OUT 94			m 11	
		G 7		l 11	
H 2		F 7		k 11	
G 2		E 7		j 11	
F 2					
E 2		D 7		i 11	
		C 7		h 11	
D 2		B 7		g 11	
C 2	PU T21	A 7		f 11	
B 2	PU T11			e 11	
A 2	Bus A1	G 8	OE1 OUT 1	d 11	
		F 8	Drop out PS1	c 11	
H 3		E 8	PU PS 10	b 11	
G 3				a 11	
F 3		D 8			
E 3		C 8		X 11	
		B 8		W 11	NI
D 3	SALT SW2	A 8		V 11	OUT 74
C 3	PU T25			U 11	DROP OUT PS15
B 3	PU T26	G 9	CDR PULSE 5	T 11	DROP OUT PS 14
A 3	PU T37	F 9	CDR PUSLE 4		
		E9	PS 14 IN	S 11	CDR PULSE 0
H 4				R 11	CDR PULSE 1
G 4		D 9		Q 11	CDR PULSE 2
F 4		C 9		P 11	
E 4		B 9		O 11	BUS 012
		A 9			
D 4				N 11	CDR PULSE 9
C 4		G 10		M 11	CDR PULSE 3
B 4		F 10		L 11	CDR PULSE 7
A 4		E 10		K 11	IN PS 15
				J 11	

[illegible]

PROGRAM CONTROL CHART #4

NAFEC Phase I LAB			BUSSES OPERATIONAL PLUGBOARD Oct. 30, 1961		
x 27	B+ PS2		I 27	PU T33	N 28
w 27	PU T 13		H 27	PU T34	M 29
v 27	PU T 18		G 27	PU T35	L 28
u 27	PU T 41		F 27	PU T36	K 28
t 27			E 27	B+ PS16	J 28
s 27			D 27		I 28
r 27	Bus V12		C 27		H 28
q 27	Drop out PS 7		B 27		G 28
p 27	Drop Out PS 6		A 27		F 28
o 27	NS T41B				E 28
n 27	B+ PS 6		x 28	C T16 A	
m 27	PU T24		w 28	C T10 B	D 28
l 27	PU T12		v 28	SALT SW5	C 28
k 27			w 28		B 28
j 27			t 28		A 28
i 27			s 28		
h 27	B+ PS7		r 28		e 35 R SH 52
g 27	PU T31		q 28		d 35 NS T 12 A
f 27	PU T30		p 28		c 35 S T35 A
e 27			o 28		b 35 S T48 B
d 27					a 35 CDR OUT 1
c 27			n 28	NS T22B	E 35
b 27			m 28	OE 2 - OUT 2	D 35
a 27			l 28	Drop out PS 10	C 35
X 27	PU T16		k 28	Drop Out PS 11	B 35
W 27	PU T17		j 28	UN17 IN2	A 35
V 27	B+ PS 10		i 28	B+ PS 8	e 36
U 27	PU T22		h 28	PU T42	d 36
T 27			g 28	PU T43	c 36
S 27			f 28		b 36
R 27			e 28		a 36
Q 27			d 28		E 36
P 27			c 28	OUT 70	D 36
O 27			b 28	OUT 72	C 36
N 27			a 28	C T12D	B 36
M 27			X 28		A 36
L 27			W 28		K 51
K 27			V 28		J 51
J 27			U 28		I 51
			T 28		H 51
			S 28		G 51
			R 28		F 51
			Q 28		
			P 28		
			O 28		

APPENDIX XIX

HIGH-SPEED PRINTER PLUGBOARD WIRING CHARTS

PHASE 1 - UFC 1
MASTER OPERATIONAL PROGRAM

HIGH SPEED PRINTER FORMS - FAA MODIFIED

	MULTILINE REPRINT	
1	—	—
2	—	—
3	—	—
4	—	—
5	—	—
6	—	X

	IN	OUT
	—	—

	LINE SPACE
1	LS-S
2	—
3	—
S	LS-1

PRINTER CONTROL	
C	PC
C C	T
T	CC
P C	C

ZERO SUPPRESS	
START	STORAGE ADDRESS
1	109
END	112

	MULTILINE COUNTER	
	L	R
1	—	
2	MCS-C	MCS-NS
3	—	
4	T/S-IPF	T/S-OUT
5	—	
6	—	
H	MCS-IN	MCS-S

TRACK SWITCH		
PF	IPF	OUT
—	MC-4L	MC-4R

	MULTILINE
1	MS-V
2	MS-P
3	MS-M
4	MS-K
5	MS-H
6	MS-E

	A	—	—
M	B	—	—
U	C	(—
L	D	()
T	E	ML-6)
I	F	(—
L	G	()
I	H	ML-5)
N	I	—	—
E	J	—)
S	K	ML-4)
E	L	—)
L	M	ML-3)
E	N	(—
C	O	()
T	P	ML-2)
O	R	—)
R	S	()
S	T	()
	V	ML-1)

MULTILINE COUNTER SELECTOR	
C	MC-2L
NS	MC-2R
S	MC-HR
IN	MC-HL

FIELD	FIELD	FIELD	FIELD	FROM STORAGE	TO MULTILINE	TO MULTILINE	TO MULTILINE	TO MULTILINE
ORIGINAL STRIP		CONTINUATION ROUTE STRIP						
1ST TRACK	2ND TRACK	1ST TRACK	2ND TRACK		1ST BUFFER	2ND BUFFER		
				1				
				2				
				3				
				4				
				5	V5			
				6	V6			
				7	V7			
				8	V8			
				9	V9	C6		
				10	V10	C7		
				11	V11	C8		
				12	V12			
				13	T1			
				14	T2			
				15	T3			
				16	T4			
				17	T5			
				18	T6			
				19	T7			
				20	T8			
				21	T9			
				22	T10			
				23	T11			
				24	T12			

FIELD	FIELD	FIELD	FIELD	FROM STORAGE	TO MULTILINE	TO MULTILINE	TO MULTILINE	TO MULTILINE		
ORIGINAL STRIP		CONTINUATION ROUTE STRIP								
1ST TRACK	2ND TRACK	1ST TRACK	2ND TRACK		1ST BUFFER	2ND BUFFER				
2ND JUNCTION	2ND WORD OF REMARKS	9TH JUNCTION		25	P1	E1				
				26	P2	E2				
				27	P3	E3				
				28	P4	E4				
△				△		29	P5	E5		
3RD AIRWAY		10TH AIRWAY		30	P6	E6				
				31	P7	E7				
				32	P8	E8				
				33	P9	E9				
△				△		34	P10	E10		
3RD JUNCTION		10TH JUNCTION		35	P11	E11				
				36	P12	E12				
				37	01					
				38	02					
△				△		39	03			
4TH AIRWAY		11TH AIRWAY		40	04					
				41	05					
				42	06					
				43	07					
△				△		44	08			
						45	09			
						46	010			
						47	011			
						48	012			

HIGH SPEED PRINTER FORMS - FAA MODIFIED

FIELD	FIELD	FIELD	FIELD	FROM STORAGE	TO MULTILINE	TO MULTILINE	TO MULTILINE	TO MULTILINE
ORIGINAL STRIP		CONTINUATION ROUTE STRIP						
1ST TRACK	2ND TRACK	1ST TRACK	2ND TRACK		1ST BUFFER	2ND BUFFER		
	4TH JUNCTION △	L/C	11TH JUNCTION △	49		K1		
				50		K2		
L/C				51	M5	K3		
				52		K4		
AIRCRAFT IDENTIFICATION	5TH AIRWAY △	AIRCRAFT IDENTIFICATION	12TH AIRWAY △	53		K5		
				54	M6	K6		
				55	M7	K7		
				56	M8	K8		
				57	M9	K9		
				58	M10	K10		
				59	M11	K11		
HOURS OF TIME	5TH JUNCTION △	AIRCRAFT IDENTIFICATION	12TH JUNCTION △	60	M12	K12		
				61		J1		
				62		J2		
				63		J3		
				64		J4		
				65	L5	J5		
				66	L6	J6		
				67		J7		
				68	L7	J8		
				69		J9		
				70	R10	J10		
				71	R11	J11		
MINUTES OF TIME	6TH AIRWAY △	U/C	13TH AIRWAY △	72		J12		

FIELD	FIELD	FIELD	FIELD	FROM STORAGE	TO MULTILINE	TO MULTILINE	TO MULTILINE	TO MULTILINE
ORIGINAL STRIP		CONTINUATION ROUTE STRIP						
1ST TRACK	2ND TRACK	1ST TRACK	2ND TRACK		1ST BUFFER	2ND BUFFER		
PREVIOUS FIX TIME	6TH JUNCTION △	7TH JUNCTION	13TH JUNCTION △	73		H1		
				74	N2	H2		
				75	N3	H3		
				76	N4	H4		
DEPARTURE POINT	7TH AIRWAY △		14TH AIRWAY △	77	N5	H5		
				78		H6		
				79		H7		
				80		H8		
				81	V1	H9		
				82	V2	H10		
				83	V3	H11		
				84	V4	H12		
TYPE OF AIRCRAFT	CTTY	14TH JUNCTION △	85	L1	G1			
			86	L2	G2			
			87	L3	G3			
			88	L4	G4			
Distance and Direction Rho-Theta or Latitude	DESTINATION △	15TH AIRWAY △	89	N6	G5			
			90	N7	G6			
			91	N8	G7			
			92	N9	G8			
THIS FIX OR LONGITUDE	△		93	M1	G9			
			94	M2	G10			
			95	M3	G11			
			96	M4	G12			

HIGH SPEED PRINTER FORMS - FAA MODIFIED

FIELD		FIELD		FIELD		FIELD		FROM STORAGE	TO MULTILINE	TO MULTILINE	TO MULTILINE	TO MULTILINE
ORIGINAL STRIP		CONTINUATION ROUTE STRIP										
1ST TRACK	2ND TRACK	1ST TRACK	2ND TRACK			1ST BUFFER		2ND BUFFER				
<div><div><div>Δ</div><div>Δ</div><div>Δ</div><div>Δ</div></div><div><div>PROP. DEPR. TIME</div><div>PREVIOUS FIX TIME</div><div>PREVIOUS FIX OR LONGITUDE</div></div><div><div>PREVIOUS FIX, DISTANCE & DIRECTION</div><div>RHO-THETA OR LATITUDE</div></div></div>	<div><div>1ST</div><div>WORD</div><div>OF</div><div>REMARKS</div></div>	<div><div>DESTINATION</div><div>•</div><div>•</div><div>•</div><div>•</div><div>Δ</div><div>Δ</div><div>Δ</div><div>Δ</div></div>	97		D1							
			98		D2							
			99		D3							
			100		D4							
			• 101	S5	D5							
			• 102	S6	D6							
			• 103	S7	D7							
			• 104	S8	D8							
			Δ 105	S9	D9							
			Δ 106	S10	D10							
			Δ 107	S11	D11							
			Δ 108	S12	D12							
ALTITUDE	SPEED	<div><div>L/C or 1</div><div>X or A</div></div>	109	S1	C1							
			110	S2	C2							
			111	S3	C3							
			112	S4	C4							
L/C	<div><div>L/C or 1</div><div>X or A</div></div>	L/C	113		F4							
			114									
L/C	<div><div>L/C or 1</div><div>X or A</div></div>	L/C	115		F1							
			116	R2								
COLOR CODE	<div><div>L/C or 1</div><div>L/C</div></div>	COLOR CODE	<div><div>L/C or 1</div><div>L/C</div></div>	117		F2						
			<div><div>L/C or 1</div><div>L/C</div></div>	118	R3	F3						
				119								
				120								

FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	PRINT COLUMN
		M6				1
		M7			C6	2
		M8			COMPUTER FLIGHT NUMBER C7	3
		M9			C8	4
		M10				5
		M11				6
		M12				7
						8
S5		L1			C1	9
S6		L2			C2	10
PREVIOUS FIX TIME S7		L3			SPEED C3	11
S8		L4			C4	12
S9	N2					13
S10	N3					14
PREVIOUS FIX TIME S11	PREVIOUS FIX TIME N4					15
S12	N5					16
		M5				17
		L/C L5				18
		HOURS OF TIME L6				19
MINUTES OF TIME R10		U/C L7				20
R11						21
						22
						23
						24
LINE 1	LINE 2	LINE 3	LINE 4	LINE 5	LINE 6	PRINTER LINE
LINE 1	LINE 2	LINE 3			LINE 5	STRIP LINE

INACTIVE
PRINT
COLUMN

INACTIVE
PRINT
COLUMN

FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	PRINT COLUMN
	N6	M1				25
	N7	M2				26
	N8	M3				27
	N9	M4				28
						29
S1						30
S2						31
S3						32
S4						33
						34
						35
						36
V1	P1		K1	H1	D1	37
V2	P2		K2	H2	D2	38
V3	P3		K3	H3	D3	39
V4	P4		K4	H4	D4	40
△ V5	△ P5		△ K5	△ H5	D5	41
V6	P6		K6	H6	D6	42
V7	P7		K7	H7	D7	43
V8	P8		K8	H8	D8	44
V9	P9		K9	H9	D9	45
V10	P10		K10	H10	D10	46
V11	P11		K11	H11	D11	47
△ V12	△ P12		△ K12	△ H12	D12	48
LINE 1	LINE 2	LINE 3	LINE 4	LINE 5	LINE 6	PRINTER LINE
LINE 1	LINE 2	LINE 3	LINE 3	LINE 4	LINE 5	STRIP LINE

INACTIVE
PRINT
COLUMN

INACTIVE
PRINT
COLUMN

FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	FROM MULTILINE	PRINT COLUMN
T1	01		J1	Y G1	E1	49
JUNCTION T2	JUNCTION 02		JUNCTION J2	T G2	E2	50
T3	03		J3	F G3	E3	51
T4	04		J4	C G4	E4	52
△ T5	△ 05		△ J5	△ G5	E5	53
T6	06		J6	G6	E6	54
T7	07		J7	DESTINATION G7	E7	55
T8	08		J8	G8	E8	56
AIRWAY T9	AIRWAY 09		AIRWAY J9	G9	E9	57
T10	010		J10	G10	E10	58
T11	011		J11	G11	E11	59
△ T12	△ 012		△ J12	△ G12	E12	60
						61
						62
						63
						64
				L/C or F4		65
L/C R2				1 or L/C F2		66
COLOR CODE R3				L/C or 1 F3		67
				X or A F1		68
						69
LINE 1	LINE 2	LINE 3	LINE 4	LINE 5	LINE 6	PRINTER LINE
LINE 1	LINE 2		LINE 3	LINE 4	LINE 5	STRIP LINE

INACTIVE
PRINT
COLUMN

APPENDIX XX

**INPUT TRANSLATE AND FORMAT CONTROL UNIT
PLUGBOARD NARRATIVE AND WIRING CHARTS**

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INPUT MESSAGE NARRATION

General:

The Input Message T & F plugboard, which is explained by the following diagrams and narration, represents the best combination of hardware elements to date. In referencing this material, it is advisable that the reader have access to the particular diagram while he is reading the narration.

I. Teletype Substation Address:

In both the Synchro Tape and the Teletype Input Substation the first character, after the last disconnect, to appear at the T & F plugboard is the substation address. The substation address is a binary number which is determined by the setting of the substation number selector switch on the operators panel of the input substation. The T & F plugboard has been designed to operate in a Synchro Tape mode with receipt of substation addresses ten through fifteen. Receipt of addresses one through nine causes the T & F to operate in a teletype mode. Using a substation address above ten for a Teletype message will result in errors as will utilization of substation address nine or below for a Synchro Tape message. For this reason, the substation selector switch should be marked to indicate the proper range for Teletype and Synchro Tape. At PD 1, i. e., PD 1 following a Master Clear or an Input Disconnect, PD 1 is programmed to step on decode. Receipt of the substation address in the Incoming Buffer Output Register causes a Decoder Group I hub, corresponding to the binary value of the substation address, to emit and PD to be advanced to 2. Upon stepping to PD 2 with the decoding of the substation address, Decoder Input hub 64 is activated causing the substation address to now make an appearance at a corresponding Decoder Group III hub. PD 2 is also programmed to pick up Low Speed Selector 10 (LSS 10) so that PD 1 will be stepped on encode for the duration of the message. During the output cycle of the teletype substation character cycle the substation address from Decoder Group III is transferred to Word 0 Character 11 of the 120 character buffer. During the function cycle of the substation address character cycle Function Detector b (FD b), which has been enabled by PD 2, is activated. FD b1 through Bus W-25 clears the buffer to spaces. This action erases the substation address that was encoded from Decoder Group III. At FD b2 time, Bus X-21 is enabled causing

a space to be encoded (this action causes the Word Address Counter and Character Address Counter - WAK and CAK - to be set to 0-10) and the timer circuitry to be energized. The decoder which was turned off at the beginning of the function cycle is turned on by a signal from FD b3. Since the substation address is still in the Incoming Buffer Output register, the Decoder Group III hub representing this substation address is activated causing the substation address to be stored in Word 0 Character 10 of the buffer. At FD b4 time the end of message selector (HSS 3) is picked up.

If the substation selector switch has been set to any number from one through nine (indicating a Teletype address), that particular substation address will be encoded into Word 0 Character 10. If the substation address happened to be a number from ten to fifteen (indicating a Synchro Tape substation) an alpha character within the range A-F corresponding to a number in the range of 10-15, is encoded in Word 0 Character 10. In the latter instance the correct substation address (A-F) is decoded from a DEC Group IV Hub. Word 0 Character 10 will not normally be used in message processing. It is, however, an additional identification that may be used in event the addressor field of a message is garbled.

II. Synchro Tape Substation Address:

As stated in I above, if the substation address selector switch is set to any number from ten to fifteen the T & F is conditioned for the Synchro Tape operating mode. After the substation address is received in the Incoming Buffer Output register PD is advanced to 2, causing the substation address to make an appearance at a Decoder Group III hub. If the Address falls within the address range of 10-15, bus b-25 is activated causing High Speed Selector 5 (HSS 5), LSS 6 and LSS 7 to be picked up. Picking up these selectors places the T & F in the Synchro Tape operating mode. Picking up HSS 5 activates Decoder Input Hub 32. With Decoder Input Hubs 64 and 32 enabled the substation address appears at a Decoder Group IV Hub. During the Output cycle of the Substation Address Character cycle the appropriate Synchro Tape substation address A-F is encoded in Word 0 Character 11 of the buffer. Function Detector b which is activated by PD 2 performs the functions described in paragraph 2 Section I - Teletype Substation Address. At FD b1 time the buffer is cleared to spaces and at

FD b2 time a space is encoded and the timer is energized. At FD b3 time the Synchro Tape Substation Address is encoded from Decoder Group IV since Decoder Input Hubs 64 and 32 are still enabled. At FD b4 time the end of message selector (HSS 3) is picked up.

In Diagram II of the "Synchro Tape" diagrams, Bus b-25 picks up HSS 5; HSS 5 in the select position connects the sixth incoming buffer output information level (32 and 0-32) to the sixth information level of the Decoder Input (32 and 0-32) through busses a-20 and X-27. Had this been a Teletype message, HSS 5 would not have been picked up and incoming precedence output would be connected to the Decoder Input hubs, C-32 to create a sixth level for Teletype. Incoming precedence, upper or lower case, is set by the appearance of figures or letters in the input message. Precedence, once set to a given state remains in the given state until changed by the detection of opposite precedence. In Teletype, letters and figures are used to create a sixth information level whereas in Synchro Tape it is provided continuously with each character appearing in the Incoming Buffer Output register. At the end of message precedence is set to lower case by a signal from Bus F-30.

III. Synchro Tape - Teletype:

In Diagram III of the "Synchro Tape diagrams, the effects derived from picking up LSS 6 are shown. Element a of LSS 6 controls the toggling of the Channel Parity Register. If LSS 6 is in the dropped out condition (Teletype mode) a Channel Parity Register-In enable through Bus w-20, LSS 4d NS, LSS 6a NS, and HSS 2d NS, will step the Channel Parity Register with the receipt of each character in the Incoming Buffer Output Register. It is necessary to route the CPR-IN enable through HSS 4 (Channel Parity Selector) to inhibit stepping the Channel Parity Register upon receipt of the Channel Parity Character. In the Synchro Tape mode, the stepping of the Channel Parity Register is inhibited by picking up LSS 6. The CPR-IN enable is also routed through HSS 2d (mode selector) to prevent an erroneous toggling of the channel parity register when remarks are present in an input ICM.

Element b of LSS 6 qualifies the Line Feed symbol. In the Teletype mode, it should appear in the beginning of message,

immediately after the channel check character in the channel check group (carriage return, letters, channel check character, Line Feed, letters) and possibly as the channel check character. If this character (Line Feed) occurs at any other place in the inter-center message, it will be detected as an out of place Line Feed. If a character parity error is detected by the Output T & F or Output Reader at the center transmitting a message, Line Feed and E characters are inserted to inform the receiving T & F Unit that an error was transmitted to it. The Line Feed must, therefore, be detected as a flag at the receiving input T & F. As the diagram shows a signal from Unibus 5 Out, through Bus d-23, activates the Channel Parity Register and provides an input to Inverter 5. The inputs to Unibus 5 and Unibus L occur at the times a Line Feed character is expected, i. e., the beginning of the message, the channel check character (which may be a Line Feed) and the character immediately after the channel check character. If there is no signal at any of the Unibus L in Hubs a Line Feed is not expected. A no-signal condition at the Input to the Inverter causes a signal to be emitted from the Out hub. The decoding of a Line Feed character provides a signal at DEC II-2 or DEC I-2 which is routed through Bus v-30 to enable And Gate 6. An Output from And Gate 6 denotes an out of place Line Feed. The enable is routed to the common of LSS 6b to qualify the out of place Line Feed. If the input is from Synchro Tape selector 6 is picked up and the out of place Line Feed signal from the out of And Gate 6 is terminated. In the teletype mode, the out of place Line Feed is routed through LSS 6b NS and Unibus F to Bus d-24 to activate HSCL X and encode a colon. The colon will type out as a northeast wind arrow (↗) on the supervisory typewriter. This arrow, in teletype operation, indicates that a Line Feed has been inserted by the preceding Univac T & F output and as such is a transmitted error.

Element c of LSS 6 qualifies an output from DEC II-5 which is the bell symbol. In the Synchro Tape mode a signal at DEC II-5 is routed through the select side of element c to an invalid character chain. Thus, in the Synchro Tape mode, the bell character is an invalid character. In teletype, the bell symbol is a legitimate character inserted by an output T & F to indicate the presence of a parity incorrect character. In this instance the bell character is routed through the non-select side of LSS 6c to Unibus F to activate HSCL X and encode a colon.

Element d of LSS 6 qualifies PD 3. In the teletype mode the program distributor steps on decode from PD 3 through PD 6 to accommodate the appearance of the beginning of message sequence (letters, letters, line feed, letters). In Synchro Tape, however, the beginning of message sequence has only two characters which are line feed and letters. To adjust the program distributor when operating in the Synchro Tape mode, PD 3 is routed through the select side of Element d to FD e. FD e2, through Bus w-30 and Unibus 2, sets the PD CHAR IN to 5. Enabling PD CHAR and/or WORD IN hubs to given value will cause a signal to be emitted from the PD hub on the plugboard that has a value of one (1) greater than the value of the SET PD IN hubs enabled. In this instance, enabling PD CHAR IN Hubs 4 and 1 sets PD to 6. The net effect is that in Synchro Tape operations the letters character of the beginning of message of group steps PD to 7 with the decode cycle and the first character of the addressor steps PD to 8 with the encode cycle.

IV. Synchro Tape-Teletype:

LSS 7 is shown in Diagram IV of the "Synchro Tape-Teletype" Diagrams. Note that LSS 7 is also picked up by a signal at Bus b-25 which is activated by a Synchro Tape substation address. Element c is used to qualify the message void character. In Synchro Tape mode, a signal from DEC II-12 (message void) goes through the select side of LSS 7c to Bus N-24. This is a valid character and its effects are diagrammed in Diagram X "Message Void". In a teletype mode, the message void is an invalid character and is treated as an invalid character by routing the signal through the non-select side of LSS 7c to Unibus 16, HSS 4b NS, and Unibus 13 to Function Detector d which activates HSCL X and encodes the ϕ character which will be printed as a north wind arrow (\downarrow) on the Supervisory Typewriter. In addition the stepping of PD with the encoding of the ϕ character is inhibited.

Element d of LSS 7 qualifies the presence of the north wind arrow (\downarrow) which appears at DEC II-20. In the Synchro Tape mode, the north wind arrow is an invalid character and is treated as such by routing a signal from DEC II-20 to an invalid character chain through DEC I-0. In the teletype mode, an output from DEC II-20 is deleted by not enabling any of the encoding hubs. In teletype operations the north wind arrow is a valid character because the Output T & F, at the center transmitting the message, will insert a north wind arrow if a synchronization error has been detected.

V. PD Stepping:

The program distributor in most instances is programmed to step on encode. However, there are several instances when it is desirable to step on decode. These PD positions, where stepping is to be performed on decode, are shown as Inputs to Unibus 17, 11 and 12. At these positions Bus e-28 is enabled causing PD to be stepped on decode. At all other PD positions, Bus e-28 is not enabled, hence the Output from Inverter 1 (INV 1) enables PD Step In 3 which steps PD with each character encoded. PD 2, 4, 5, and 6 always step on decode to test for the presence of the beginning of message (letters, letters, Line Feed, letters) in teletype operations. At PD 1, when the substation address appears at a Decoder Group I hub, PD is stepped on Decode. After the substation address is decoded LSS 10 is picked up and PD 1 is programmed to step on encode for the duration of the message. PD 3 in the Synchro Tape mode steps on encode to enable the advancing of PD to 6 to adjust the program distributor for two less beginning of message characters. PD 14, 23 and 29 disable the step on encode mode and enable the step on decode mode to allow the T & F to jump the Word Address Counter (WAK) and Character Address Counter (CAK) with PD Outs. This is necessary because WAK and CAK cannot be jumped with PD outs when PD is stepping on encode. LSS 8, the route selector, qualifies PD 35 and 42 so that when LSS 8 is non-select these PD counts can be used to set WAK and CAK. At PD 48 all of the identification data has been transferred to the buffer in the T & F (see Table I "Input from HSPT") and LSS 8 is picked up. PD is then set to 31 (see Diagram VII "Route") and PD is advanced through the range of 31-130 depending on length of route. When LSS 8 is in the select position, PD 42 and 35 are disconnected from Bus e-28 so that PD is stepped on encode at these positions. At the end of the route portion, if there are remarks, PD is set to 91 (see Diagram IX "Remarks") and PD is stepped on encode for the remainder of the message.

VI. Beginning of Message Check:

The Beginning of Message Check diagram shows the wiring of DEC 1-31 (representing letters) and DEC 1-2 (representing Line Feed) to Unibus 10. The Out of Unibus 10 is wired to INV 2 and the Out of INV 2 is wired to And Gate 1. PD 3, 4, 5 and 6 serve

as inputs to Unibus 20 providing the other input to And Gate 1. If, while in the teletype operating mode; any character other than Line Feed or letters are decoded during PD 3 through 6, an error called "No Beginning of Message" results, i. e. , if any other decoder hub is activated, the absence of a signal at the Input to INV 2 causes a signal to be emitted from the INV 2 Out hub that enables And Gate 1. A no beginning of message error will activate FD d which will encode a ¢ character, inhibit step on encode and activate HSCL X. The ¢ character prints as a north wind arrow on the Supervisory typewriter.

VII. Setting Word Address Counter and Character Address Counter (WAK and CAK):

Table I "Input From HSPT" shows an Input message arranged in the format that is transferred from the buffer to I/O tracks 01 and subsequently to track 97 and 96. To arrange the identification portion of the data in the format shown PD 6, 14, 23, 29, 35 and 42 are stepped on decode so that the Word and Character Address counters can be set to the buffer address that is to receive the first character of the next field to be processed.

PD 6, which marks the decoding of the last beginning of message character (letters) in Synchro Tape operations and the decoding of the first character of the addressor in Teletype operations sets the Buffer Word and Character Address Counters to 0-6 for the receipt of the first character of the addressor field.

In a similar manner, PD 23, which denotes the end of the Aircraft Identification field, sets WAK to 2 and CAK to 3 for the receipt of the first character of the Type of Aircraft field. After the last field of the Identification portion of the message has been transferred to the buffer, PD 48 initiates a track switch function that transfers the buffer content to the I/O track and track switches. The buffer is then cleared to spaces. Clearing the buffer to spaces sets WAK and CAK to 0-11 in preparation for the receipt of the Route portion of the Input Message. If the Input Message contains Remarks, WAK and CAK are set to 7-11.

VIII. Route:

At PD 47 a check is made for the space at the end of the Altitude field. A space at this position will indicate that the Identification

portion of the message is now contained in the T & F Buffer. (see Table I). PD 48 is routed through the non-select side of LSS 8c to the In of FD a. FD a1 enables input stop and track switch through Bus h-20. The track switch function transfers the Identification portion of the message from the buffer to I/O track left and track switches. A signal from FD a2 sets PD to 31. (PD Character 6 and PD Word 2 is the indication for PD 31) by enabling Bus h-13, h-14 and Unibus 2. The signal from FD a3 is routed through the non-select side of HSS 1a to enable Bus W-25 which clears the buffer to spaces in preparation for the Route portion of the message. FD a4 picks up low speed selectors 8 and 9, the route selectors. In summary, PD is set to 31 and after the track switch WAK and CAK are set to Word 0 Character 11. As the route is read, a check for the proper field length is made by checking for spaces at the PD counts shown in Table I, "Input From HSPT", and Diagram XIV, "Field Length Check".

IX. Remarks:

At the end of any of the route fields a remarks code (upper case C) may be detected. The upper case C emits from DEC II-14. A signal from DEC II-14 is routed through Unibus 6 to activate FD a. The remarks signal also enables Bus b-19 which activates HSCL Y. Bus b-19 also picks up HSS 1, the Remarks selector.

A signal from FD a1 enables Bus h-20 which subsequently enables the track switch function causing the Route information to be put on track right. The signal from FD a2 enables Bus h-13 and 14 which sets PD to 31 and through the select side of HSS 1b enables Bus u-2 which sets PD Word 1 and 4, making a total PD count of 91. The signal from PD 91 is routed through the select side of HSS 1 to FD h IN.

The enable at Bus u-2 from FD a2 also goes to Unibus 14 enabling Output 1. Enabling this hub transfers the identification data from track left to the buffer. In addition, Bus u-2 picks up HSS 2. Output 1 is enabled before HSS 2 is picked up because HSS 2 requires another clock pulse before it is picked up. When HSS 2 is picked up Output 1, 2, and 3 are connected to power putting the T & F in the Output mode. Thus, when HSS 2 is picked up, the T & F is switched from an Input mode to an Output mode of operation. A signal from FD a3

is routed through the select side of HSS 1 and Unibus G to Function Input 1, to perform an Output stop function. This will inhibit the transfer of characters to the Input Scanner while in the Output mode. After the Buffer is filled with the Identification portion of the message, a space code occupying buffer position 120 (Word 0 Character 11) is transferred to the Incoming Buffer Output register. Since the Output stop has been enabled, the character is not encoded, i. e., transferred to Output. Receipt of this character at the Incoming Buffer Output Register causes a signal to be emitted from the CPR-IN hub. This signal is routed to Bus w-20 causing HSS 2 to be dropped out, and the T & F to be placed in the Input mode of operation again. During the function sequence portion of the space character cycle, FD h Out hubs will emit signals since FD h is enabled during this character cycle by PD 91. The signal from FD h2 is routed to Bus F-6 which sets the buffer address to Word 7 Character 11 and the signal from FD h3 is routed through Unibus H to Output start so that the next character to be received from an Input Substation (the first character of Remarks) can be transferred to Word 7 Character 11 in the buffer.

As a result of the above action, the route information is transferred to track right and the T & F is switched to the Output mode, enabling the information from track left (which contains the Identification) to be re-loaded into the buffer. T & F operation is then switched to the Input mode, enabling the remainder of the buffer (starting at Word 7 Character 11) to be filled with the Remarks. Switching from the Input to the Output mode is necessary to return the information on track left to the buffer because buffer transfers to and from the I/O track are 120 characters at a time.

X. Message Void:

There are presently two methods of nullifying an Input error made by a Synchro Tape operator. The first method, which may be utilized when an error is discovered immediately after it is made, requires the depression of the back-space key followed by a depression of the delete key. This action will cause a delete character to be overpunched in the frame where the error occurred. The operator may then re-enter the correct data. The delete character is ignored by the T & F. If the Synchro TApe operator wishes to nullify an entire message, the message void key is depressed followed by a depression of the tape skip key. Depressing the tape skip key causes the program tape to be advanced so that

the end of message codes are punched in the message tape. The Synchro Tape operator may then re-enter the complete message.

The message void character when received by the T & F causes a signal to be emitted from Decoder II hub 12. This signal when received at the common of LSS 7c is routed to bus N-24 and N-25 when the Input is from Synchro Tape. In Teletype operations a signal at DEC II-12 is routed to Unibus 16 for treatment as an Invalid Character. Bus N-24 and N-25 activates HSCL X, encodes a prime (prime is printed as a northwest wind arrow on the T/W), and sets Two Character Function Detector b (2FD b) by routing a signal to 2FD b hub 1. If the next character to be received is the figures character, HSCL W is activated. Note that if the tape skip key was depressed immediately after the depression of the message void key the next character received must be figures. The computer program must be programmed so that the voided message will be ignored upon detecting the presence of HSCL W and X. In the event that a message void character is not immediately followed by figures, the computer program, upon detecting the presence of HSCL X, will cause the message to be typed out on the supervisory typewriter. The presence of the northwest wind arrow on the typeout will indicate the detection of a non-legitimate message void, i. e. , a message void not followed by figures.

XI. Out of Place Line Feed and Bell Symbol Errors (Transmitted Errors):

Output T & F systems are presently programmed to send three characters, Line Feed, E, and bell characters, to indicate the presence of an error in a message transmitted from the originating station. The Line Feed character is a valid character in a teletype message when it appears in the Beginning of a Message group of characters, immediately after a channel check character, and as the channel parity character. If the Line Feed character is detected, a signal is emitted from the DEC II-2, which enables one In of And Gate 6. The out of Unibus L, which is wired to the In of Inverter 5, represents those positions in the message where a Line Feed is expected. The power input to Bus b-14 through HSS 3b Non-Select and HSS 4a Select represents the channel check character position and the power input to Unibus 5 through HSS 3b Select and HSS 4a Select represents the position following the Channel Check Character. If an out of place Line Feed is received the Out of INV 5 will

enable And Gate 6. This out of place Line Feed signal is routed to the common of LSS 6b. In teletype this selector would not be picked up so the signal would be routed through Bus d-24 to encode a colon and activates HSCL X.

The bell character signal, which appears at DEC II-5, is routed directly to LSS 6c where it is qualified by the Synchro Tape/Teletype Mode selector. In Synchro Tape Operations the Bell Character is invalid while in teletype operations receipt of the bell character denotes the presence of a parity incorrect character.

XII. Character Parity Check:

If the contents of the Incoming Buffer Output Register has an even parity count, a signal is emitted from the "Character Parity Odd" hub. This signal, through Unibus 13, is routed to the In of FD d which encodes a ¢ character, inhibits the stepping of PD with the encoding of the ¢ character, and activates HSCL X.

XIII. Invalid Characters Check:

Invalid characters which are wired together to form chains are shown as inputs to Unibus 16 by the wiring from DEC II-18, 25, 15, etc. Unibus 16 Out through Unibus 13 Out enables Function Detector d to activate HSCL X and encode a ¢ character.

XIV. Field Length Check:

The T & F checks for proper field length by checking for spaces at certain PD counts as shown in Table I, "Input From HSPT". The small numbers above the spaces in each field indicate the PD counts where the test for space is made. The test for space is made at the end of each field in the identification portions of the message. At PD 48 the space at the end of the Altitude field marks the end of the Flight Plan Identification. This PD is used to track switch, set PD to 31, clear the buffer to spaces, and set WAK and CAK to 0-11. PD is then stepped through a maximum of nine fields to PD 130. At the end of the Route, if there are any Remarks, PD is then reset to 91. The check for space of field length is not performed for the remainder of the message.

PD Inputs for the field length check are routed into Unibus 9 and Unibus 8 to enable one of the In hubs of And Gate 3. Because of the resetting of PD in the case of route, LSS 8 (the route selector) is used to qualify PD 35 and PD 48. Note that a space occurs at PD 42 in both the identification portion and the route portion of the message. PD 97, 108, 119, 130, 53, 86, 75 and 64 are qualified by HSS 1c of the Remarks selector. This qualification is necessary because the check for space is not made in the remarks portion of the message. The other input to And Gate 3 contains a number of decoder outputs through Unibus D and Unibus 3. Normally the space decoder hub (DEC 1-4) will be emitting to provide the check for space at the end of field length. However, certain other characters such as Line Feed, letters, figures, carriage return, the channel parity character, and message void may be in this position during the route portion of the message. This condition may occur because PD is stepped on encode during the entire route portion of the message and since a channel check group or message void (all of which are not encoded) may appear at the end of any field in the route portion, PD will not be stepped upon the detection of these characters. Hence, PD will still be at the PD count where the space appeared. Thus, the absence of any of the characters shown as inputs to Unibus 3 will cause a signal to be present at hub 2 of And Gate 3. The presence of carriage return, figures, etc., does not affect the validity of the field length test. An output from And Gate 3 will mark the presence of an improper field length by encoding a ¢ character and activating HSCL X.

XV. Time Out Error:

The timer installed in the FAA High Speed Paper Tape System is used to permit continued operation in the event one communication line fails. Normally about five seconds will be allowed for each message to perform its cycle. If the operation cycle of a message exceeds the limitation placed upon it by the timer setting, the T & F Unit will stop processing this message, lock out the substation involved, and continue the processing of messages from other substations.

Hub 2 of FD b is wired to the Timer Start hub. This means that during every message the timer, which has a retriggerable delay, is restarted. If the timer is set for five seconds, approximately 300 character times can elapse before the "1" hub of the timer will emit. If five seconds elapse before the end of

message has been detected, a signal from the "1" hub of the timer through the non-select side of Alternate Switch F will enable FD f In. The signal from FD f1 enables the Lock Out hub to lock out this substation. Before another bid can be sent from substation, manual intervention to locate the cause of this timeout will have to be performed. The signal from FD f2 will encode a comma by enabling Encoder I Hub 17. The signal from FD f3 enables HSCL Y and FD f4 enables the end of message busses which ends the timer cycle and clears the timeout flip-flop to the zero state. The end of message busses drop out all active selectors and initializes the T & F for the next message.

XVI. Channel Parity Check:

As each character arrives at the Input Punch in the input substation, character parity is generated for inclusion as part of each character. Throughout the input units, the computer, and the output units, each character is checked for proper parity. As all five levels are necessary for information in teletype communications, the parity check used incorporates a longitudinal or channel parity check. A channel parity character used in this check is a character or frame that is derived from the number of bits transmitted in each of the five levels. If the total number of bits in a particular level is even, and if odd channel parity is desired, a bit is inserted in that level of the channel check character to make the total an odd number.

A channel parity count in the receiving T & F starts with the first character of the addressor which immediately follows the synchronization codes (letters, letters, Line Feed, letters). A binary count of the bits in each channel is made up to the channel check character of the channel check group (carriage return, letters, channel check character, Line Feed, letters). At the beginning of the message, the channel parity register (the register that is used to half-add a binary count) is cleared by enabling the CPR-ACT hub with PD 3, 4, 5 and 6. Thereafter, the channel parity count is accumulated by taking the CPR-IN enable through the non-select hub of HSS 4d, the non-select side of LSS 6a, and the non-select side of HSS 2 d to the CRP-STEP hub. This results in the half adding of each incoming bit to the content of the Channel Parity Register. Each frame received over Teletype facilities is half-added in this manner until the carriage return, letters combination is detected. Note that the

channel parity register has eight levels. As Teletype facilities utilize only five levels, the upper three levels of the CPR-INPUT are wired directly to the three upper levels of the CPR-OUTPUT. This wiring inhibits comparison on these three levels.

Receipt of a carriage return through DEC 1-8 enables hub 1 of 2FD a and receipt of the letters code through bus 1-1 enables hub 2 of 2FD a. The signal out of 2FD a is routed to the common of HSS 3a. If Figures did not precede the carriage return and letters characters, the signal is routed through the non-select hub of HSS 3a, the non-select hub of LSS 7a, to Unibus 18 which enables FD c during the letters character cycle. When FD c2 emits, the signal is routed to HSS 4c which is in the dropped-out condition at this time. FD c2 at this time does nothing. The signal from FD c4 picks up HSS 4. Picking up HSS 4 at this time puts power through the select hub of HSS 4a and the non-select hub of HSS 3b which activates Unibus 18 through bus b-14. During the next character cycle when the channel check character is brought in, power still remains applied to FD c IN and at function time of this character cycle FD c is enabled, FD c2 emits and through HSS 4c S picks up HSS 3 through Bus F7 and Bus a25. Once HSS 3 has been picked up the channel parity register will be activated (cleared) until HSS 4 dropped out. Note that power is now supplied through the select hub of HSS 4a and HSS 3b. HSS 4 is not dropped until the letters character is detected at the end of the channel check group. When the letters hub (DEC 1-31) emits, Bus 1-34 and one side of And Gate 5 are activated. The CPR-IN signal from the letters character cycle is routed through the select hub of HSS 3d to drop out HSS 4. During the first character cycle following the letters character cycle of the channel check group, the CPR-IN signal through HSS 4d non-select, LSS 6c non-select, and HSS 2d non-select, steps the channel parity register by half-adding the binary counts of the bits in each of the five channels of the teletype character to the cleared (all ones) content of the register.

The Channel Parity Register non-compare hub (CPR-NC) emits during part of every Input cycle in which the channel parity register does not agree with the incoming character. This enable which occurs with almost every character cycle is qualified by And Gate 4. The other Input to And Gate 4 comes when HSS 4 is in the select position and HSS 3 is in the non-select position. This occurs only when the channel check character is in the Incoming

Buffer Output register. Therefore, if a channel parity register non-compare is present at this time, there will be an output from And Gate 4 and during the channel check character function cycle FD g will be activated. FD g encodes a plus sign (+) and activates HSCL X.

The channel check character can be any code combination of the five teletype levels. If this code combination happens to be the same as some encodable character, this combination is deleted by applying power through HSS 4a select, 3b non-select and Bus b-14 to the Unconditional Delete hub. If the channel check character code represents an invalid character, Unibus 16 Out will emit a signal to HSS 4b and as the select side if unwired this enable will be terminated and no false error will occur. During any other portion of the message an Output from Unibus 16 representing an invalid character is routed through the non-select side of HSS 4b to Unibus 13 which activates FD d.

XVII. Overflow Check:

Normally the Program Distributor will never advance to PD 132. PD is reset to 31 at the beginning of route and should perform the last test for space at PD 130. In Remarks, PD is reset to 91 allowing a maximum of forty-one characters for remarks. If more than thirty-six characters of remarks are encountered, the excess (up to five more) will be stored in the beginning of the track 97. Should PD ever advance to more than 131, function input #1 (Output stop) is enabled to prevent subsequent characters from being read into the buffer and HSCL X is activated to indicate an overflow error. PD is stopped at 132 and any subsequent incoming characters are not encoded.

XVIII. End of Message:

The sequence, figures, carriage return, and letters, signal the presence of the end of message. Figures, through Bus F-7 and a-25 picks up HSS 3. HSS 3 is dropped out with a CPR-OUT signal through And Gate 2. If an error is detected after HSS 3 has been picked up, the CPR-OUT signal (resulting from encoding the ¢ symbol) to HSS 3 DO is terminated at And Gate 2 so that HSS 3 remains picked up. If the Figures code is a precedence code which is followed by another character that is to be encoded, HSS 3 will be dropped with the encoding of the character. However, as carriage

return and letters are not encoded, HSS 3 remains picked up. A carriage return signal from DEC 1-8 enables hub 1 of 2FD a and letters enables hub 2 of 2FD a. The output from 2FD a through HSS 3a S enables the end of message group of busses, F-25 through F-30. These busses drop out the selectors indicated, end time out, set precedence to lower case, enable output start, enable the disconnect hub (this allows the scanner to proceed to the next bid) and enable Function Input 6. Note that HSS 3 is also picked up during the substation address character cycle by a signal from FD b4. This wiring will enable the T & F to go Ready and the scanner to advance to the next station requesting service if a message consisting of only figures, carriage return, letters is received.

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TABLE II
ERROR INDICATIONS AND DIAGNOSIS

28 Set Indication	HSCL Indication	Character Encoded	T/W Indication	Position In Error Type Out	Diagnosis
None	X	(.)	→	Flag (Word 5 Character 6) and another	Shift-Buffer-to-UFC (Detection of a local Parity Error)
None	Y	(.)	→	Last Character of a Message	Time Out Error
None	X	(')	↗	Anywhere in a Message	Detection of an illegitimate Message Void
None	X	(f)	↓	Before Addressor Field	Beginning of Message Error
None	X	(f)	→	After a Field	Out of Format
None	X	(f)	→	Anywhere in a Message	Detection of a a) Parity error detected at incoming buffer output. b) Detecting an Invalid Char- acter.
None	X	(+)	+	After the following in a Tele- type message: a) Altitude b) Route Segment c) Remarks	Detection of incorrect channel parity.
Line Feed, E, Bell	X	(:)	/E	Anywhere in a Teletype message	a) Detection of a character parity error transmitted from another center that was initially detected at the Incoming Buffer-Output register of the sending T & F at sending center.

TABLE II (Continued)
ERROR INDICATIONS AND DIAGNOSIS

28 Set Indication	HSCL Indication	Character Encoded	T/W Indication	Position In Error Type Out	Diagnosis
Line Feed, E or Line Feed, 3	X	(:)	/E or /3	Anywhere in a Teletype message	b) Detection of a character parity error transmitted from another center that was initially detected by the Output Reader at the sending center.
None	X	None	See posi- tion in error type out at right	If the route exceeds eight segments up to five of the excess characters will appear after the eighth route segment.	Message too long, i. e., PD = 132
				If remarks exceeds thirty-six characters the excess characters (up to five) will appear before the addressor field. Note that the substation address will be erased if the excess characters is two or greater.	

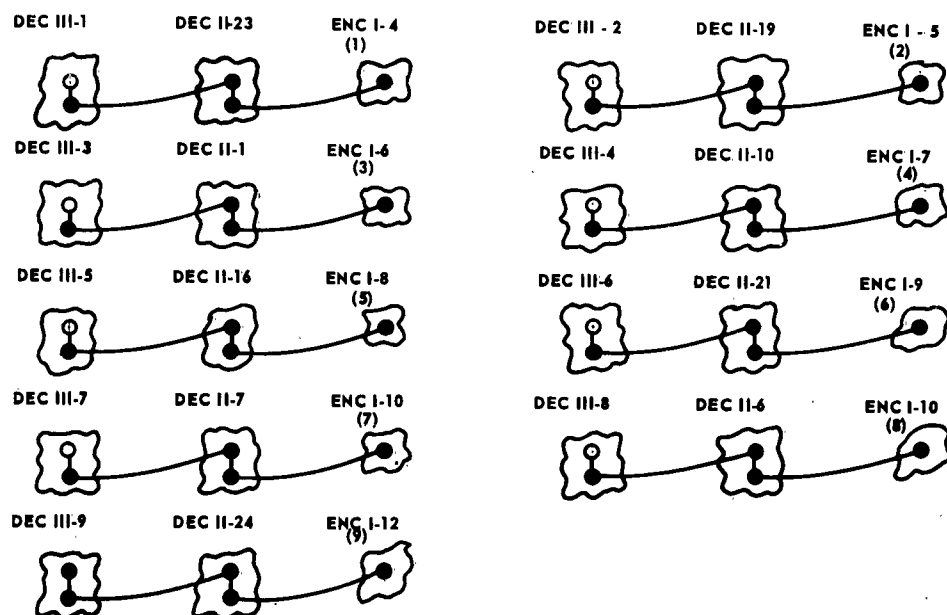
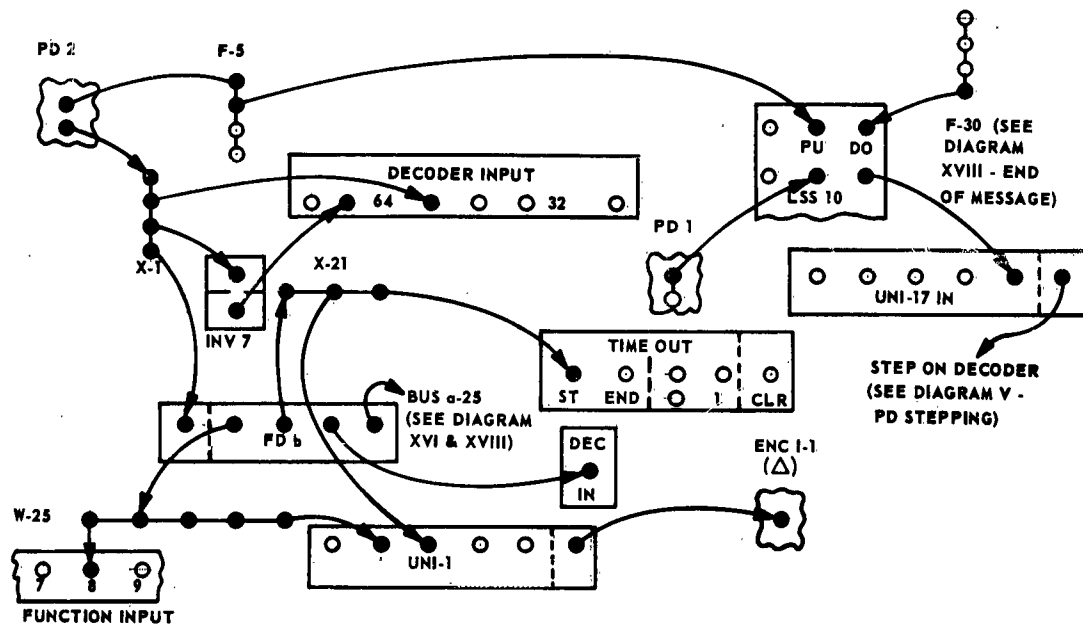


DIAGRAM I TELETYPE SUBSTATION ADDRESSES

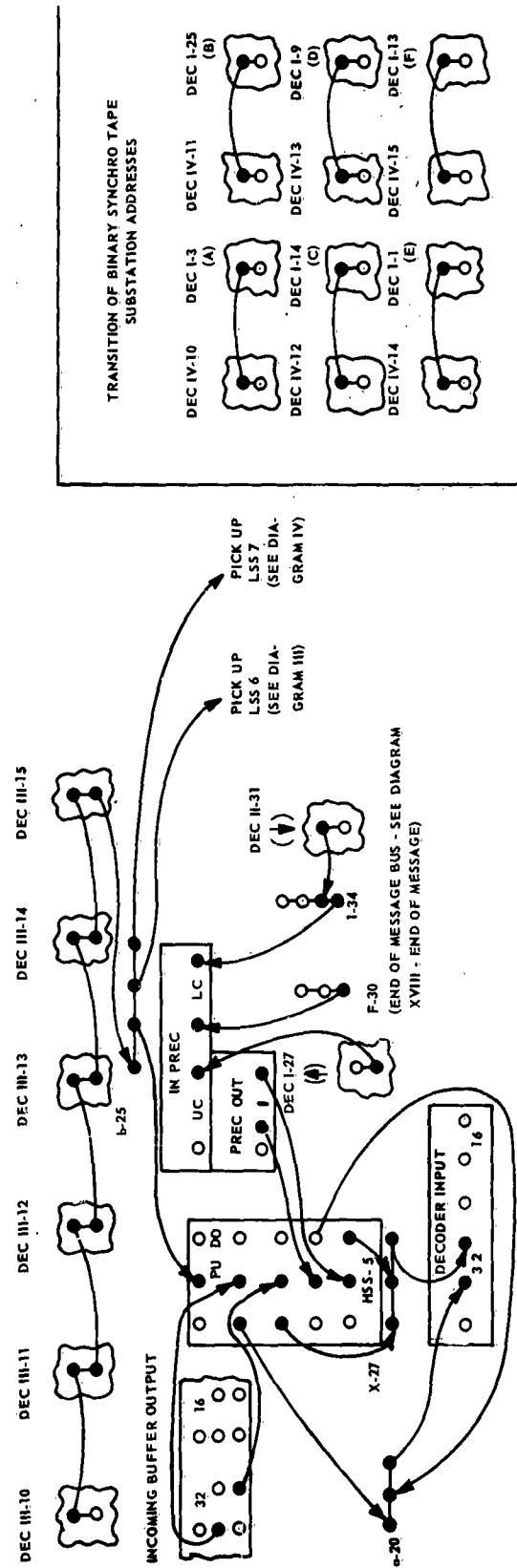
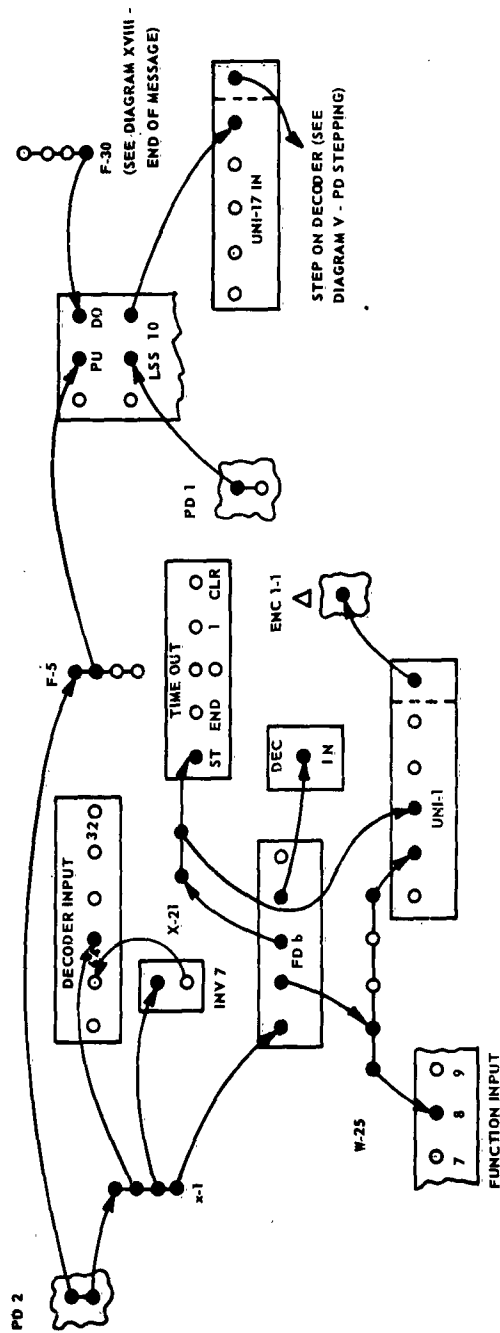
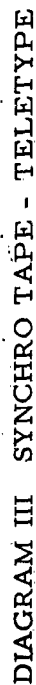


DIAGRAM II SYNCHRO TAPE SUBSTATION ADDRESS



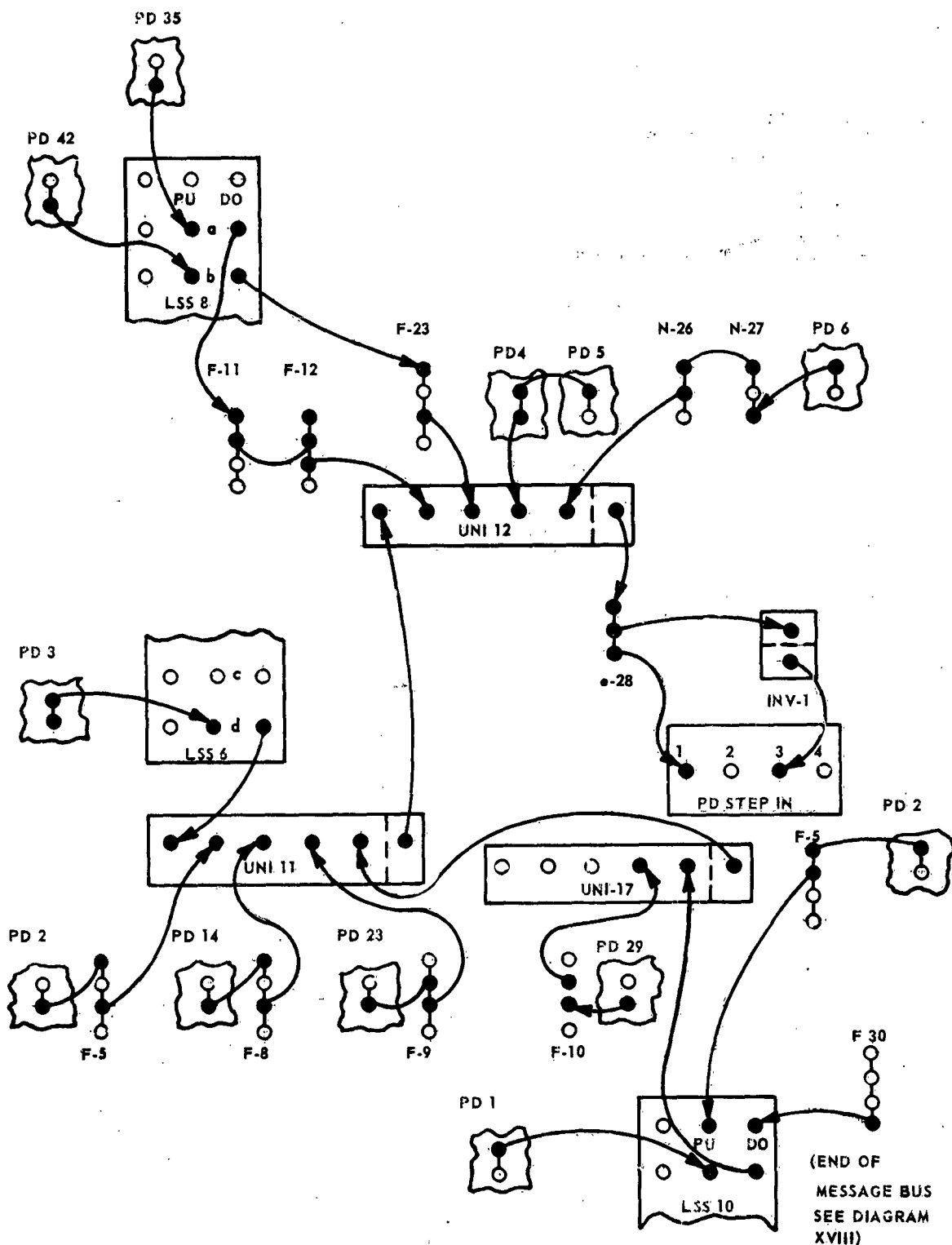


DIAGRAM V PD STEPPING

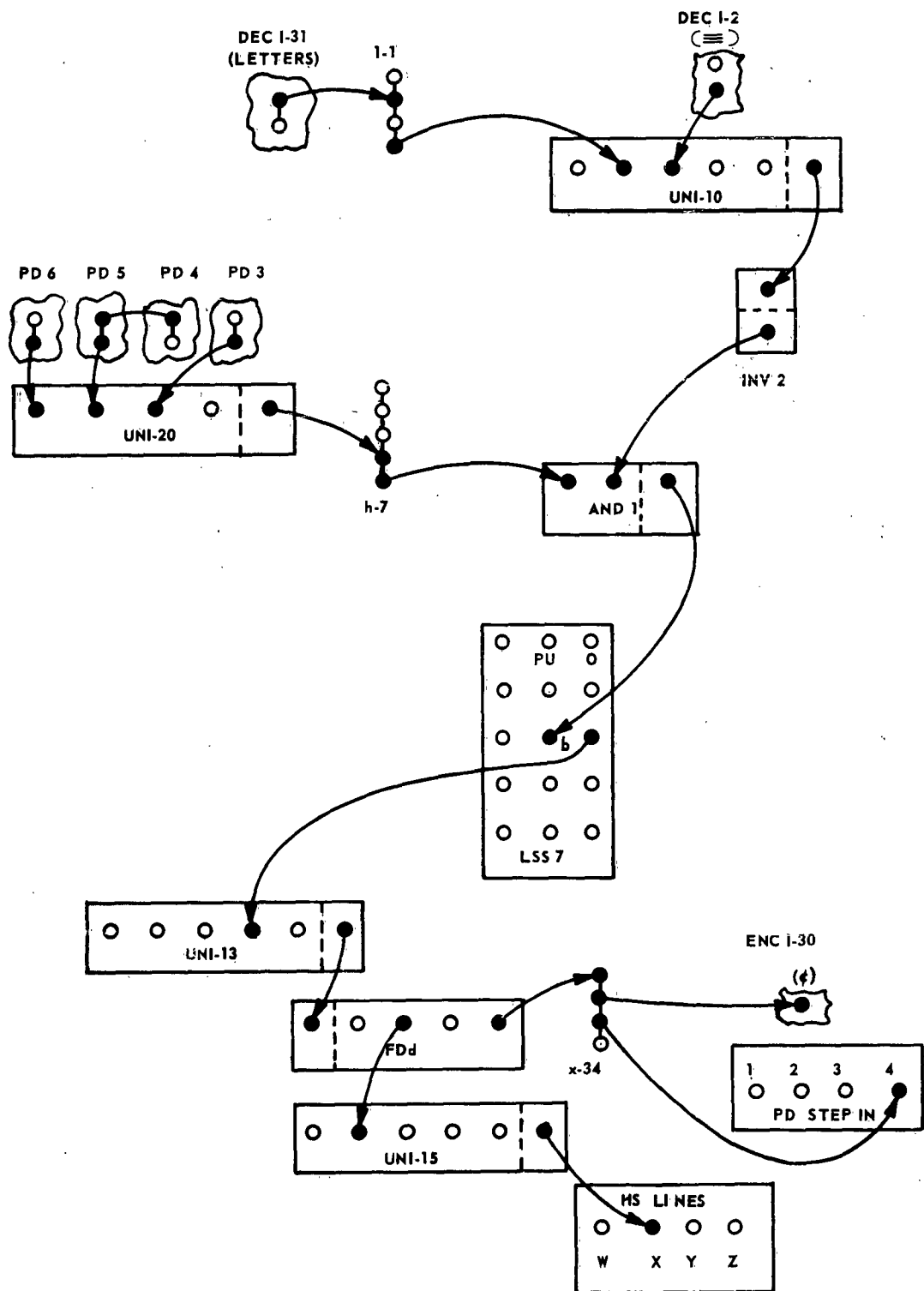


DIAGRAM VI BEGINNING OF MESSAGE CHECK

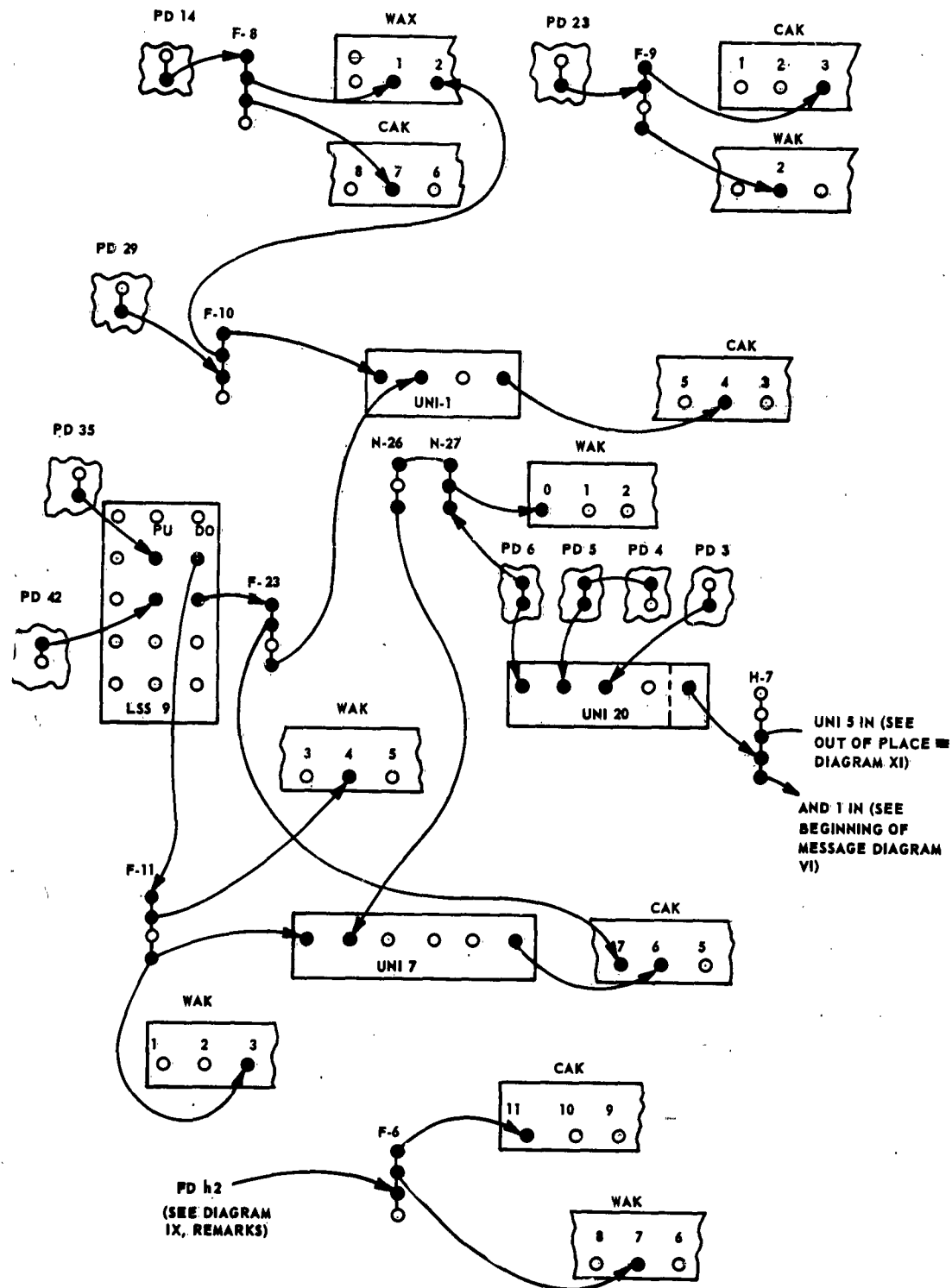


DIAGRAM VII SETTING WORD ON CHARACTER ADDRESS COUNTERS

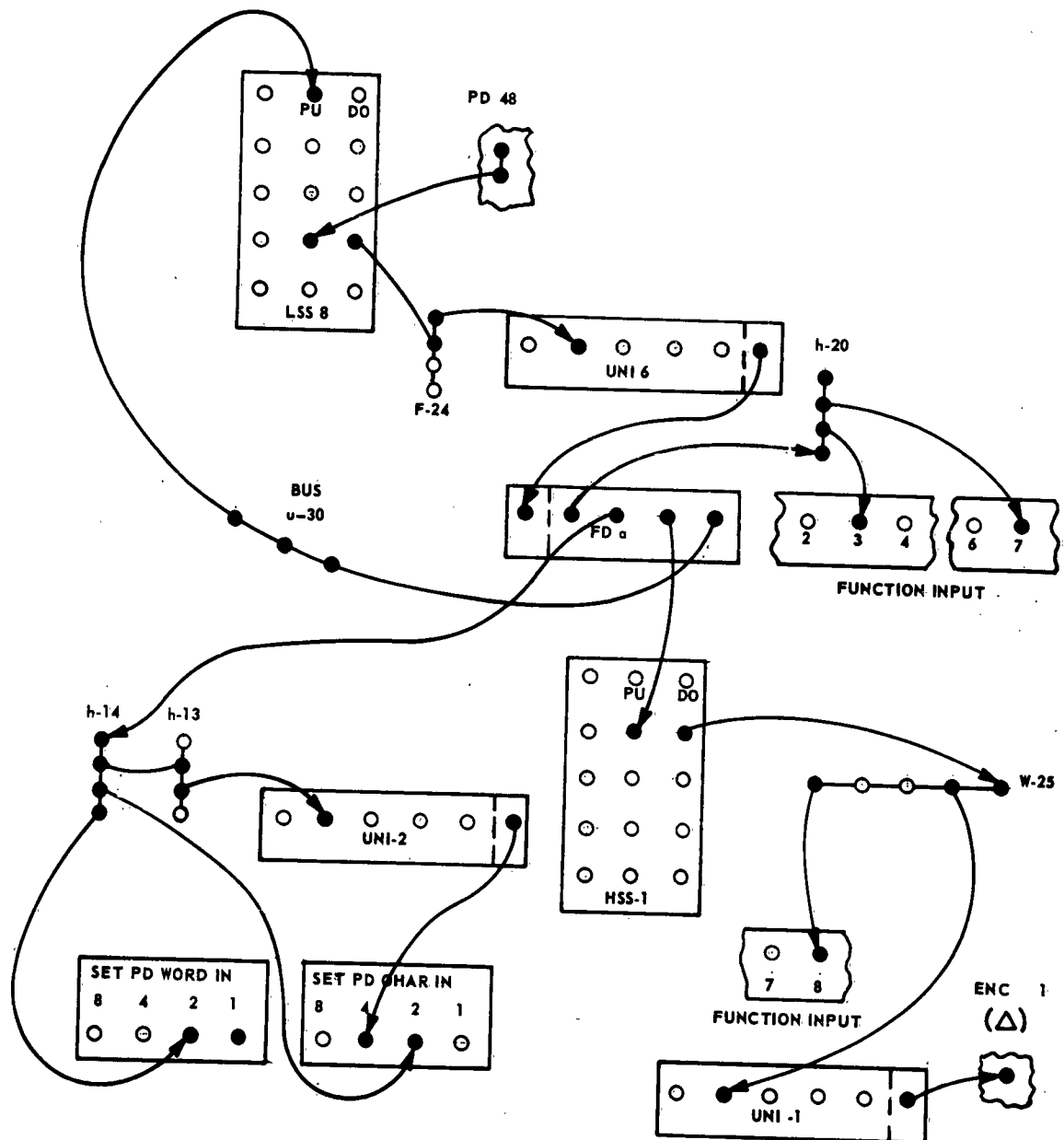


DIAGRAM VIII ROUTE

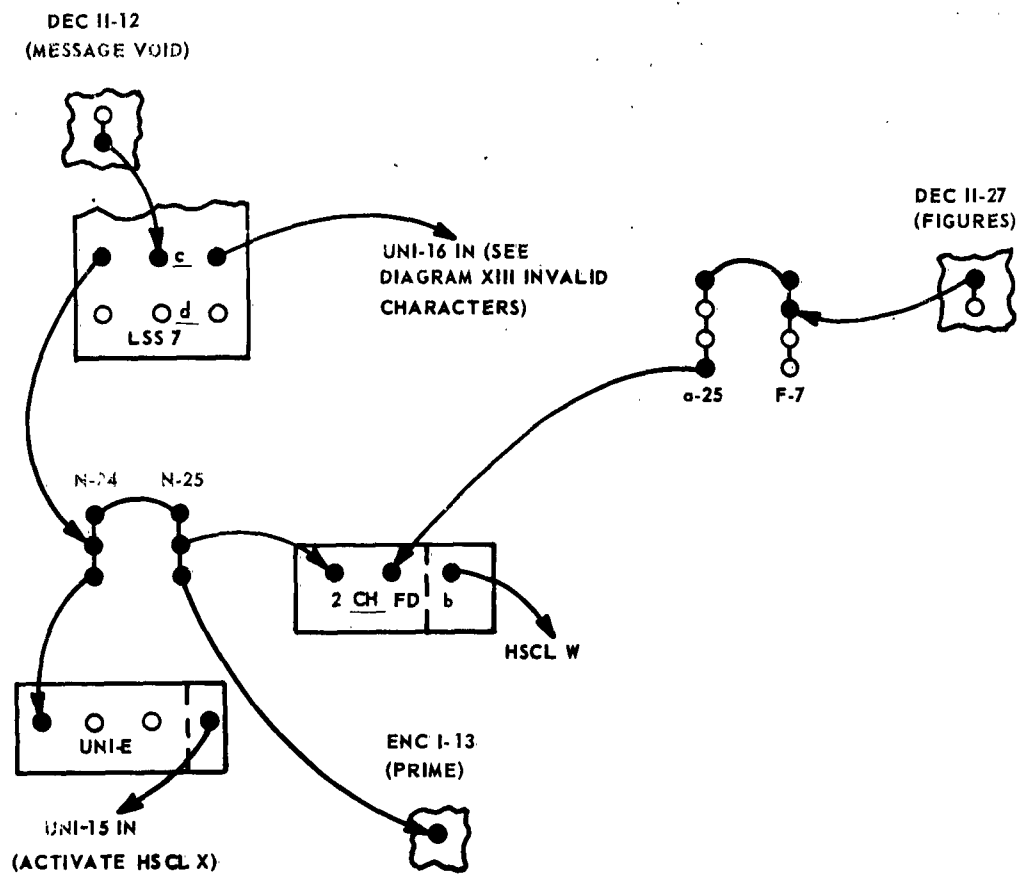


DIAGRAM X MESSAGE VOID

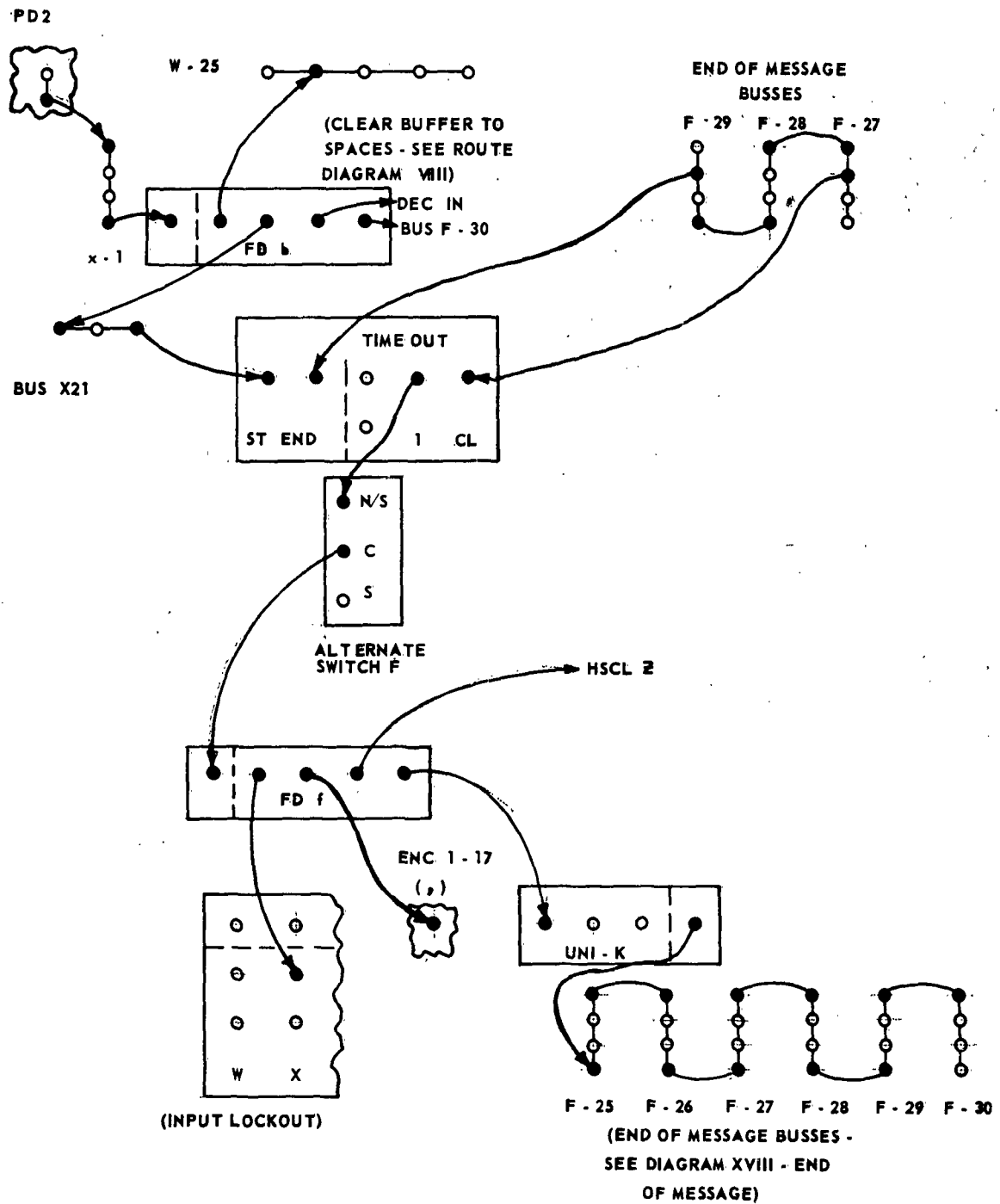


DIAGRAM XV TIME OUT.



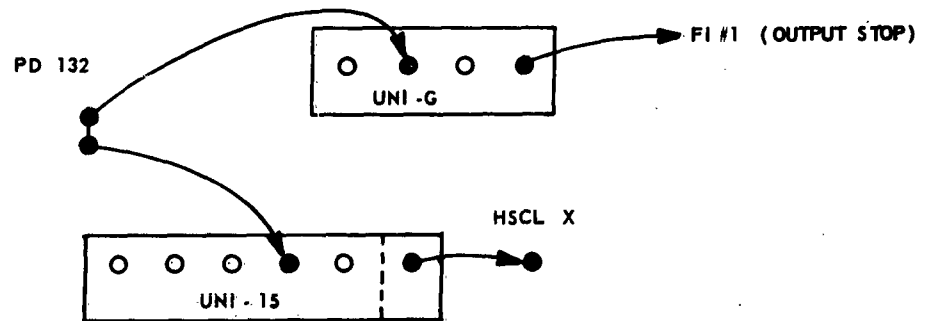


DIAGRAM XVII OVERFLOW CHECK

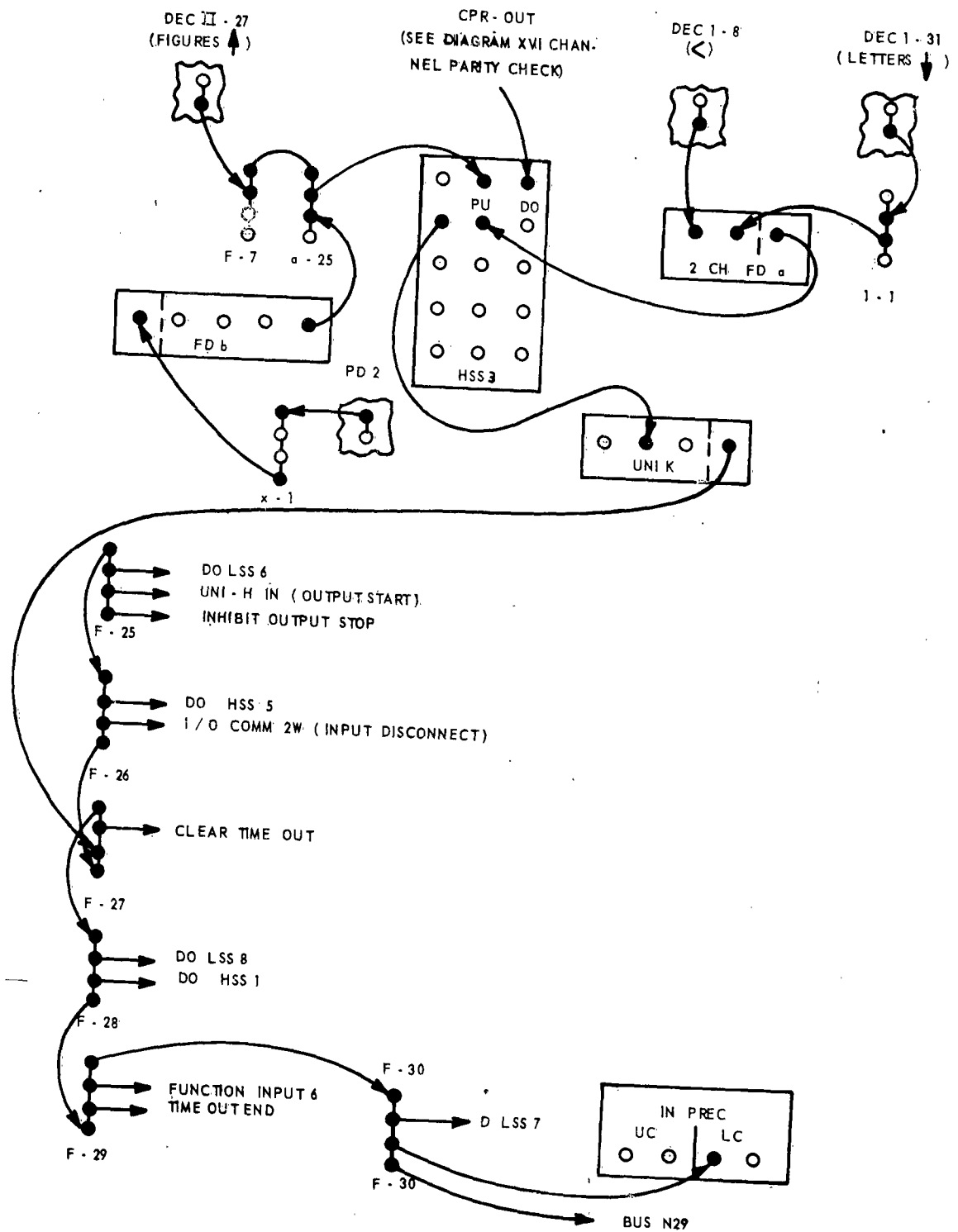


DIAGRAM XVIII END OF MESSAGE

T & F UNIT - DECODER WIRING

PROGRAM: FAA INPUT MESSAGE

DECODER OUTPUT, GP. I			CODE
HUB	WIRED TO	CHAR.	PURPOSE
0	LSS 7d S DEC II-0		INVALID
1	DEC IV-14 ENC I-24	E	
2	DEC II-2 UNI 10 IN	≡	
3	DEC IV-10 ENC I-20	A	
4	DEC II-4 UNI 3 IN	△	
5	ENC II-21	S	
6	ENC I-28	I	
7	ENC II-23	U	
8	2FD a1 DEC II-8	<	
9	DEC IV-13 ENC I-23	D	
10	ENC II-12	R	
11	ENC II-4	J	
12	ENC II-8	N	
13	DEC IV-15 ENC I-25	F	
14	DEC IV-12 ENC I-22	C	
15	ENC II-5	K	
16	ENC II-22	T	
17	ENC II-28	Z	
18	ENC II-6	L	
19	ENC II-25	W	
20	ENC I-27	H	
21	ENC II-27	Y	
22	ENC II-10	P	
23	ENC II-11	Q	
24	ENC II-9 DEC IV-11	O	
25	ENC I-21	B	
26	ENC I-26	G	

DECODER OUTPUT, GP. II			CODE
HUB	WIRED TO	CHAR.	PURPOSE
0	DEC II-3 DEC I-0		INVALID
1	ENC I-6 DEC III-3	3	SS #3
2	DEC I-2 BUS v-30	≡	OUT OF PLACE
3	DEC II-0 DEC II-17	↑	INVALID
4	DEC I-4 UNI 1 IN	△	ENCODE △
5	LSS 6c C	BELL	TTY BELL
6	DEC III-S ENC I-11	8	SS #8
7	DEC III-7 ENC I-10	7	SS #7
8	DEC I-8 UNI 3-IN	<	
9	DEC II-11 LSS 6c S	/	INVALID
10	DEC III-4 ENC I-7	4	SS #4
11	DEC II-9 DEC II-13	/	INVALID
12	UNI D IN LSS 7c G	MSG. VOID	TTY INVALID
13	DEC II-11 DEC II-15	→	INVALID
14	UNI 6 IN BUS b-19	↑C	REMARKS
15	DEC II-13 UNI 16 IN	←	INVALID
16	ENC I-8 DEC III-5	5	SS #5
17	DEC II-18 DEC II-3	+	INVALID
18	DEC II-17 UNI 16 IN	↖	INVALID
19	DEC III-2 ENC I-5	2	SS #2
20		↓	DELETE
21	DEC III-6 ENC I-9	6	SS #6
22	ENC I-3 DEC III-1	ø	
23	ENC I-4 ENC I-12	1	SS #1
24	DEC III-9 DEC II-26	9	
25	UNI 16 IN	⊙	INVALID
26	DEC II-30 DEC II-25	↘	INVALID

T & F UNIT - DECODER WIRING (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

DECODER OUTPUT, GP. I			CODE
HUB	WIRED TO	CHAR	PURPOSE
27	IN PREC UC DEC II-27	FIG	
28	ENC- II-7	M	
29	ENC II-26	X	
30	ENC II-24	V	
31	BUS I-1 DEC II-31	LTRS	

DECODER OUTPUT, GP. II			CODE
HUB	WIRED TO	CHAR	PURPOSE
27	DEC I-27 BUS F-7	FIG	
28	ENC I-18	•	DOT PAD
29	ENC I-2	/	ENCODE -
30	DEC II-26 DEC I-31	⊕	INVALID
31	BUS I-34	LTRS	

T & F UNIT - DECODER WIRING

PROGRAM: FAA INPUT MESSAGE

DECODER OUTPUT, GP. III			CODE
HUB	WIRED TO	CHAR.	PURPOSE
0			
1	DEC II-23	1	SS #1
2	DEC II-19	2	SS #2
3	DEC II-1	3	SS #3
4	DEC II-10	4	SS #4
5	DEC II-16	5	SS-#5
6	DEC II-21	6	SS #6
7	DEC II-7	7	SS #7
8	DEC II-6	8	SS #8
9	DEC II-24	9	SS #9
10	DEC III-11		PICK UP :
	DEC III-10		
11	DEC III-12		HSS 5
	DEC III-11		
12	DEC III-13		LSS 6
	DEC III-12		
13	DEC III-14		LSS 7
	DEC III-13		
14	DEC III-15		
	DEC III-14		
15	BUS b-25		
16			
17			
18			
19			
20			
21			
22			
23			
24			
25			
26			

DECODER OUTPUT, GP. IV			CODE
HUB	WIRED TO	CHAR.	PURPOSE
0			
1			
2			
3			
4			
5			
6			
7			
8			
9			
10	DEC I-3	A	SS #10
11	DEC I-25	B	SS #11
12	DEC I-14	C	SS #12
13	DEC I-9	D	SS #13
14	DEC I-1	E	SS #14
15	DEC I-13	F	SS #15
16			
17			
18			
19			
20			
21			
22			
23			
24			
25			
26			

I
II
III
IV
V
VI
VII
VIII
IX
X
XI
XII

PROGRAM: FAA INPUT MESSAGE

DECODER OUTPUT, GP. III			CODE
HUB	WIRED TO	CHAR	PURPOSE
27			
28			
29			
30			
31			

DECODER OUTPUT, GP. IV			CODE
HUB	WIRED TO	CHAR	PURPOSE
27			
28			
29			
30			
31			

ENC. INPUT, GP. I		
HUB	WIRED FROM	CHAR
0		1
1	UNI 1 OUT	Δ
2	DEC II-29	-
3	DEC II-22	0
4	DEC II-23	1
5	DEC II-19	2
6	DEC II-1	3
7	DEC II-10	4
8	DEC II-16	5
9	DEC II-21	6
10	DEC II-7	7
11	DEC II-6	8
12	DEC II-24	9
13	BUS N-25	'
14		&
15		(
16		r
17	FD f2	,
18	DEC II-28	°
19		;
20	DEC I-3	A
21	DEC I-25	B
22	DEC I-14	C
23	DEC I-9	D
24	DEC I-1	E

ENC. INPUT, GP. II		
HUB	WIRED FROM	CHAR
0		t
1		"
2		
3)
4	DEC I-11	J
5	DEC I-15	K
6	DEC I-18	L
7	DEC I-28	M
8	DEC I-12	N
9	DEC I-24	O
10	DEC I-22	P
11	DEC I-23	Q
12	DEC I-10	R
13		\$
14		*
15		?
16		Σ
17		β
18	BUS d-24	:
19	BUS h-28	+
20		/
21	DEC I-5	S
22	DEC I-16	T
23	DEC I-7	U
24	DEC I-30	V

ENC. INPUT, GP. III		
HUB	WIRED FROM	CHAR
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
20		
21		
22		
23		
24		

ENC. INPUT, GP. IV		
HUB	WIRED FROM	CHAR
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
20		
21		
22		
23		
24		

T & F UNIT - ENCODER WIRING (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

ENC. INPUT, GP. I			ENC. INPUT, GP. II			ENC. INPUT, GP. III			ENC. INPUT, GP. IV		
HUB	WIRED FROM	CHAR	HUB	WIRED FROM	CHAR	HUB	WIRED FROM	CHAR	HUB	WIRED FROM	CHAR
25	DEC I-13	F	25	DEC I-19	W	25			25		
26	DEC I-26	G	26	DEC I-29	X	26			26		
27	DEC I-20	H	27	DEC I-21	Y	27			27		
28	DEC I-6	I	28	DEC I-17	Z	28			28		
29		#	29		%	29			29		
30	BUS x-34	c	30		=	30			30		
31		@	31		DEL	31			31		

T & F UNIT - DECODER/ENCODER WIRING

PROGRAM: FAA INPUT MESSAGE

INCOMING BUFFER OUTPUT		
HUB	WIRED TO	PURPOSE
1	CPR IN 1 DEC IN 1	
0	DEC IN 0-1	
2	CPR IN 2 DEC IN 2	
0	DEC IN 0-2	
4	CPR IN 4 DEC IN 4	
0	DEC IN 0-4	
8	CPR IN 8 DEC IN 8	
0	DEC IN 0-8	
16	CPR IN 16 DEC IN 16	
0	DEC IN 0-16	
32	HSS 5b IN	
0	HSS 5a IN	
64	UNWIRED	
0	UNWIRED	
PAR	DEC IN PAR	
0	DEC IN 0-PAR	

INC. PREC. IN	
HUB	WIRED FROM
UC	DEC I-27
UC	
LC	BUS 1-34
LC	BUS F-30

ENCODER OUTPUT		
HUB	WIRED TO	PURPOSE
1	P/C 1	
2	P/C 2	
4	P/C 4	
8	P/C 8	
16	P/C 16	
32	P/C 32	
64	UNWIRED	
PAR	P/C PAR	

INCOMING PREC. OUTPUT		
HUB	WIRED TO	PURPOSE
1	HSS 5d IN	
0	HSS 5c IN	

OUTGOING PREC. OUTPUT		
HUB	WIRED TO	PURPOSE
IC		
UC		

OUT. PREC. IN		
HUB	WIRED	FROM
IN		
INH		

T & F UNIT - DECODER/ENCODER WIRING (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

DECODER INPUT	
HUB	WIRED FROM
1	IN BUFF 1
0	IN BUFF 0-1
2	IN BUFF 2
0	IN BUFF 0-2
4	IN BUFF 4
0	IN BUFF 0-4
8	IN BUFF 8
0	IN BUFF 0-8
16	IN BUFF 16
0	IN BUFF 0-16
32	BUS X-27
0	BUS a-20
64	BUS x-1
0	INV 7 OUT
PAR	IN BUFF PAR
0	IN BUFF 0-PAR

PUNCH/COMP. INPUT	
	WIRED FROM
1a	ENC OUT 1
1b	
2a	ENC OUT 2
2b	
4a	ENC OUT 4
4b	
8a	ENC OUT 8
8b	
16a	ENC OUT 16
16b	
32a	ENC OUT 32
32b	
64a	UNWIRED
64b	UNWIRED
PARa	ENC OUT PAR
PARb	

DELETE
WIRED FROM
POWER

DELETE
WIRED FROM

T & F UNIT - SELECTOR WIRING

PROGRAM: FAA INPUT MESSAGE

HIGH SPEED SEL. 1			
PURPOSE: REMARKS			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS b-19		BUS F-28	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	UNI G IN	FD a3	BUS W-25
b	BUS u-2	BUS b-14	
c		UNI 4 OUT	UNI 9 IN
d	FD h IN	PD 91	

HIGH SPEED SEL. 2			
PURPOSE: I/O MODE			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS u-2		BUS w-20	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	UNI 14 IN	PWR	INPUT 1
b	OUTPUT 2	PWR	INPUT 2
c	OUTPUT 3	PWR	INPUT 3
d		LSS 6a NS	CPR STEP

HIGH SPEED SEL. 3			
PURPOSE: CHANNEL PARITY			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS a-25		AND 2-OUT	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	UNI K IN	2FD a OUT	LSS 7a C
b	UNI 5 IN	HSS 4a S	BUS b-14
c			
d	HSS 4 DO	AND 5 OUT	

LOW SPEED SEL. 6			
PURPOSE: SYNCHRO TAPE			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS b-25		BUS F-25	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a		HSS 4d NS	HSS 2d IN
b		AND 6 OUT	UNI-F IN
c	DEC II-9	DEC II-5	UNI-F IN
d	FD e IN	PD 3	UNI 11 IN

LOW SPEED SEL. 7			
PURPOSE: SYNCHRO TAPE			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS b-25		BUS F-30	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a		HSS 3a N/S	UNI-18 IN
b		AND 1 OUT	UNI-13 IN
c	BUS N-24	DEC II-12	UNI 16 IN
d	DEC I-0	DEC II-20	

LOW SPEED SEL. 8			
PURPOSE: ROUTE			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS u 30		BUS E 28	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a		PD 35	BUS F-11
b		PD 42	BUS F-23
c		PD 48	BUS F-24
d			

T & F UNIT - SELECTOR WIRING (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

HIGH SPEED SEL. 4			
PURPOSE: CHANNEL PARITY			
PU HUB WIRED FROM		DO HUB WIRED FROM	
FD c4		HSS 3d S	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	HSS 3b IN	PWR	
b		UNI 16 OUT	UNI 13 IN
c	BUS F-7	FD c2	
d		BUS w-20	LSS 6a C

HIGH SPEED SEL. 5			
PURPOSE: SYNCHRO TAPE			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS b-25		BUS F-26	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	BUS a-20	IN BUFF 0-32	
b	BUS X-27	IN BUFF 32	
c		PREC OUT 0	BUS a-20
d		PREC OUT 1	BUS X-27

LOW SPEED SEL. 9			
PURPOSE: CHECK FOR SPACE FOLLOWING ALT.			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS u 30		BUS N29	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a			
b			
c			
d		PD 47	BUS N28

LOW SPEED SEL. 10			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS F-5		BUS N 29	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a		PD 1	UNI 17 IN
b			
c			
d			

T & F UNIT - FUNCTION DETECTOR WIRING

PROGRAM: FAA INPUT MESSAGE

FUNCTION DETECTOR a		
PURPOSE: REMARKS AND ROUTE		
INPUT HUB WIRED FROM: UNI 6 OUT		
OUT HUB	WIRED TO	PURPOSE
1	BUS h-20	INPUT STOP TRACK SWITCH
2	BUS h-13	SET PD TO 31
3	HSS 1a IN	OUTPUT STOP CLEAR BUFF. TO 's
4	BUS u 30	PICK UP ROUTE SELECTORS

FUNCTION DETECTOR b		
PURPOSE: BEGINNING OF MESSAGE		
INPUT HUB WIRED FROM: BUS x-1		
OUT HUB	WIRED TO	PURPOSE
1	BUS W-25	CLEAR BUFF TO 's
2	BUS X-21	ENCODE A Δ AND START TIMER
3	DEC IN	TURN DECODER ON
4	BUS a-25	PICK UP HSS 3

FUNCTION DETECTOR c		
PURPOSE: CHANNEL PARITY		
INPUT HUB WIRED FROM: UNI 18 OUT		
OUT HUB	WIRED TO	PURPOSE
1		
2	HSS 4c IN	
3		
4	HSS 4 PU	

FUNCTION DETECTOR d		
PURPOSE: ERROR		
INPUT HUB WIRED FROM: UNI 13 OUT		
OUT HUB	WIRED TO	PURPOSE
1		
2	UNI 15 IN	ACTIVATE HSCL X
3		
4	x-34	INHIBIT STEP ON EN-CODE-ENCODE Δ SYMBOL

FUNCTION DETECTOR e		
PURPOSE: SYNCHRO TAPE		
INPUT HUB WIRED FROM: LSS 6d S		
OUT HUB	WIRED TO	PURPOSE
1		
2	BUS w-30	SET PD TO 5
3		
4		

FUNCTION DETECTOR f		
PURPOSE: TIME OUT ERROR		
INPUT HUB WIRED FROM: ALT SW F C		
OUT HUB	WIRED TO	PURPOSE
1	LOCK OUT I/O 2X	COORDINATE B 32
2	ENC 1-17	ENCODE (.)
3	HSCL Z	ACTIVATE HSCL Z
4	UNI K IN	ACTIVATE END OF MESSAGE BUS

FUNCTION DETECTOR g		
PURPOSE: CHANNEL PARITY ERROR		
INPUT HUB WIRED FROM: AND 4 OUT		
OUT HUB	WIRED TO	PURPOSE
1	BUS h-28	ENCODE A + AND ACTIVATE HSCL X
2		
3		
4		

FUNCTION DETECTOR h		
PURPOSE: REMARKS		
INPUT HUB WIRED FROM: HSS 1d S		
OUT HUB	WIRED TO	PURPOSE
1		
2	BUS F-6	SET WAK TO 7 SET CAK TO 11
3	UNI H IN	OUTPUT START
4		

T & F UNIT - UNIBUS, "AND", WIRING

PROGRAM: FAA INPUT MESSAGE

UNIBUSES							
UNIBUS	INPUT HUBS WIRED FROM					OUT PUT HUB WIRED TO	PURPOSE
1	DEC II-4	BUS W-25	BUS X-21			ENC I-1	ENCODE Δ
2	BUS w-30	BUS h-14				SET PDC 4	SET PDC 4
3	DEC I-4	DEC II-8	BUS 1-34	BUS X-24	UNI D OUT	INV 3 IN	CHECK FOR Δ
4	PD 108	PD 119				HSS 1c IN	CHECK FOR Δ
5	BUS w-20	BUS h-7	HSS 3b S	BUS F-5		BUS d-23	OUT OF PLACE \equiv
6	DEC II-14	BUS F-24				FD a IN	
7	BUS F-8	BUS N-26				CAK 6	SET CAK 6
8	PD 12	PD 14	PD 23	PD 29	UNI 9 OUT	AND 3 IN	CHECK FOR Δ
9	BUS F-12	BUS F-24	PD 42	HSS 1c NS		UNI 8 IN	CHECK FOR Δ
10		BUS 1-1	DEC I-2			INV 2 IN	BEGINNING OF MESSAGE
11	LSS 6d NS	BUS F-5	BUS F-8	BUS F-9	UNI-17 OUT	UNI 12 IN	STEP ON DECODE
12	UNI 11 OUT	BUS F-12	BUS F-23	PD 4	BUS N-26	BUS e-28	STEP ON DECODE
13	HSS 4b NS	ODD CHARAC STER PARITY		LSS 7b NS	AND 3 OUT	FD d IN	ERROR FD
14	BUS u-2	HSS 2a S				OUTPUT 1	REMARKS
15	UNI E OUT	FD d2	BUS h-28	PD 132		HSCL X	HSCL X INVALID CHAR.
16	LSS 7c NS	DEC II-18	DEC II-25	DEC II-15		HSS 4b IN	STEP ON DECODE
17				BUS F-10	LSS 10a NS	UNI-11 IN	CHAN. PAR. CHECK
18	BUS b-14	LSS 7a NS				FD c IN	
19							
20	PD 6	PD 5	PD 3			BUS h-7	BEGINNING OF MESSAGE

T & F UNIT - UNIBUS, "AND", WIRING (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

"AND"				
"AND"	INPUT HUBS WIRED FROM		OUTPUT HUB WIRED TO	PURPOSE
1	BUS h-7	INV 2 OUT	LSS 7b C	BEGINNING OF MESSAGE
2	INV 4 OUT	CPR-OUT	HSS 3 D.O.	INHIBIT D.O. OF HSS 3 WITH ENCODING OF ϕ CHARACTER
3	UNI 8 OUT	INV 3 OUT	UNI 13 IN	FIELD LENGTH
4	BUS b-14	CPR-NC	FD G IN	CHANNEL PARITY
5	BUS w-20	BUS 1-34	HSS 3d IN	D.O. HSS 4 WITH LETTERS CHARACTER FOLLOWING CH. CHECK CHAR.
6	BUS v-30	INV 5 OUT	LSS 6b C	OUT OF PLACE \equiv
7				
8				

UNIBUS					
	IN	IN	IN	OUT	PURPOSE
L	BUS d-23	BUS X-24		INV 5-IN	INHIBIT OUT OF PLACE \equiv CHECK
K	FD f4	HSS 3a S		BUS F-27	END OF MESSAGE
J	BUS F-10	BUS F-23		WAK 3	SET WAK TO 3
I	BUS F-10	BUS F-9		CAK 4	SET CAK 4
H	FD h3	BUS F-25		FI-2	OUTPUT START
G	HSS 1a S	PD 132		FI-1	OUTPUT STOP
F	LSS 6b NS	LSS 6c NS		BUS d-24	
E	BUS N-24	BUS d-24		UNI-15 IN	MESSAGE VOID
D	BUS v-30	BUS F-7	DECII-12	UNI 3 IN	CHECK FOR SPACE

T & F BUSSES

PROGRAM: FAA INPUT MESSAGE

BUS	HUB	WIRED TO	PURPOSE
x-1	x-1	PD 2	PD 2
	w-1	DEC IN 64	
	v-1	INV 7 IN	
	u-1	FD b IN	
u-2	u-2	HSS 1b S	REMARKS
	u-3	UNI 14 IN	
	u-4	HSS 2 PU	
	u-5	SET PD W4	
	u-6	SET PD W1	
w-20	w-20	AND 5 IN	
	w-21	UNI 5 IN	
	v-18	GPR IN	
	v-19		
	v-20	HSS 2 DO	
	v-21	HSS 4d IN	
v-30	v-30	UNI D IN	
	v-31	DEC II-2	
	v-32	AND 6 IN	
	v-33		
u-30	u-30		
	u-31		
	u-32		
	u-33		
l-1	l-1		LETTERS
	k-1	DEC I-31	
	j-1	2FD a2	

BUS	HUB	WIRED TO	PURPOSE
i-1	i-1	UNI 10 IN	
l-34	l-34	AND 5 IN	LETTERS
	k-34	UNI 3 IN	
	j-34	DEC II-31	
	i-34	IN PREC LC	
h-7	h-7		B.O.M.
	g-7		
	f-7	UNI 5 IN	
	e-7	UNI 20 OUT	
	d-7	AND 1 IN	
h-13	h-13	FD a2	SET PD
	g-13	BUS h-14	WITH TS
	f-13	SET PD C2	
	e-13	SET PD W2	
h-14	h-14	HSS 1b IN	SET PD
	g-14	BUS h-13	WITH TS
	f-14	UNI 2 IN	
	e-14		
h-20	h-20		TS
	g-20	FI #7	
	f-20	FI #3	
	e-20	FD a1	
h-21	j-21		
	g-21		
	f-21		
	e-21		

T & F BUSSES (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

BUS	HUB	WIRED TO	PURPOSE
h-28	h-28	ENC II-19	ENCODE
	g-28	UNI 15 IN	HSCL X
	f-28	FD g1	
e-28	e-28	UNI 12 OUT	STEP ON DECODE
	d-28	INV 1 IN	
	c-28	PD STEP IN 1	
b-14	b-14	BUS X-24	CHAN. PAR.
	b-15	HSS 3b NS	
	b-16	UNCONDITI- ONAL DELETE	
	b-17	AND 4 IN	
	b-18	UNI 18 IN	
b-19	b-19	HSCL Y	REMARKS
	b-20	HSS 1 PU	
	b-21	DEC II-14	
	b-23		
a-20	a-20	HSS 5a S	
	a-21	HSS 5c NS	
	a-22	DEC IN 0-32	
d-23	d-23	UNI-5 OUT	CPR ACT
	c-23	CPR-ACT	
	b-23	UNI-L IN	
	a-23		
d-24	d-24	UNI-F OUT	TRANS- MITTED
	e-24	UNI-E IN	ERRORS
	b-24	ENC II-18	
	a-24		

BUS	HUB	WIRED TO	PURPOSE
b-25	b-25	DEC III-15	SYNCHRO
	b-26	HSS 5 PU	
	b-27	LSS 6 PU	
	b-28	LSS 7 PU	
a-25	a-25	BUS F-7	
	a-26	HSS 3 PU	
	a-27	FD b4	
	a-28	2FD h2	
x-34	x-34	FD d4	
	w-34	ENC I-30	ENCODE SYMBOL
	v-34	PD STEP IN 4	INHIBIT STEP ON ENCODE
	u-34	INV 4 IN	
w-30	w-30	FD e2	SET PD TO 6
	w-31	UNI 2 IN	
	w-32	SET PD C 1	
	w-33		
u-30	u-30	FD a4	
	u-31	PULSS 9	
	u-32	PULSS 8	

T & F BUSES

PROGRAM: FAA INPUT MESSAGE

BUS	HUB	WIRED TO	PURPOSE
X-21	X-21	FD b2	
	X-22	UNI-1 IN	
	X-23	START TIMER	
X-24	X-24	BUS b-14	CHAN. PAR.
	X-25	UNI-L IN	
	X-26	UNI-3 IN	
X-27	X-27	HSS 5b S	SYN VS TTY
	X-28	HSS 5d NS	
	X-29	DEC IN 32	
W-25	W-25	FI #8	BUFFER CLEAR
	W-26	FD b1	
	W-27		
	W-28	UNI 1 IN	
	W-29	HSS 1a NS	
F-5	F-5	PD 2	PD 2
	E-5	PN LSS 10	
	D-5	UNI 11 IN	
	C-5	UNI 5 IN	
F-6	F-6	CAK 11	REMARKS
	E-6	WAK 7	
	D-6	FD h2	
	C-6		
F-7	F-7	BUS a-25	
	E-7	DEC II-27	
	D-7	HSS 4c S	
	C-7	UNI D IN	

BUS	HUB	WIRED TO	PURPOSE
F-8	F-8	PD 14	PD 14
	E-8	WAK 1	
	D-8	UNI 11 IN	
	C-8	UNI 7 IN	
F-9	F-9	UNI I IN	PD 23
	E-9	PD 23	
	D-9	UNI 11 IN	
	C-9	WAK 5	
F-10	F-10	UNI I IN	PD 29
	E-10	UNI 17 IN	
	D-10	PD 29	
	C-10	UNI J IN	
F-11	F-11	LSS 8a NS	PD 35
	E-11	BUS F-12	
	D-11	WAK 4	
	C-11	CAK 5	
F-12	F-12		
	E-12	BUS F-11	
	D-12	UNI 12 IN	
	C-12	UNI 9 IN	
F-23	F-23	LSS 8b NS	PD 42
	E-23	UNI J IN	
	D-23	UNI 12 IN	
	C-23	CAK 7	

T & F BUSSES (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

BUS	HUB	WIRED TO	PURPOSE
F-24	F-24	UNI 6 IN	PD 48
	E-24	LSS 8c NS	
	D-24	UNI 9 IN	
	C-24		
F-25	F-25	BUS F-26	E.O.M.
	E-25	LSS 6 DO	
	D-25	UNI H IN	
	C-25	INHIB OUT- PUT STOP	
F-26	F-26	BUS F-25	E.O.M.
	E-26	HSS 5 DO	
	D-26	I/O COMM 2W	
	C-26	BUS F-27	
F-27	F-27	BUS F-28	E.O.M.
	E-27	T.O. CLR	
	D-27	UNI-K OUT	
	C-27	BUS F-26	
F-28	F-28	BUS F-27	E.O.M.
	E-28	LSS 8 DO	
	D-28	HSS 1 DO	
	C-28	BUS F-29	
F-29	F-29	BUS F-30	E.O.M.
	E-29	FI 6	
	D-29	T.O. END	
	C-29	BUS F-28	

BUS	HUB	WIRED TO	PURPOSE
F-30	F-30	BUS F-29	E.O.M.
	E-30	LSS 7 D.O.	
	D-30	IN PREC. IN LC	
	C-30	BUS N-29	
N-24	N-24	BUS N-25	
	M-24	LSS 7c S	
	L-24	UNI E IN	
N-25	N-25	BUS N-24	
	M-25	2FD b1	
	L-25	ENC I-13	
N-26	N-26	BUS N-27	SET WAK
	M-26	UNI-12 IN	AND CAK
	L-26	UNI 7 IN	TO 0-6
N-27	N-27	BUS N-26	AT PD 6
	M-27	WAK "O"	AND STEP
	L-27	PD 6	ON DEC- ODE
N-28	N-28	LSS 9c NS	
	M-28		
	L-28	WAK 6	
N-29	N-29	DO LSS 10	
	M-29	BUS F-30	
	L-29	DO LSS 9	

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PROGRAM: FAA INPUT MESSAGE

PROGRAM DISTRIBUTOR OUT		
HUB	WIRED TO	PURPOSE
1	LSS 10a C	
2	BUS x-1 BUS F-5	
3	LSS 6d C UNI 20 IN	
4	UNI 12 IN PD 5	
5	UNI 20 IN PD 4	
6	BUS N-27 UNI-20 IN	
7		
12	UNI 8 IN	
14	BUS F-8 UNI 8 IN	
23	BUS F-9 UNI 8 IN	
29	BUS F-10 UNI 8 IN	
35	LSS 8a C	
42	LSS 8b C UNI 9 IN	
47	LSS 9d C	
48	LSS 8c C	
53	PD 86	
64	PD 75	
75	PD 64 PD 130	
86	PD 53 PD 97	
91	HSS 1d IN PD 86	
97	PD 108 PD 97	
108	UNI 4 IN	
119	UNI 4 IN PD 130	
130	PD 75 PD 119	
132	UNI 15 IN UNI G IN	

[illegible]

PD STEP IN		
HUB		WIRED FROM
1.	STEP -DECODE	BUS e-28
2.	VOID STEP- DECODE	
3.	STEP ENCODE	INV 1 OUT
4.	VOID STEP- ENCODE	BUS x-34

SET IN CHAR. ADD	
HUB	WIRED FROM
11	BUS F-6
10	
9	
8	
7	BUS F-23
6	UNI 7 OUT
5	BUS F-11
4	UNI 1 OUT
3	
2	
1	
S	

SET PD. CHAR. IN	
HUB	WIRED FROM
8	
4	UNI 2 OUT
2	BUS h-13
1	BUS w-30

SET IN WORD ADD	
HUB	WIRED FROM
0	BUS N-27
1	BUS F-8
2	BUS F-9
3	UNI J OUT
4	BUS F-11
5	
6	BUS L-28
7	BUS F-6
8	
9	

SET PD WORD IN	
HUB	WIRED FROM
8	
4	BUS u-2
2	BUS h-13
1	BUS u-2

T & F UNIT - FUNCTION WIRING

PROGRAM: FAA INPUT MESSAGE

FILL					
FILL	INHIBIT HUB WIRED FROM	START HUBS WIRED FROM	END HUBS WIRED FROM	OUT HUBS WIRED TO	PURPOSE
a					
b					
c					
d					

FUNCTION INPUTS		
FUNCTION	INPUT HUB WIRED FROM	INHIBIT HUB WIRED FROM
1. OUTPUT STP	UNI G OUT	BUS F-25
2. OUTPUT START	UNI H OUT	
3. INPUT STOP	BUS h-20	
4. CORRECTION		
5. VOID		
6. END OF DATA	BUS F-29	
7. TRACK SWITCH	BUS h-20	
8. CLEAR BUFFER	BUS W-25	
9. WORD CLEAR		
10. STOP		
11. STOP		

INVERTERS			
INV.	IN HUB WIRED FROM	OUT HUB WIRED TO	PURPOSE
1	BUS e-28	PD STEP IN 3	PD STEPPING
2	UNI 10 OUT	AND 1 IN	BEGINNING OF MESSAGE
3	UNI 3 OUT	AND 3 IN	CHECK FOR SPACE
4	BUS x-34	AND 2 IN	
5	UNI-L OUT	AND 6 IN	OUT OF PLACE ≡
6			
7	BUS x-1	DEC IN 0-64	SS ADDRESS

STOP OUTPUT	
WIRED TO	PURPOSE

DECODER OUT	
WIRED TO	PURPOSE

DECODER IN HUB WIRED FROM FD b3

2-CHAR. FUNC. DET a		
PURPOSE:		
IN 1 HUB WIRED FROM	IN 2 HUB WIRED FROM	OUT HUB WIRED TO
DEC I-8	BUS 1-1	HSS 3a IN

2-CHAR. FUNC. DET. b		
PURPOSE:		
IN 1 HUB WIRED FROM	IN 2 HUB WIRED FROM	OUT HUB WIRED TO
BUS N-25	BUS a-25	HSCL W

T & F UNIT - MISCELLANEOUS CONTROL WIRING

PROGRAM: FAA INPUT MESSAGE

CH. PAR. REG. INPUT	
HUB	WIRED FROM
1	IN BUFF 1
2	IN BUFF 2
4	IN BUFF 4
8	IN BUFF 8
16	IN BUFF 16
32	CPR OUT 32
64	CPR OUT 64
P	CPR OUT P

CH. PAR. REG. OUTPUT	
HUB	WIRED FROM
1	
2	
4	
8	
16	
32	CPR IN 32
64	CPR IN 64
P	CPR IN P

CHANNEL PARITY CONTROL		
HUB	WIRED TO/FROM	PURPOSE
ACT	BUS d-23	
ERR.		
NC	AND 4 IN	

SELECT CHARACTER PARITY		
HUB	WIRED TO/FROM	PURPOSE
ODD	UNI 13 IN	
C		
EVEN		

ADVANCE CHANNEL PAR.	
HUB	WIRED TO/FROM
IN	BUS w-20
STEP	HSS 2d NS
OUT	AND 2 IN

SELECT CHANNEL PAR.	
HUB	WIRED TO/FROM
ODD C	
C	ODD
EVEN	

COMPUTER → I/O REGISTER		
HUB	WIRED TO	PURPOSE
A-1		
A-0		
B-1		
B-0		
C-1		
C-0		
D-1		
D-0		

MODE SELECTION	
HUB	WIRED FROM
IN-1	HSS 2a NS
IN-2	HSS 2b NS
IN-3	HSS 2c NS
OUT-1	UNI 14 OUT
OUT-2	HSS 2b S
OUT-3	HSS 2c S

I/O → COMPUTER	
HUB	WIRED FROM
a	
b	
c	

T & F UNIT - MISCELLANEOUS CONTROL WIRING (CONTINUED)

PROGRAM: FAA INPUT MESSAGE

PUNCH INT'L'K		
PURPOSE		
NS HUB	C HUB	S HUB
WIRED TO	WIRED TO	WIRED TO

MISCELLANEOUS HUBS	
HUB	WIRED FROM
FILL	
UNCONDI- TIONAL DELETE	BUS b-16
REVERSE HUBS	

HIGH SPEED I/O COMP	
HUB	WIRED FROM
W	2FD b OUT
X	UNI-15 OUT
Y	FD f3
Z	BUS b-19

ALTERNATE SWITCHES				
SW.	PURPOSE	HS HUB WIRED TO	C HUB WIRED FROM	S HUB WIRED TO
A				
B				
C				
D				
E				
F	TIME OUT	T.O. 1	FD f IN	

PROGRAM INDICATORS	
IND.	WIRED FROM
1	
2	
3	
4	
5	
6	

T & F UNIT

SPECIAL FAA HUBS

PROGRAM: FAA INPUT MESSAGE

TIME OUT		
HUB	WIRED TO	PURPOSE
ST	BUS X-21	
END	BUS F-29	
0		
1	ALT. SWITCH F N/S	
CL	BUS F-27	

SIM	
WIRED TO	PURPOSE

COMM. READY		
HUB	WIRED TO	PURPOSE
A SET		
B SET		
ST RDY		

I/O COMM.		
HUB	WIRED FROM	PURPOSE
2W	BUS F-26	INPUT DISCONNECT
X	FD f1	INPUT LOCKOUT
Y		
Z		
3W		
X		
Y		
Z		

APPENDIX XXI

**OUTPUT TRANSLATE AND FORMAT CONTROL UNIT
PLUGBOARD NARRATIVE AND WIRING CHARTS**

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ABSTRACT

The Output Inter Center Message program for the High Speed Paper Tape System described on the following pages has been developed to make more extensive use of the formatting capabilities of the Paper Tape System. The format illustrated in Table II represents the arrangement of data delivered to the High Speed Paper Tape System. The product of the program is an Inter Center Message that is arranged in the standard format of input messages which is illustrated in Appendix A of Circular Letter AT 200-12. The Computer subroutine used to assemble the Inter Center Message data must first transfer Identification and Remarks data to the Input/Output track that is presented to the Translator and Format Control Unit. Data comprising the Route are transferred to the second Input/Output track. In each case, a group of three non-data characters will accompany the data transferred to each of the Input/Output tracks. These special characters cause groups of communication characters such as channel parity and end of message to be included in the Inter Center Message. The arrangement of the group of characters transferred to the first Input/Output track is always the same while the arrangement of the group transferred to the second Input/Output track varies with the presence or absence of Remarks. The methods used in processing the Enroute, Proposed, and Modification type messages by the Output Paper Tape System are alike; i. e., the data received from the Computer is transmitted in its entirety. The receipt of a Continuation type message will cause the Output Paper Tape System to pad the Speed, Fix Time, and Altitude fields with space codes.

Although the data presented to the Output Paper Tape System is laid out in a format that closely resembles the input from the High Speed Paper Tape System (See Table I of the Input Message Documentation), strict adherence to this format with regard to data information is not required although Speed, Coordinate Fix Time, and Altitude must be consecutive fields to take advantage of the "fill" feature of the Translator and Format Control Unit. Because of the above mentioned requirement and the necessity of including control characters to generate certain communication characters, this format has been designated Format A.

INTRODUCTION:

The Inter Center Message program for the Output Paper Tape System originally developed for transmitting Inter Center Messages between Computers did not make full use of the capabilities of the Output Paper Tape System because an optimum computer format was not available at that time. The Inter Center Message Program for the Output Paper Tape System described herein will reduce the computer processing time by assuming a greater portion of the formatting tasks.

DETAILED COMMENTS ON THE PROGRAM:

The detailed description of the methods used in the Inter Center Message Program for the Output Paper Tape System is described in detail in the following pages. The description has been divided into sections for ease in understanding. Sections which refer to diagrams will have the number of the diagram to which they refer notated immediately after the heading of the program area described.

I. Substation Address:

The first character (I/O track position 120 or Word 0 Character 11) of the Output Message to be received by the Output T & FC Unit is a substation address. (See Table II - Configuration of the I/O Tracks presented to the T & FC Unit). The Output Distributor, upon receipt of the substation address, sequentially scans each output position until the station requested is found. When the proper substation is found, communication between the found substation and the T & FC Unit is established.

Substation addresses in storage (computer) are assumed to be alphabetic. The Univac character, used to address a particular substation, must be translated to the teletype character that has a binary sum of levels 1 through 4 equal to the substation number desired. For example: the letter E, in teletype, is made up of a punch in level 1 (2^0) and no punches in levels 2 through 4. E therefore, has a binary value of 1. Since a Univac E is translated to a teletype E (see Decoder Group I and II, Wiring Documentation), it may be used to address substation 1. Likewise, a teletype D has a binary value of 9 for levels 1 through 4, i. e., punches in levels 1 (2^0) and 4 (2^3) only. A Univac D which is translated as a teletype D may then be used to address output

substation 9. The table shown below summarizes the Univac alpha characters that may be used to address Output substations 1 through 16.

TABLE I

SUBSTATION ADDRESSES

Substation																	
Address	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
Univac																	
Character	E	L	A	Δ	S	I	U	O	D	R	J	N	F	C	K	T	

II. Teletype Conditioning Codes (Diagram I)

A. End of Message Codes:

"End of message" codes are transmitted at the beginning and end of each message. Assuming all equipments and line linkage to be functioning normally, the "end of message" at the beginning serves no function. Should the previous message, through some transient fault in the equipments or linkage, have been interrupted, the following conditions will exist: The stunt box on the 28 RO will be in a "conditioned" state, waiting for the "end of message" codes to clear it; the punch circuitry on the read-punch module will be waiting for the "end of message" codes to cause the punching of the length of "leader" characters separating each message. The Translate and Format Control Unit will, of course, have time-out and disconnected and locked out the reader on the read-punch module.

The "end of message" codes at the beginning of a message serve to clear the system linkage to a normal state to insure that transient faults effect no more than one message.

Since PD 1 is programmed to step on encode, the PD counter will be advanced to 2 after the substation address is transferred to the Output Distributor. PD 1, through LSS 8a N/S, Enables UNI-17 to frustrate the circuit from CPR-OUT to CPR-STEP. This action prevents the toggling of the Channel Parity Register. PD 2 through LSS 10a N/S activates FD a so that figures, carriage return, and letters are encoded during the function sequence portion of the substation address character cycle. As each of these characters

are encoded, UNI-18 is enabled so that the cleared contents of the Channel Parity Register will not be disturbed. The signal from hub 4 of FD a is routed to the in of LSS 10d. Since LSS 10 is N/S, the signal is terminated at the N/S hub of pole d. If FD a is enabled after PD 13 busses F-10, 9, 8, 7, and 6, are enabled. This action drops out all selectors, enables function Input 6 (end of message) and releases the output substation currently connected to the distributor.

B. Condition Codes and Station Directing Codes:

The Station Directing Codes are made up of three characters which are used to identify the 28 Set (center) to which the output message is to be transmitted. The codes used for each center are the assigned identifier of the ARTC Centers. For example:

ZPT - Pittsburgh,	ZDC - Washington,	ZBW - Boston,
ZCL - Cleveland,	ZNY - New York,	ZID - Indianapolis,
ZAC - Atlantic City, etc.		

The appropriate Station Directing Codes immediately follow the substation address (see Table II). Note that in each case the first character of the SDC codes is a Z. PD is stepped to 3 with the decoding of the Z. PD 3, through the N/S hub of LSS 10b, enables bus a-25. Bus a-25 enables Delete Character in, FD b, and INV 2 in. Enabling the in of INV 2 breaks the circuit from DEC II-28 (Z) to UNI-4. This action prevents the encoding of the letter Z at this time. The wiring to the Delete Character in hub prevents a translator error from occurring at this time. During the function sequence portion of the Z character cycle, carriage return, carriage return, letters, and Z are encoded. The codes generated from FD a1, 2 and 3 are used to condition the receiving 28 Set for the receipt of the Station Directing Codes which follow. During each perform time of FD b, UNI-18 is enabled thereby preventing the toggling of the Channel Parity Register. The next two characters to appear at the Incoming Buffer Output Register are the last two characters of the Station Directing Codes. Receipt of the second SDC code advances PD to 4 and receipt of the third advances PD to 5. These PD outs which are qualified by LSS 8 b, and c, are also routed to UNI-17 and 18 to prevent the toggling of the Channel Parity Register. Advancing PD to 5 with receipt of the third SDC Character causes bus h-14 to be enabled. Enabling bus h-14 sets the word and character address counters to 8-6 and activates

FD c. FD c encodes the following beginning of message character: letters, letter, line feed, letters.

III. Setting Word and Character Address Counters with PD Outs (Diagram II)

Table II "Configuration of the I/O Tracks Presented to the T & FC Unit" shows an output message arranged in the format that is transferred from the I/O tracks to the T & FC Buffer. To arrange the identification data in a format that is compatible with the requirements of the input message, PD 5, 12, 19, 25, 30, 36, 41, etc. are stepped on Decode. This enables the Word and Character address counters to be set directly from these PD outs. The PD out that occurs at the end of each field (note that in some cases the fields are multiple) is used to set the Word and Character address counter to the first character of the next field to be transferred to output. For example: it is shown in Table II and Diagram II that PD 19, which marks the end of the Aircraft Identification field, sets WAK to 6, and CAK to 5, for receipt of the first character to the Type Aircraft field. Likewise, PD 41 which marks the end of the Altitude field and the end of the Identification portion of the ICM, sets WAK to 0 and CAK to 2. Decoding the ? character at Word 0 Character 2 activates FD d and steps PD 42. FD d1 encodes carriage return and FD d2 encodes letters. Note that enabling UNI-9 and UNI-11 (See Diagram I) to encode carriage return and letters respectively does not inhibit the toggling of the Channel Parity Register. FD d3 enables the Activate hub. Enabling this hub while in the output mode causes the contents of the Channel Parity Register to be transferred to output and clears the register to all one's. FD d 4 through bus 1-1 encodes a line feed. Enabling bus 1-1 in addition to encoding line feed inhibits the toggling of the Channel Parity Register.

The next character to be decoded is the @ character located at WAK 0 CAK 1. Decoding the @ character advances PD to 43 and enables bus b-25 which in turn encodes letters through UNI-11. Bus b-25 enables UNI-18 to inhibit the toggling of the Channel Parity Register.

IV. Beginning of the Route (Diagram III)

The next group of characters to be transferred to output after the Channel parity group (following the Identification Data) are those comprising the route. The decoding of the @ character steps the program distributor to 43. PD 43 through LSS 9a N/S enables bus F-30 so that PD 43 will step on encode. After the letters character

	11	10	9	8	7	6	5	4	3	2	1	S
0	2 S/S	2 S	3 D	4 C	X	X	X	X	X	41 ?	42 @	%
1	43 U	P	△	T	O	△	T	H	R	E	E	54 △
2	55 W	0	R	D	S	△	O	F	△	R	E	66 M
3	67 A	R	K	S	△	△	△	△	△	△	△	78 △
4	△	36 ∅	1	1	∅	40 △	△	25 ∅	2	6	∅	29 △
5	△	△	△	△	△	△	30 E	∅	1	∅	∅	35 △
6	△	△	△	△	△	△	19 △	D	C	7	•	24 △
7	△	△	△	△	12 E	A	3	7	•	•	18 •	△
8	△	△	△	△	△	5 I	N	D	•	△	10 N	11 △
9	△	△	△	△	△	△	△	△	△	△	△	△

ADDRESSOR - KIND

AIRCRAFT IDENT

TYPE AIRCRAFT

ALTITUDE - SPEED

FIX TIME

REMARKS (36 CHAR)

	11	10	9	8	7	6	5	4	3	2	1	S
0	44 D	C	A	•	V	8	•	•	•	•	54 △	•
1	•	•	•	V	4	2	2	•	•	65 △	M	D
2	W	•	•	•	•	•	•	•	76 △	•	•	•
3	•	•	•	•	•	•	•	87 △	•	•	•	•
4	•	•	•	•	•	•	98 △	•	•	•	•	•
5	•	•	•	•	•	109 △	112 •	113 •	•	•	•	•
6	•	•	•	•	122 △	•	•	•	•	•	•	•
7	•	•	132 •	1-2 △	3 △	△	△	△	△	△	△	△
8	△	△	△	△	△	△	△	△	△	△	△	△
9	110 [?]	111 [•]	113 [•]	•	△	△	△	△	△	△	△	△

ROUTE - THE END OF THE ROUTE IS DENOTED BY TWO SPACE CODES, IN THIS EXAMPLE THE END OF THE ROUTE IS DENOTED BY THE △ AT WORD 7 CHARACTER 7

* IF THERE ARE NO REMARKS ?,@,%, ARE STORED IN THE HIGH ORDER POSITIONS OF WORD 9.

IF THERE ARE REMARKS ?,@,\$, ARE STORED IN THE HIGH ORDER POSITIONS OF WORD 9.

TABLE II
CONFIGURATION OF THE I/O TRACKS PRESENTED TO THE T&F UNIT

has been transferred to output PD is advanced to 44. PD 44 through LSS 9a N/S also enables bus F-30 to step on encode and activate FD f at function time. FD f1 through LSS 9b N/S initiates a track switch function. The track switch function in the output mode performs the following functions:

- 1) Track switch
- 2) Transfers the data from the I/O track to the T & FC buffer
- 3) Clears WAK and CAK to 0-11, and,
- 4) Clears PD.

FD f2 through LSS 9c N/S energizes UNI-K which sets PD equal to 43. The signal from FD f3 is terminated at the N/S hub of HSS 3a and the signal from FD f4 picks up LSS 9. The result of the operation described above is that the T & FC is now prepared to receive the route information from the buffer.

V. End of Route Before the Fifth Segment (Diagram IV)

The processing of route information can end anytime after the first airway. The end of the route is determined by the presence of two consecutive space characters. This program has been designed so that a channel check group is always transferred to output after the last route segment. If there are five segments or less of route information there will be one channel check group pertaining to route information. If there are more than five segments there will be two channel check groups; one after the fifth segment and one after the last segment. The transfer of the channel check group to output is accomplished by decoding ? and @ characters from Word 9 Character positions 11 and 10 (See Table II). If this ICM contains Remarks a \$ will be present in Word 9 Character 9. If no Remarks are present a % character will be present in Word 9 Character 9.

The space character decoded at the end of each segment routes a signal to Encoder I-4 and Inhibit Output Precedence via bus e-28. The signal denoting the decoding of the space character is also routed to 2FD a1 via HSS 4a N/S, HSS 5c N/S and bus N-27. If the next character to be received is also a space (this denotes the end of the route information) Hub 2 of 2FDa is enabled causing the out hub of 2FDa to emit. This signal is routed through UNI-14, LSS 9d (LSS 9 was picked up at the beginning of the route) to bus X-24 which picks up HSS 1, and HSS 3. Picking up HSS 3 causes

power to be applied to the Select hub of HSS 1a one clock period (42 u) after the pick up hub was enabled. Power through HSS 1a S enables bus u-2 and h-20. Bus h-20 routes power through HSS 4b N/S to UNCONDITIONAL DELETE. Applying power to the Unconditional Delete hub inhibits the encoding of the second space, i. e., the space that caused 2FDa to emit. Bus h-20 also activates FD g and FD f. Bus u-2 enables AND 5 and since PD 100 is not emitting AND 5 out emits a signal which is routed to PD STEP IN 3 to cause the PD that is currently emitting to step on encode. During the function sequence portion of the second space character cycle FD f and FD g emit during each perform time. FD f1, 2, and 4, perform no function. FD f3 through HSS 3a, S, HSS 2d N/S sets WAK to 9 and since HSS 5 is N/S CAK to 11. Signals from FD g1, 2, and 3, are terminated without performing any special functions. FD g4 drops out HSS 1 to remove power from the Unconditional Delete hub.

The next character to be decoded is the ? character from Character 11 of Word 9 (see Table I and Diagram II). Decoding the ? activates FDd. FDd encodes carriage return and letters from FD d1 and 2 respectively. FD d3 enables the ACTIVATE hub to unload and clear the contents of the Channel Parity Register. FD d4 encodes line feed and inhibits the stepping of the Channel Parity Register.

Decoding the @ character from Character 10 Word 9 encodes a letters character (See Diagram I) and inhibits the toggling of the channel parity register. No function detectors are enabled during the function sequence of the @ character cycle.

If the ICM does not contain Remarks a % character will be decoded from Character 9 Word 9 (See Diagram I). In this instance a signal from DEC II-29, representing the % character is routed to bus h-13 to activate FDa. During the function sequence portion of the % character cycle figures, carriage return, and letters are encoded. In each case the stepping of the Channel Parity Register is inhibited by enabling UNI-17 in. The perform 4 signal from FD a4 is routed through LSS 10D S to enable the end of message busses F-6, 7, 8, 9, and 10.

VI. End of Route after the Fifth Segment (Diagram V)

If the route information contains at least five segments, PD will be stepped to 97 with the decoding of the space marking the end of the fifth segment. To unload the channel check group onto tape, PD 97 is routed through HSS 3 c N/S to Bus N-26 to set CAK and WAK to 11-9.

Upon decoding the ? character at character 11 word 9 PD is stepped to 98. Decoding the ? also activates FD d (see Diagram II). Carriage return and letters are encoded from FD d1 and 2 respectively. FD d3 enables the ACTIVATE hub and FD d4 encodes line feed and enables UNI-17 to prevent the half adding of line feed to the contents of the Channel Parity Register. Decoding the @ character from character 10 word 9 steps the program distributor to 99. Decoding the @ character causes a letters character to be encoded (see Diagram I). In addition, UNI-18 is enabled thereby preventing the addition of letters to the contents of the Channel Parity Register. The signal from PD 99 is routed to the in of INV 3 and through the N/S hub of HSS 3d to Bus N-28. Enabling Bus N-28 picks up HSS 5 and sets WAK and CAK to 5-5 at the beginning of the next character cycle.

If there is a space at Word 5 Character 5 DEC I-1 emits through HSS 4a N/S, HSS 5c S, UNI-14, LSS 9d S, and Bus X-24, to pick up HSS 3 and HSS 1. Picking up HSS 1 routes power to HSS 1a S to activate Bus u-2 and h-20. This action deletes the space character at Word 5 Character 5 by directing power to THE UNCONDITIONAL DELETE via the N/S hub of HSS 4b. Power is also applied to the in of AND 5 but because PD is still at 100 UNI-8 is not enabled, so that PD 99 is not stepped on encode. Note that in all other instances when power is applied to bus u-2 and h-20 to re-set WAK and CAK or PD, PD must be stepped on encode. Several clock periods after HSS 1 has been picked up PD will be advanced to 100. PD 100 through HSS 3b S and UNI-1 activates FD f. The signal from FD f1 is terminated at HSS 2a N/S. The signal from FD f2 is terminated at HSS 2b N/S. FD f3 is routed through HSS 3a S, HSS 2d N/S to bus N-25. The signal at bus N-25 sets WAK to 9 and because HSS 5 is picked up CAK is set to 9.

If there is a % character at WAK 9-CAK 9 FD a is enabled to generate end of message codes and initialize the T & FC Unit. In this instance, HSS 5 is dropped out by routing PD 101 through UNI-6 to HSS 5 DO. Likewise if there is a \$ character at WAK 9-CAK 9 HSS 5 will be dropped out by the signal emitted from PD 101.

If there is no space at WAK 5-CAK 5 power will not be applied to bus u-2 and h-20 hence, there will be no input to INV 4. This condition will cause INV 4 to emit a signal to enable AND 6. Since the decoding of the character at word 5 character 5 caused PD to be advanced to 100, AND 6 out will emit a signal that is directed to the drop out of HSS 5. Route processing will then continue until two

consecutive spaces are detected. The action taken by the T & FC Unit upon the detection of the two spaces will be exactly like that described in the section describing TC. C operations when the end of the route occurs before the fifth segment.

VII. Remarks (Diagram VI)

If an ICM contains Remarks, a \$ character will be contained in word 9 character 9. Decoding the \$ causes a signal to be emitted from DEC II-13. This signal is used to encode figures C, pickup HSS 2, HSS 4, and activate FD f. FD f1 through LSS 9b S, HSS 2a S, and UNI-L performs a track switch function. The signal from FD f2 is routed through LSS 9c S, HSS 2b S, to bus F-25. Bus F-25 resets PD to 43 and WAK and CAK to 1-11. Setting WAK and CAK to 1-11 and PD to 43 enables 36 characters of Remarks to be unloaded to tape. PD 79, which marks the encoding of the 36th Remarks character, activates UNI-D causing WAK and CAK to be reset to 0-2. Decoding the ? and @ characters from character positions 2 and 1 of word 0 unloads the channel parity groups of characters onto tape. Decoding the % character from word 0 character S activates FD a (see Diagram I). Activating FD a after PD 13 encodes the end of message groups of characters and enables the end of message group of busses to terminate the transmission.

VIII. Continuation Message (Diagram VII)

Intercenter messages exceeding eight route segments will require those segments in excess of eight to be transmitted via a continuation or X Message. The Continuation message is processed in much the same manner as the Enroute, Modification, and Proposed messages. The outstanding exceptions are that the Aircraft Speed, Coordinate Fix Time, and Altitude are erased to space characters. The Coordination Fix information is deleted altogether, i. e., the field is cleared to pad characters. The erasure of the Speed, Fix Time, and Altitude fields to spaces is accomplished by the Output T & FC Unit while the erasure of the Coordination Fix Field to pads is accomplished by the computer program.

If an X is decoded at PD 11, a signal is emitted from AND 3. This signal is routed to the pick up of LSS 7. PD 25, which denotes the end of the Type Aircraft field, enables FD e. Hub 1 of FD e initiates a FILL function. FD e2, which emits until the fill function is terminated, enables bus h-7 causing PD to be stepped on encode

during the execution of the fill function. Since the Speed, Fix Time and Altitude are consecutive, PD 30, and 36, are qualified by LSS 7. PD 40 is used to end the fill function. PD 41, which marks the end of the Altitude field, sets WAK and CAK to 0-2 to enable the unloading of the channel parity group of characters. Message processing continues as previously described in the sections dealing with route processing.

IX. Channel Parity Register (Diagram VIII)

Each time a character is encoded a signal or combinations of signals appear at Encoder Output hubs 1, 2, 4, 8, and 16. If the character was received from Encoder Group I (Alpha) no signal is present at Encoder Output hub 32. If the character at Encoder Output was received from Encoder Group II (Figures) a signal is present at Encoder Output hub 32. Hub 64 of the Encoder Output group does not have a signal present during the encoding of Flight Plan information. If the number of signals present at Encoder Output hubs, 1 through 64 is even, a signal will be present at Encoder Output Par. No signal will be present at the Par Hub if the count is already odd. Although levels 1 through 16 are the only levels transmitted over the teletype lines, levels 32 and Parity must be punched in the output tape because they are included in the parity count and the Output Reader at the addressed substation checks each character for proper parity prior to transmission.

Each character punched in the Output Paper tape is half-added or tallied to the contents of the Channel Parity Register as shown by the wiring of encoder Output hubs to Channel Parity Register Input hubs. The result of the tally is that a character is generated that will reflect odd parity for each of the five tape channels. The Channel Parity Character, upon being received at the Output Reader, is also checked for proper parity. It, therefore, must also have correct character parity. Correct parity for the Channel Parity Character is accomplished by wiring the parity level of Encoder Output through an Inverter to the Parity level in the Channel Parity Register Input. The principle upon which this wiring is based is that an even number when added to an odd channel parity as shown on line one below, and the letter A (teletype) is half-added to its content the result shown on line 3 has even parity. If the contents of the register were transferred to output the incorrect parity of the Channel Parity Character would be detected at the Output Reader and Operations would be stopped by the error condition.

Levels	P	32	16	8	4	2	1	
Parity Register	1	1	1	1	1	1	1	ODD
Teletype A in Encoder Output	1	0	0	0	0	1	1	<u>/ODD</u>
Parity Register	0	1	1	1	1	0	0	EVEN

If the parity level of the Encoder Output is inverted prior to the tally, the Parity of the Channel Parity Character is correct.

Levels	P	32	16	8	4	2	1	
Parity Register	1	1	1	1	1	1	1	ODD
Teletype A in Encoder Output	0	0	0	0	0	1	1	<u>/EVEN</u>
Parity Register	1	1	1	1	1	0	0	ODD

X. Invalid Character Error - Synchronization Error - Character Parity Error (Diagram IX)

The transfer of a parity incorrect character from the computer to the T & FD Unit Incoming-Buffer-Output will cause an error signal to be emitted from the Character Parity - Odd hub.

The rate at which characters are transferred from the UFC Drum to the T & FC buffer is governed by timing signals from the High Speed Drum. If the buffer and the drum should get out of synchronization anytime during the transfer of information from the drum to buffer, the buffer address is cleared and the message from the drum is re-read. If the synchronization error occurs on the second read the buffer address is cleared and the flag character (ç) is inserted at an address that has been defined by pinboard wiring. The buffer content is then available for transfer to the T & FC. When the cent character identifying this as an erroneous message is received by the T & FC, a signal will be emitted from Decoder I hub 30.

The two above mentioned error signals or any signal resulting from the receipt of an Invalid character are routed to the in of Function Detector 4 through Unibus 15. At Perform 1 time of the error character cycle a line feed character is encoded. At Perform 2 time an E is encoded and at Perform 3 time a Bell character is

encoded. Hub 4 of Function Detector h is to activate High Speed Control Line W. The next time the Output T & FC is demanded, the high speed control line is used to re-initiate the transmission of the message and call for a type out of the output error notification message. Both 28 Sets, the Output set transmitting the message and the Input 28 Set at the center receiving the message, will line feed, print an E, and ring the 28 Set bells providing a visual and audible error indication.

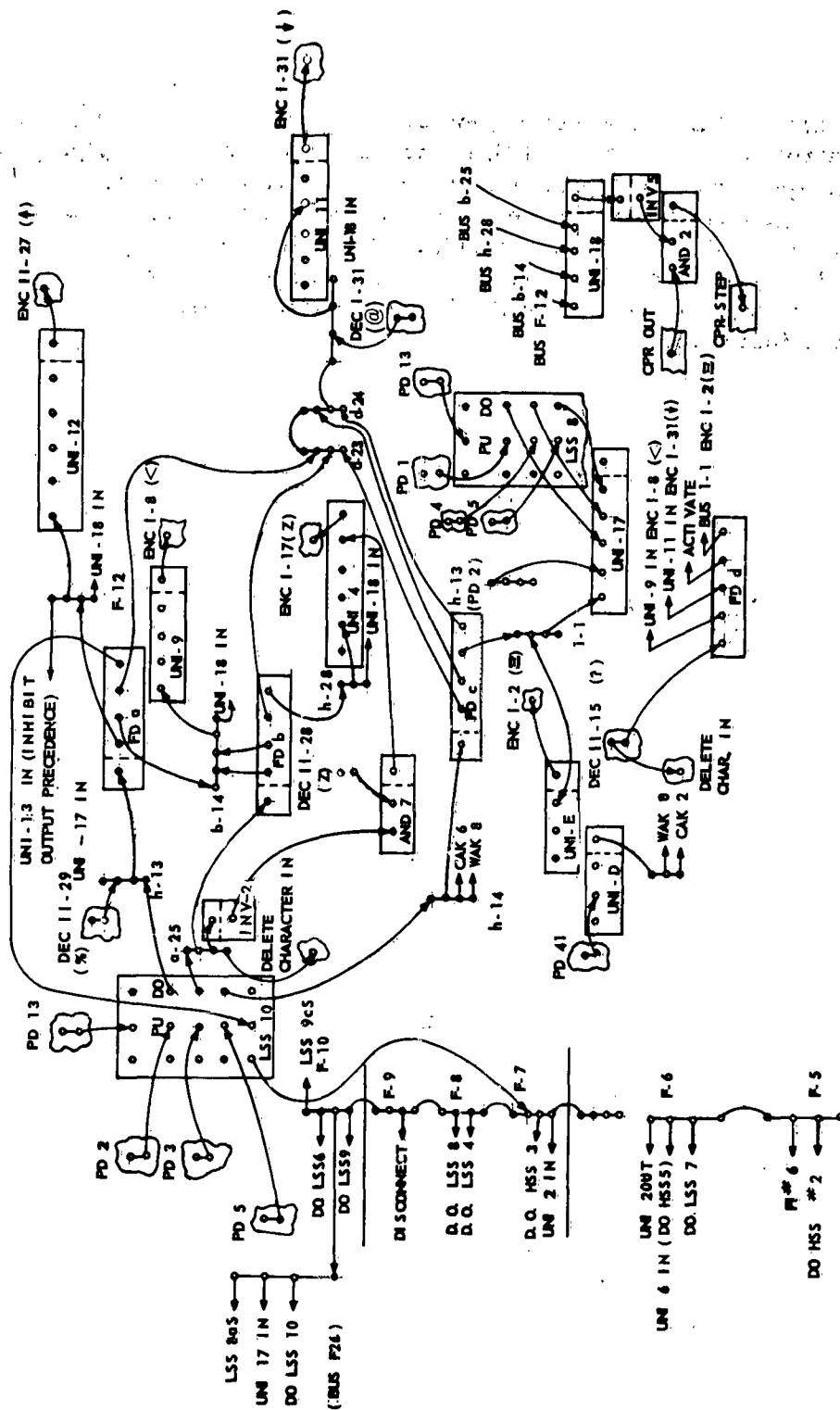
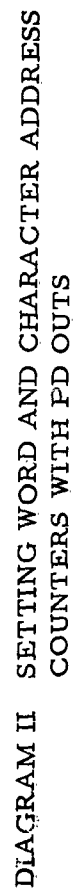


DIAGRAM I CONDITIONING CODES - BEGINNING OF MESSAGE -
END OF MESSAGE



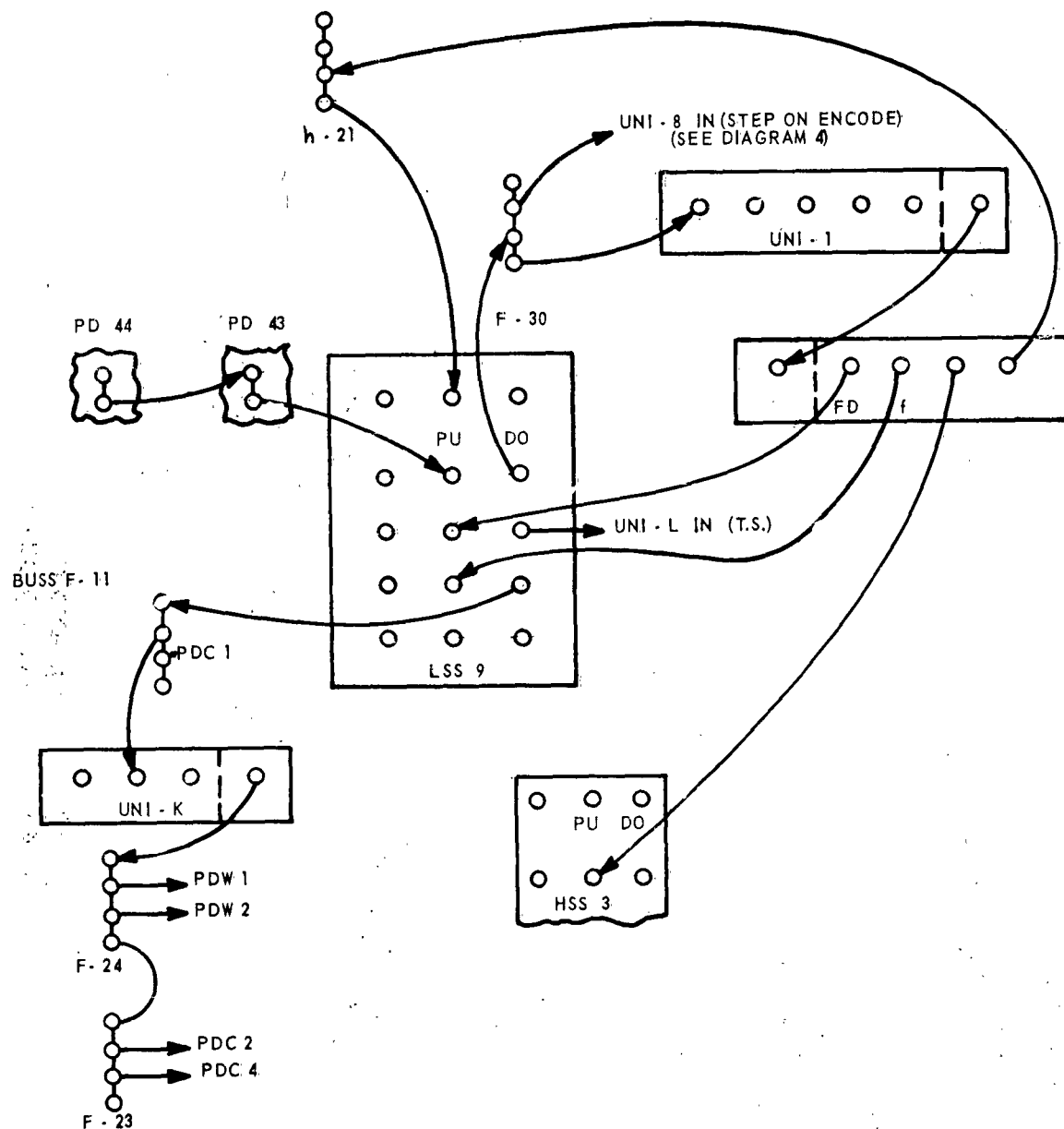


DIAGRAM III BEGINNING OF THE ROUTE

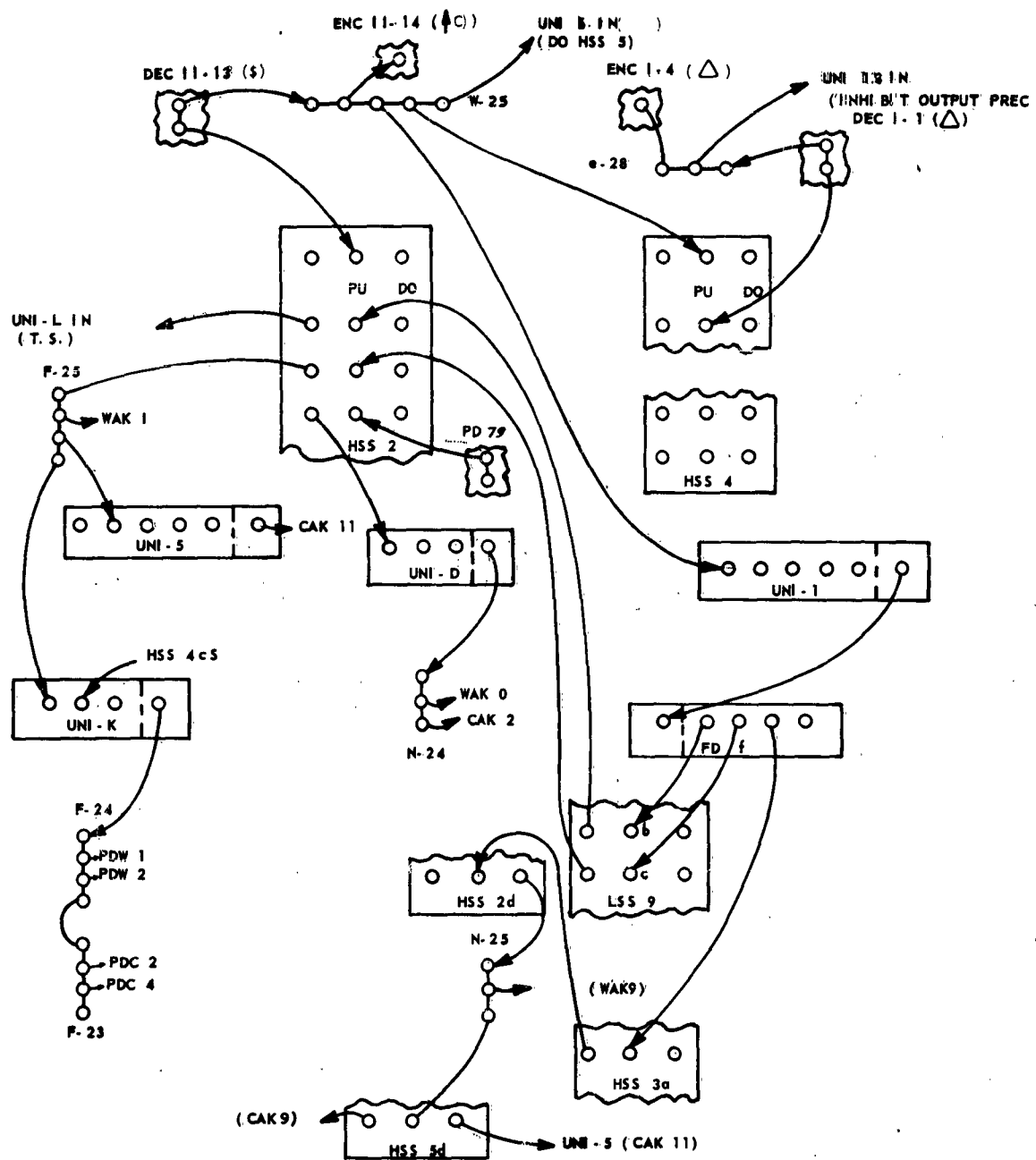


DIAGRAM VI REMARKS

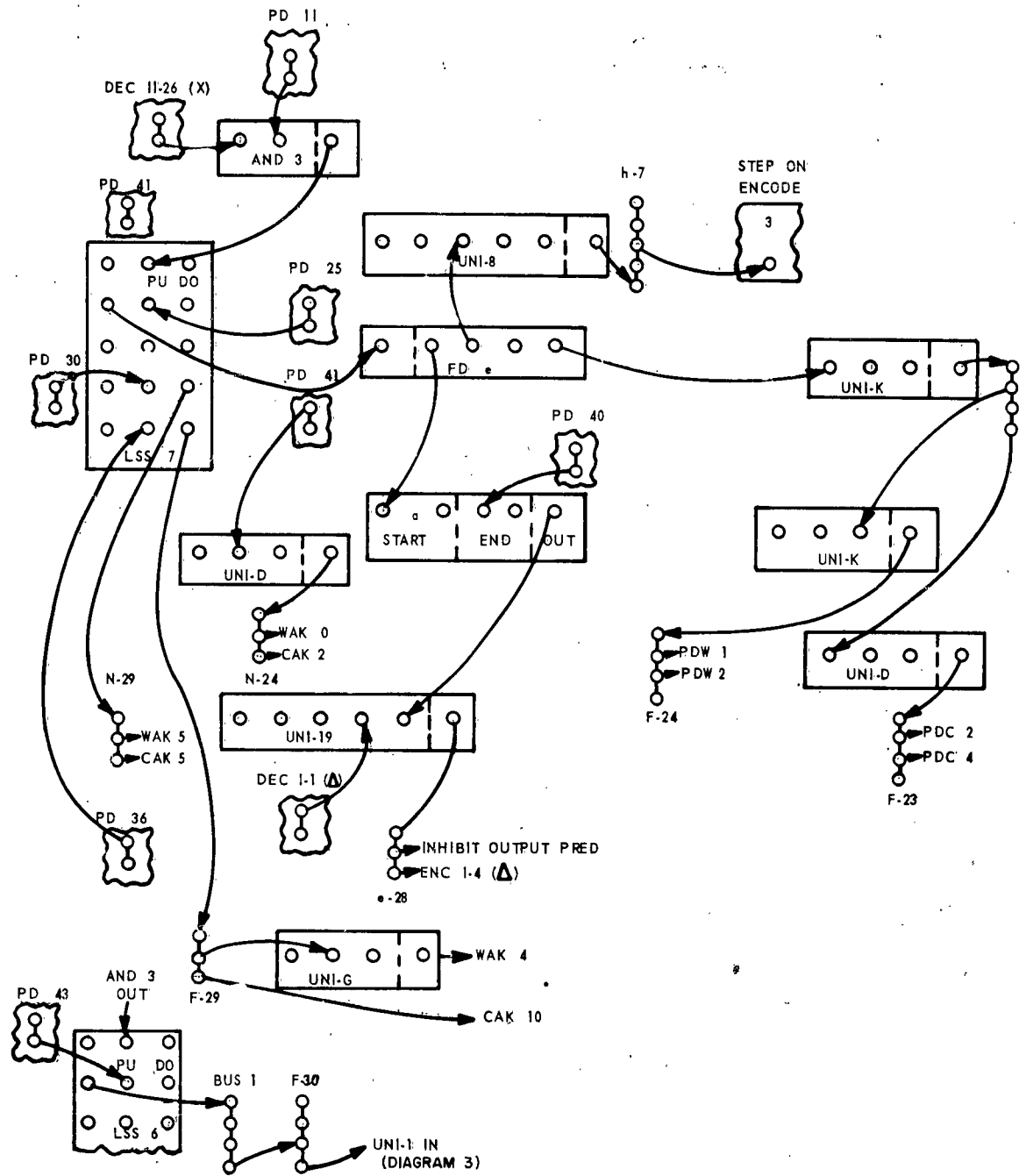


DIAGRAM VII CONTINUATION

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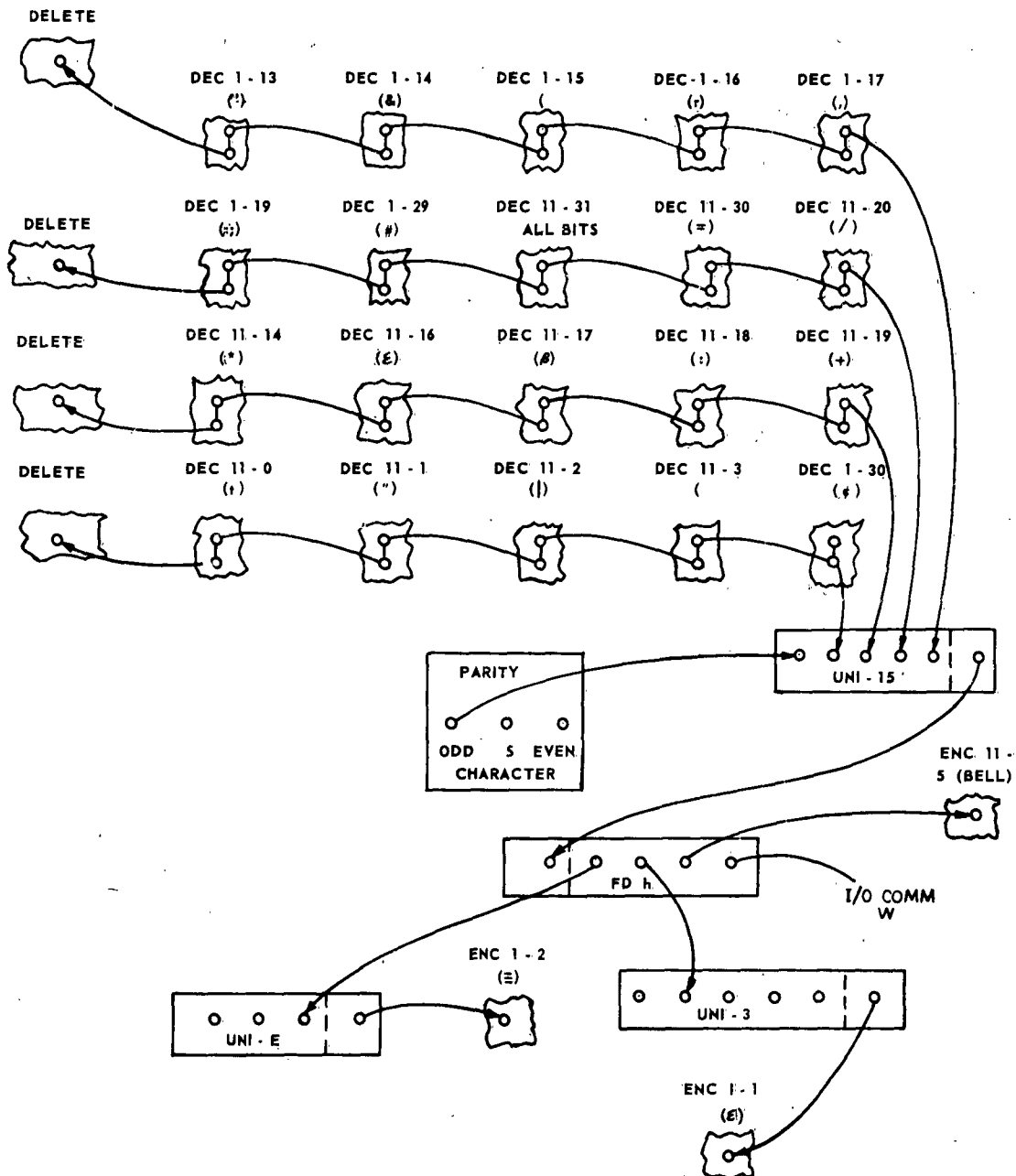


DIAGRAM IX INVALID CHARACTERS - INCORRECT CHARACTER
PARITY - SYNCHRONIZATION ERROR

WIRING DOCUMENTATION:

This section contains the instructions which are to be followed in wiring the Inter Center Message Board for the Output Paper Tape System. Great care should be exercised in wiring the board and each wire should be back checked, i. e., if DEC I-11 is wired to ENC II-6, hub 6 of ENC II should be checked to be sure it is wired to DEC I-11. The abbreviations used are consistent with those shown in the bulletin "Tabulation Abbreviations of T & F Plugboard" dated July 15, 1959.

T & F UNIT - DECODER WIRING

Date: January 24, 1961

Program: Output Format A

DECODER OUTPUT, OP. I CODE			
HUB	WIRED TO	CHAR.	PURPOSE
0	DELETE	1	
1	UNI-19 IN IN HSS 4a	△	
2	ENC II-29	-	
3	ENC II-22	ø	
4	ENC II-23	1	
5	ENC II-19	2	
6	ENC II-1	3	
7	ENC II-10	4	
8	ENC II-16	5	
9	ENC II-21	6	
10	ENC II-7	7	
11	ENC II-6	8	
12	ENC II-24	9	
13	DELETE DEC I-14	'	INVALID
14	DEC I-13 DEC I-15	&	INVALID
15	DEC I-14 DEC I-16	(INVALID
16	DEC I-15 DEC I-17	r	INVALID
17	DEC I-16 UNI-15 IN	.	INVALID
18	ENC II-28	.	PAD
19	DELETE DEC I-29	:	INVALID
20	ENC I-3	A	
21	ENC I-25	B	
22	ENC I-14	C	
23	ENC I-9	D	
24	UNI-3 IN	E	
25	ENC I-13	F	
26	ENC I-26	G	

DECODER OUTPUT, OP. II CODE			
HUB	WIRED TO	CHAR.	PURPOSE
0	DELETE DEC II-1	t	INVALID
1	DEC II-0 DEC II-2	"	INVALID
2	DEC II-1 DEC II-3		INVALID
3	DEC II-2 DEC I-30)	
4	ENC I-11	J	
5	ENC I-15	K	
6	ENC I-18	L	
7	ENC I-28	M	
8	ENC I-12	N	
9	ENC I-24	O	
10	ENC I-22	P	
11	ENC I-23	Q	
12	ENC I-10	R	
13	P.U. HSS 2 BUS W-25	\$	
14	DELETE DEC II-16	*	INVALID
15	DELETE FDDIN	?	
16	DEC II-14 DEC II-17	Σ	INVALID
17	DEC II-16 DEC II-18	ß	INVALID
18	DEC II-17 DEC II-19	:	INVALID
19	DEC II-18 UNI-15 IN	+	INVALID
20	UNI-15 IN DEC II-30	/	INVALID
21	ENC I-5	S	
22	ENC I-16	T	
23	ENC I-7	U	
24	ENC I-30	V	
25	ENC I-19	W	
26	ENC I-29 AND 3 IN	X	

Date: January 24, 1961

T & F UNIT - DECODER WIRING (Con't)

Program: Output Format A

DECODER OUTPUT, GP. I CODE			
HUB	WIRED TO	CHAR.	PURPOSE
27	ENC I-20	H	
28	ENC I-6	I	
29	DEC I-19 DEC II-31	#	INVALID
30	DEC II-3 UNI-15 IN	+	SYNC, ERROR
31	DELETE BUS b-25	•	

DECODER OUTPUT, GP. II CODE			
HUB	WIRED TO	CHAR.	PURPOSE
27	ENC I-21	Y	
28	AND 7 IN	Z	
29	DELETE BUS h-13	%	
30	DEC II-20 DEC II-31	-	INVALID
31	DEC II-30 DEC I-29	All Bits	INVALID

T & F UNIT - ENCODER WIRING

Date: January 24, 1961

Program: Output Format A

ENG. INPUT, GP. I		
HUB	WIRED FROM	CHAR.
0		
1	UNI-3 OUT	E
2	UNI-E Out	≡
3	DEC I-20	A
4	BUS e-28	△
5	DEC II-21	S
6	DEC I-28	I
7	DEC II-23	U
8	UNI-9 OUT	<
9	DEC I-23	D
10	DEC II-12	R
11	DEC II-4	J
12	DEC II-8	N
13	DEC I-25	F
14	DEC I-22	C
15	DEC II-5	K
16	DEC II-22	T
17	UNI-4 OUT	Z
18	DEC II-6	L
19	DEC II-25	W
20	DEC I-27	H
21	DEC II-27	Y
22	DEC II-10	P
23	DEC II-11	Q
24	DEC II-9	O

ENG. INPUT, GP. II		
HUB	WIRED FROM	CHAR.
0		
1	DEC I-6	3
2		≡
3		↑
4		△
5	FD h 3	BELL
6	DEC I-11	8
7	DEC I-10	7
8		<
9		↗
10	DEC I-7	4
11		↙
12		⊙
13		→
14	BUS W-25	0
15		←
16	DEC I-8	5
17		+
18		↖
19	DEC I-5	2
20		↓
21	DEC I-9	6
22	DEC I-3	ø
23	DEC I-4	1
24	DEC I-12	9

ENG. INPUT, GP. III		
HUB	WIRED FROM	CHAR.
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
20		
21		
22		
23		
24		

ENG. INPUT, GP. IV		
HUB	WIRED FROM	CHAR.
0		
1		
2		
3		
4		
5		
6		
7		
8		
9		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
20		
21		
22		
23		
24		

T & F UNIT - ENCODER WIRING (Continued)

Date: January 24, 1961

Program: Output Format A

ENC. INPUT GP. I		
HUB	WIRED FROM	CHAR.
25	DEC I-21	B
26	DEC I-26	G
27		↑
28	DEC II-7	M
29	DEC II-26	X
30	DEC II-24	V
31	UNI-11 OUT	↓

ENC. INPUT. GP. II		
HUB	WIRED FROM	CHAR.
25		⊕
26		↘
27	UNI-12 OUT	↑
28	DEC I-18	.
29	DEC I-2	/
30		⊖
31		↓

ENC. INPUT. GP. III		
HUB	WIRED FROM	CHAR.
25		
26		
27		
28		
29		
30		
31		

ENC. INPUT. GP. IV		
HUB	WIRED FROM	CHAR.
25		
26		
27		
28		
29		
30		
31		

Date: January 24, 1961

T & F UNIT - DECODER/ENCODER WIRING

Program: Output Format A

INCOMING BUFFER OUTPUT		
HUB	WIRED TO	PURPOSE
1	DEC IN 1	
0	DEC IN 0-1	
2	DEC IN 2	
0	DEC IN 0-2	
4	DEC IN 4	
0	DEC IN 0-4	
8	DEC IN 8	
0	DEC IN 0-8	
16	DEC IN 16	
0	DEC IN 0-16	
32	DEC IN 32	
0	DEC IN 0-32	
64		
0		
PAR	DEC IN PAR	
0	DEC IN 0-PAR	

ENCODER OUTPUT		
HUB	WIRED TO	PURPOSE
1	BUS w-30	
2	BUS v-30	
4	BUS u-30	
8	BUS x-34	
16	BUS w-20	
32	BUS 1-34	
64		
PAR	BUS x-1	

INCOMING PRECEDENCE OUTPUT		
HUB	WIRED TO	PURPOSE
1		
0		

OUTGOING PRECEDENCE OUTPUT		
HUB	WIRED TO	PURPOSE
LC	UNI-11 IN	Encode Prec. ↓
UC	UNI-12 IN	Encode Prec. ↑

INC. PREC. IN	
HUB	WIRED FROM
UC	
UC	
LC	
LC	

OUT. PREC. IN	
HUB	WIRED FROM
IN	BUS 1-34
INH.	UNI-13 OUT

T & F UNIT - DECODER/ENCODER WIRING (Continued)

Date: January 24, 1961

Program: Output Format A

DECODER INPUT		
HUB	WIRED FROM	
1	IN BUFF	1
0	IN BUFF	0-1
2	IN BUFF	2
0	IN BUFF	0-2
4	IN BUFF	4
0	IN BUFF	0-4
8	IN BUFF	8
0	IN BUFF	0-8
16	IN BUFF	16
0	IN BUFF	0-16
32	IN BUFF	32
0	IN BUFF	0-32
64		
0		
PAR	IN BUFF PAR	
0	IN BUFF 0-PAR	

PUNCH/COMP. INPUT	
HUB	WIRED FROM
1a	BUS u-30
1b	CPR-OUT 1
2a	BUS v-30
2b	CPR-OUT 2
4a	BUS u-30
4b	CPR-OUT 4
8a	BUS x-34
8b	CPR-OUT 6
16a	BUS u-20
16b	CPR-OUT 16
32a	BUS l-34
32b	CPR-OUT 32
64a	BUS x-1
64b	CPR-OUT PAR
PAR-a	
PAR-b	

DELETE
WIRED FROM
DEC I-0 (i)
DEC I-13 (i)
DEC I-19 (i)
DEC II-14 (*)
DEC II-0 (t)
BUS a-25
DEC II-15 (i)

DELETE
WIRED FROM
DEC II-29 (%)

T & F UNIT - SELECTOR WIRING

Date: January 24, 1961

Program: Output Format A

HIGH SPEED SEL. 1			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS X-24		HSS 1b S	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	BUS 11-2	POWER	
b	D.O. HSS1	FD g 4	
c			
d			

LOW SPEED SEL. 6			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
PD 103		BUS F10	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a	BUS F28	PD 130	
b	HSS 4c IN	LSS 8a S	
c			
d			

HIGH SPEED SEL. 2			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
DFC II-13 (\$)		BUS F-5	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	UNI-L IN	LSS 9b S	
b	BUS F-25	LSS 9c S	
c	UNI-D IN	PD 79	
d		HSS 3a S	BUS N-25

LOW SPEED SEL. 7			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
AND 3 OUT		BUS F-6	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a	FD e IN	PD 25	UNI F IN
b			
c		PD 30	BUS N29
d		PD 36	F-29

HIGH SPEED SEL. 3			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS X-24		BUS F-7	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a	HSS 2d IN	FD f.3	
b	UNI-1 IN	PD 100	
c		PD 97	BUS N-26
d		PD 99	BUS N-28

LOW SPEED SEL. 8			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
PD 13		BUS F-3	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a	LSS 6b C	PD 1	BUS F26
b		PD 4	UNI-17 IN
c		PD 5	UNI-17 IN
d			

T & F UNIT - SELECTOR WIRING (Continued)

Date: January 24, 1961

Program: Output Format A

HIGH SPEED SEL. 4			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS W-25		BUS F-8	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a		DEC I-1	HSS 5c IN
b		BUS h-20	UNCOND. DEL.
c	UNI K IN	LSS 6b S	BUS F27
d			

LOW SPEED SEL. 9			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
FD f-4		BUS F-10	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a		FD 43	BUS F-30
b	HSS2a IN	FD f1	UNI-L IN
c	HSS2b IN	FD f2	BUS F11
d	BUS X-24	UNI-14 OUT	

HIGH SPEED SEL. 5			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
BUS N-28		UNI-6 Out	
ELEMENT	S HUB TO	IN HUB FROM	NS HUB TO
a		BUS h-20	UNI-1 IN
b			
c	UNI-14 IN	HSS 4a N/S	BUS N-27
d	SET CAK 9	BUS N-25	UNI-5 IN

LOW SPEED SEL. 10			
PURPOSE:			
PU HUB WIRED FROM		DO HUB WIRED FROM	
FD 13		BUS F-26	
ELEMENT	S HUB TO	C HUB FROM	NS HUB TO
a		FD 2	BUS h-13
b		FD 3	BUS a-25
c		FD 5	BUS h-14
d	BUS F-7	FDa 4	

T & F UNIT - FUNCTION DETECTOR WIRING

Date: January 24, 1961

Program: Output Format A

FUNCTION DETECTOR a		
Purpose:		
Input Hub Wired From: BUS h-13		
Out Hub	Wired to	Purpose
1	BUS F-12	ENCODE ↑
2	BUS b-14	ENCODE <
3	BUS d-23	ENCODE ↓
4	LSS 10d C	

FUNCTION DETECTOR e		
Purpose: LSS 7a S		
Input Hub Wired From:		
Out Hub	Wired To	Purpose
1	START FILL a	
2	UNI-8 IN	STEP ON ENCODE
3		
4		

FUNCTION DETECTOR b		
Purpose:		
Input Hub Wired From: BUS a-25		
Out Hub	Wired To	Purpose
1	BUS b-14	ENCODE <
2	BUS b-14	ENCODE <
3	BUS d-23	ENCODE ↓
4	BUS h-28	ENCODE Z

FUNCTION DETECTOR f		
Purpose:		
Input Hub Wired From: UNI-1 OUT		
Out Hub	Wired To	Purpose
1	LSS 9b C	
2	LSS 9c C	
3	HSS 3a IN	
4	PU LSS 9	

FUNCTION DETECTOR c		
Purpose:		
Input Hub Wired From: BUS h-14		
Out Hub	Wired To	Purpose
1	BUS d-23	ENCODE ↓
2	BUS d-24	ENCODE ↓
3	BUS 1-1	ENCODE ≡
4	BUS d-24	ENCODE ↓

FUNCTION DETECTOR g		
Purpose:		
Input Hub Wired From: BUS h-20		
Out Hub	Wired To	Purpose
1		
2		
3		
4	HSS 1b IN	


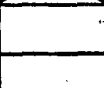

FUNCTION DETECTOR d		
Purpose:		
Input Hub Wired From: DEC II-15 (?)		
Out Hub	Wired To	Purpose
1	UNI-9 IN	ENCODE <
2	UNI-11 IN	ENCODE ↓
3	ACTIVATE	
4	BUS 1-1	ENCODE ≡

FUNCTION DETECTOR h		
Purpose:		
Input Hub Wired From: UNI-15 OUT		
Out Hub	Wired To	Purpose
1	UNI-E IN	ENCODE ≡
2	UNI-3 IN	ENCODE E
3	ENC II-5	ENCODE BELL
4	I/O COM 3W	HSCL-W

T & F UNIT - UNIBUS, "AND", WIRING

Date: January 24, 1961

Program: Output Format A

UNIBUSES							
UNIBUS	INPUT HUBS WIRED FROM					OUTPUT HUB WIRED TO	PURPOSE
1	HSS 5a N/S	HSS 3b S	BUS F-30	BUS W-25		FD f IN	
2				BUS F7		BUS F6	
3	(E) DEC I-24	FD h 2				ENC I-1	ENCODE E
4	BUS h-28		AND 7 OUT			ENC I-17	ENCODE Z
5	BUS N-26		HSS 5d N/S	BUS F-25		CAK 11	
6	AND 6 OUT	BUS F-6	BUS W25	PD 101		D.O. HSS 5	
7							
8	AND 5 OUT	PD 1	BUS F-30	FD e 2	BUS F28	BUS h-7	STEP ON ENCODE
9	BUS b-14	FD d 1				ENC I-8	ENCODE <
10							
11	OUTPUT PREC.-LC	FD d 2	BUS b-25			ENC I-31	ENCODE ↓
12	OUTPUT PREC.-UC	BUS F-12				ENC II-27	ENCODE ↑
13	BUS e-28	BUS F-12				OUTPUT PREC.-INH	
14	2FDa OUT	HSS 5c S				LSS 9a C	
15	Parity Odd	(#) DEC I-30	(,) DEC I-17	(/) DEC II-20	(+) DEC II-19	FD h IN	ERRORS
16							
17	BUS 1-1	BUS F26	BUS h-13	LSS8b N/S (PD 4)	LSS8c N/S (PD 5)	UNI-18 IN	
18	BUS h-28	BUS b-25	BUS b-14	BUS F-12	UNI-17 OUT	INV 5 IN	
19	DEC I-1(Δ)	FILL a OUT				BUS e-28	
20							

Date: January 24, 1961

T & F UNIT - UNIBUS, "AND", WIRING (Continued)

Program: Output Format A

"AND"				
"AND"	INPUT HUBS WIRED FROM		OUTPUT HUB WIRED TO	PURPOSE
1				
2	INV 5 OUT	CPR-OUT	CPR-STEP	
3	PD 11	DEC II26	PU LSS 7	
4				
5	INV 3 OUT	BUS u-2	UNI-8 IN	
6	INV 4 OUT	PD 100	UNI-6 IN	D.O. HSS 5 IF NO Δ AT W5C5
7	INV 2 OUT	DEC II-28 (2)	UNI-4 IN	
8				


UNIBUS					
	IN	IN	IN	OUT	PURPOSE
L	HSS 2a S	LSS 9b N/S		F I #7	Track Switch
K	BUS F11	BUS F-25	HSS 4c S	BUS F-24	SET PDW 1 & 2
J	BUS N-25	BUS N-26		WAK 9	
I					
H	BUS L-29	PD 19		CAK 5	
G	BUS b-19	BUS F-29		WAK 4	
F	BUS N-28	LSS 7a N/S		BUS b-19	SET WAK-5 CAK-5
E	BUS 1-1	PD h-1		ENC I-2	ENCODE ≡
D	HSS 2c S	PD 41		BUS N-24	

T & F BUSSES

Date: January 24, 1961

Program: Output Format A

BUS	HUB	WIRED TO	PURPOSE
x-1	x-1	ENC Out-PAR	
	v-1	P/C 64	
	v-1	INV 1 IN	
	u-1		
u-2	u-2	HSS1a S	
	u-3	BUS h-20	
	u-4		
	u-5	AND 5 IN	
	u-6	INV 4 IN	
w-20	w-20	ENC Out 16	
	w-21	P/C 16	
	v-18	CPR IN 16	
	v-19		
	v-20		
	v-21		
u-30	u-30	ENC Out 4	
	u-31	P/C 4	
	u-32	CPR IN 4	
	u-33		
v-30	v-30	ENC Out 2	
	v-31	P/C 2	
	v-32	CPR IN 2	
	v-33		
w-30	w-30	ENC Out 1	
	w-31	P/C 1	
	w-32	CPR IN 1	

BUS	HUB	WIRED TO	PURPOSE
w-30	w-33		
x-34	x-34	ENC Out 8	
	w-34	P/C 8	
	v-34	CPR IN 8	
	w-34		
1-1	1-1	FDc 3	
	k-1	UNI-E IN	ENCODE 
	j-1	UNI-17 IN	
	i-1	FD d 4	
1-34	1-34	ENC Out 32	
	k-34	P/C 32	
	j-34	CPR IN 32	
	i-34	OUTPUT PREC. IN	
h-7	h-7	UNI-8 Out	
	g-7	INV 7 IN PD STEP	
	f-7	IN-3	
	e-7		
	d-7		
h-13	h-13	FD a IN	
	g-13	ISS 10a N/S	
	f-13	DEC II-29	
	e-13	UNI-17 IN	
h-14	h-14	FD c IN	
	g-14	CAK 6	
	f-14	WAK 8	
	e-14	ISS 10c N/S	

T & F BUSSES (Continued)

Date: January 24, 1961

Program: Output Format A

BUS	IUB	WIRED TO	PURPOSE
h-20	h-20	HSS 5a IN	
	f-20	HSS 4b IN	
	f-20	FD f IN	
	e-20	BUS u-2	
h-21	h-21		
	f-21		
	f-21		
	e-21		
h-28	h-28	UNI-4 IN	
	f-28	FD b 4	
	f-28	UNI-18 IN	
e-28	e-28	UNI-13 IN	
	d-28	ENC I-4	
	c-28	UNI-19 OUT	
b-14	b-14	FD a 2	
	b-15	FD b 1	
	b-16	FD b 2	
	b-17	UNI-9 IN	
	b-18	UNI-18 IN	
b-19	b-19	UNI-F OUT	
	b-20	CAK 4	
	b-21	UNI-G IN	
	b-22		
a-20	a-20		
	a-21		
	a-22		

[illegible]

T & F BUSES

Date: January 24, 1961

Program: Output Format A

BUS	HUB	WIRED TO	PURPOSE
X-21	X-21		
	X-22		
	X-23		
X-24	X-24	PU HSS 1	
	X-25	P.U. HSS 3	
	X-26	LSS 9d S	
W-25	W-25	P.U. HSS 4	
	W-26	DEC II-13	
	W-27	UNI-1 IN	
	W-28	ENC II-14	
	W-29	UNI 6 IN	
X-27	X-27		
	X-28		
	X-29		
N-24	N-24	CAK 2	
	M-24	WAK 0	
	L-24	UNI-D OUT	
N-25	N-25	UNI J IN	
	M-25	HSS 5d IN	
	L-25	HSS 2d NS	
N-26	N-26	HSS 3c N/S	
	M-26	UNI-5 IN	
	L-26	UNI-J IN	
N-27	N-27	HSS 5c N/S	
	M-27	2FD a 1	
	L-27	2ED a 2	

BUS	HUB	WIRED TO	PURPOSE
N-28	N-28	UNI-F IN	
	M-28	HSS 3d N/S	
	L-28	PU HSS 5	
N-29	N-29	LSS 7c NS	
	M-29	WAK 5	
	L-29	UNI H IN	
F-5	F-5	BUS F6	
	E-5	FI 6	
	D-5	D.O. HSS 2	
	C-5		
F-6	F-6	BUS F5	
	E-6	DO LSS 7	
	D-6	UNI-6 IN	
	C-6	UNI 2 OUT	
F-7	F-7	UNI 2 IN	
	E-7	LSS 10d S	
	D-7	D.O. HSS 3	
	C-7	BUS F-8	
F-8	F-8	BUS F-9	
	E-8	D.O. HSS 4	
	D-8	D.O. LSS 8	
	C-8	BUS F-7	
F-9	F-9	BUS F-8	
	E-9	DISCONNECT	
	D-9		
	C-9		

T & F BUSSES (Continued)

Date: January 24, 1961

Program: Output Format A

BUS	HUB	WIRED TO	PURPOSE
F-10	F-10	D.O. LSS 9	
	E-10	BUS F26	
	D-10	D.O. LSS 6	
	C-10		
F-11	F-11	LSS 9c NS	
	E-11	UNI K IN	
	D-11	SET PD c1	
	C-11		
F-12	F-12	UNI-12 IN	
	E-12	UNI-18 IN	
	D-12	FD a 1	
	C-12	UNI-13 IN	
F-23	F-23		
	E-23	BUS F-24	
	D-23	SET PDC 2	
	C-23	SET PDC 4	
F-24	F-24	BUS F-23	
	E-24	SET PDW 2	
	D-24	SET PDW 1	
	C-24	UNI-K OUT	
F-25	F-25	HSS 2b S	
	E-25	UNI-5 IN	
	D-25	WAK 1	
	C-25	UNI-K IN	

BUS	HUB	WIRED TO	PURPOSE
F-26	F-26	LSS 8a NS	
	E-26	UNI 17 IN	
	D-26	LSS 10 DO	
	C-26	BUS F10	
F-27	F-27	SET PD c8	
	E-27	HSS 4c S	
	D-27	BUS F28	
	C-27		
F-28	F-28	LSS 6a S	
	E-28	UNI 8 IN	
	D-28	SET PD W8	
	C-28	BUS F27	
F-29	F-29		
	E-29	LSS 7d NS	
	D-29	SET CAK 10	
	C-29	UNI G IN	
F-30	F-30		
	F-30	UNI-8 IN	
	D-30	UNI-1 IN	
	C-30	LSS 9 a N/S	

DATE: JANUARY 24, 1961

T & F UNIT - PROGRAM DIST. & ADDRESS WIRING

PROGRAM: OUTPUT FORMAT A

PROGRAM DISTRIBUTOR OUT		
HUB	WIRED TO	PURPOSE
1	LSS 8a C UNI-8 IN	
2	LSS 10a C	
3	LSS 10b C	
4	LSS 8b C	
5	LSS 8c C LSS 10c C	
11	AND 3 IN	
12	CAK 7 WAK 7	
13	PU LSS 8 PU LSS 10	
19	UNI-H IN WAK 6	
25	LSS 7a C	
30	LSS 7c C	
36	LSS 7d C	
40	END FILL	
41	UNI-D IN	
43	PD 44 LSS 9a C	
44	PD 43	
79	HSS 2c IN	
97	HSS 3c IN	
99	INV 3 IN HSS 3d IN	
100	AND 6 IN HSS 3b IN	
101	UNI-6 IN	
103	PU LSS 6	
130	LSS 6a C	

MOD 12 PDK OUT		
HUB	WIRED TO	PURPOSE

STEP PD IN	
HUB	WIRED FROM
1. STEP - DECODE	INV 7 OUT
2. VOID STEP-DECODE	
3. STEP - ENCODE	BUS h-7
4. VOID STEP-ENCODE	

T & F UNIT - PROGRAM DIST. & ADDRESS WIRING (Continued)

Date: January 24, 1961

Program: Output Format A

SET IN CHAR. ADD	
HUB	WIRED FROM
11	UNI-5 OUT
10	BUS F29
9	HSS 5d s
8	
7	PD 12
6	BUS h-14
5	UNI H OUT
4	BUS b19
3	
2	BUS N-24
1	
8	

SET IN WORD ADD	
HUB	WIRED FROM
0	BUS N-24
1	BUS F25
2	
3	
4	UNI - G OUT
5	BUS N-29
6	PD 19
7	PD 12
8	BUS f14
9	UNI - J OUT

SET PD CHAR. IN	
HUB	WIRED FROM
8	BUS F27
4	BUS F-23
2	BUS F-23
1	BUS F11

SET PD WORD IN	
HUB	WIRED FROM
8	BUS F28
4	
2	BUS F-24
1	BUS F-24

T & F UNIT - FUNCTION WIRING

Date: January 24, 1961

Program: Output Format A

FILL					
FILL	INHIBIT HUB WIRED FROM	START HUBS WIRED FROM	END HUBS WIRED FROM	OUT HUB WIRED TO	PURPOSE
a		FD 1	PD 40	UNI-19 IN	PAD SPEED FIX TIME, AND ALT, IN X MSG.
b					
c					
d					

FUNCTION INPUTS		
FUNCTION	INPUT HUB WIRED FROM	INHIBIT HUB WIRED FROM
1. OUTPUT STOP		
2. OUTPUT START		
3. INPUT STOP		
4. CORRECTION		
5. VOID		
6. END OF DATA	BUS F- 5	
7. TRACK SWITCH	UNI-L OUT	
8. BUFFER CLEAR		
9. WORD CLEAR		
10. STOP		
11. STOP		

STOP OUTPUT	
WIRED TO	PURPOSE

DECODER OUT	
WIRED TO	PURPOSE

DECODER IN HUB WIRED FROM:

2-CHAR. FUNC. DET. a		
PURPOSE:		
IN 1 HUB WIRED FROM	IN 2 HUB WIRED FROM	OUT HUB WIRED TO
BUS N-27	BUS N-27	UNI-14 IN

INVERTERS			
INV.	IN HUB WIRED FROM	OUT HUB WIRED TO	PURPOSE
1	BUS x-1	CPR IN-PAR	INVERT PARITY OF ENC. OUT
2	BUS a-25	AND 7 IN	
3	PD- 99	AND 5 IN	
4	BUS u-2	AND 6 IN	
5	UNI-18 OUT	AND 2 IN	
6			
7	BUS h-7	PD STEP IN 1	

2-CHAR. FUNC. DET. b		
PURPOSE:		
IN 1 HUB WIRED FROM	IN 2 HUB WIRED FROM	OUT HUB WIRED TO

T & F UNIT - MISCELLANEOUS CONTROL WIRING

Date: January 24, 1961

Program: Output Format A

CHANNEL PAR. REG. INPUT	
HUB	WIRED FROM
1	BUS w-30
2	BUS v-30
4	BUS u-30
8	BUS x-34
16	BUS w-20
32	BUS 1-34
64	
P	INV-1 OUT

CHANNEL PAR. REG. OUTPUT	
HUB	WIRED FROM
1	P/C 1
2	P/C 2
4	P/C 4
8	P/C 8
16	P/C 16
32	P/C 32
64	
P	P/C 64

CHANNEL PARITY CONTROL		
HUB	WIRED TO/FROM	PURPOSE
ACT.	FD d 3	
ERROR		
NC		

SELECT CHARACTER PARITY		
HUB	WIRED TO/FROM	PURPOSE
ODD	UNI-15 IN	
C		
EVEN		

ADVANCE CHANNEL PAR.	
HUB	WIRED TO/FROM
IN	
STEP	AND 2-OUT
OUT	AND 2-IN

SELECT CHANNEL PARITY	
HUB	WIRED TO/FROM
ODD	CHANNEL PARITY S
S	CHANNEL PARITY ODD
EVEN	

COMPUTER I/O REGISTER		
HUB	WIRED TO	PURPOSE
A-1		
A-0		
B-1		
B-0		
C-1		
C-0		
D-1		
D-0		

MODE SELECTION	
HUB	WIRED FROM
IN-1	
IN-2	
IN-3	
OUT-1	POWER
OUT-2	POWER
OUT-3	POWER

I/O COMPUTER	
HUB	WIRED FROM
a	
b	
c	
d	

T & F UNIT - MISCELLANEOUS CONTROL WIRING (Continued)

Date: January 24, 1961

Program: Output Format A

PUNCH INT'LX. PURPOSE		
NS HUB	G HUB	S HUB
WIRED TO	WIRED TO	WIRED TO

MISCELLANEOUS HUBS	
HUB	WIRED FROM
FILL	
DEL.	
REVERSE	
HUBS	

HIGH SPEED I/O COMP	
HUB	WIRED FROM
V	
X	
Y	
Z	

ALTERNATE SWITCHES				
SWITCH	PURPOSE	N/S HUB	C HUB	S HUB
		WIRED TO	WIRED FROM	WIRED TO
a				
b				
c				
d				
e				
f				

PROGRAM INDICATORS	
IND.	WIRED FROM
1	
2	
3	
4	
5	
6	

**T & F UNIT
SPECIAL FAA HUBS**

Date: January 24, 1961

Program: Output Format A

TIME OUT		
HUB	WIRED TO	PURPOSE
ST		
END		
0		
1		
CL		

STM	
WIRED TO	PURPOSE

COMM. READY		
HUB	WIRED TO	PURPOSE
A SET		
B SET		
ST RDX		

I/O COMM.			
	HUB	WIRED FROM	PURPOSE
2	W		
	X		
	Y		
	Z		
3	W	BUS F-9 FD h 14	
	X		
	Y		
	Z		